

BOMIFY-PDN: CHANGE THE FUSE RATING IN PDN.
AFTER SOME CURRENT MEASUREMENT IN PROTO UNITS.

CHECK-PWR: USAGE OF CMP+/-

I2C ADDR :
7-BIT : 0X
8-BIT : 0X

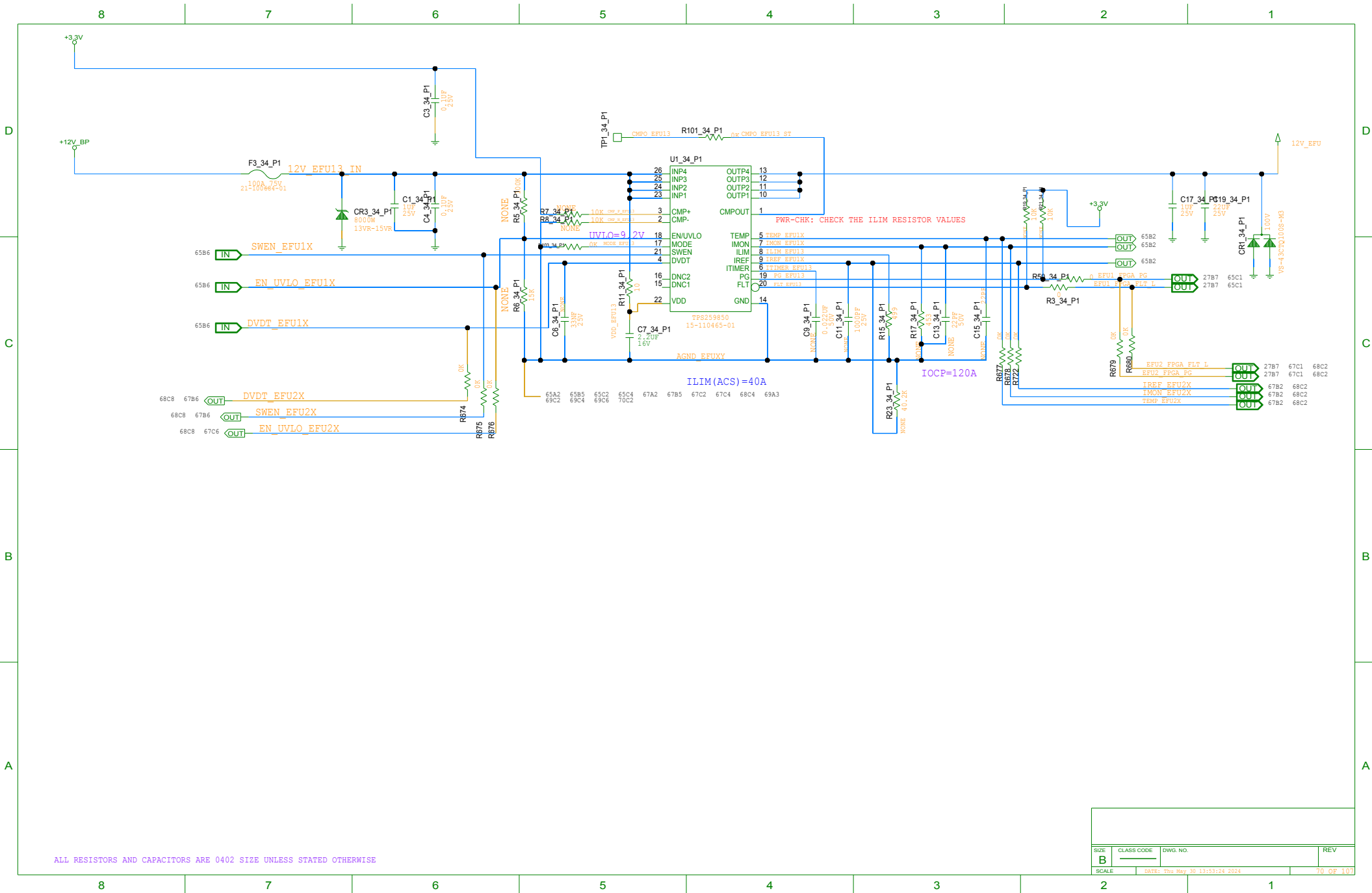
NOTE:BLEEDER RESISTOR

AGND_EFU1X
I_{LM}(STU)=24A
I_{LM}(ACS)=40A

IOCP=120A

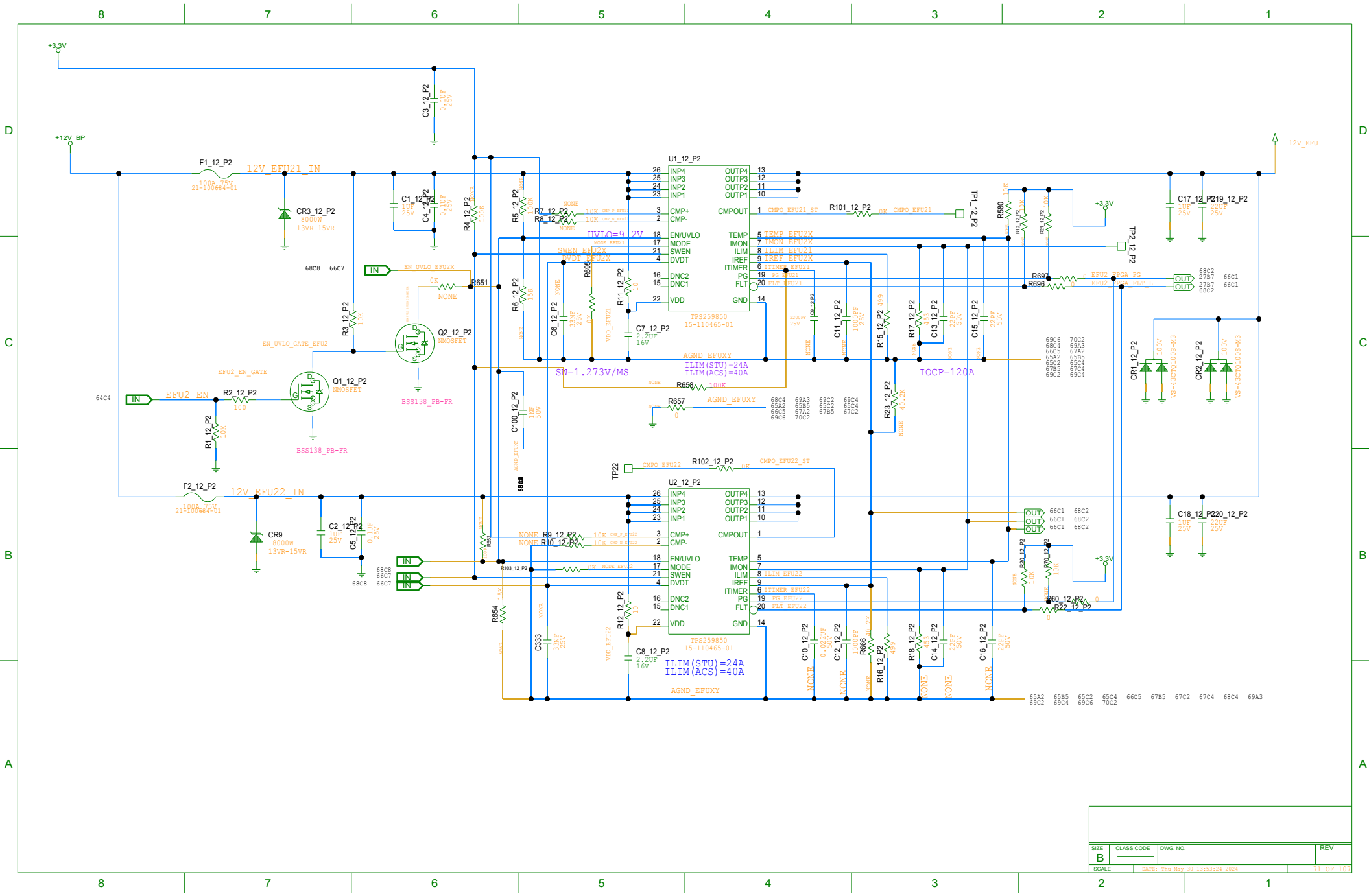
AGND_EFU2X
I_{LM}(STU)=24A
I_{LM}(ACS)=40A

SIZE	CLASS CODE	DWG. NO.	REV
B			
SCALE	DATE: Thu May 30 13:53:23 2024		69-OP 107

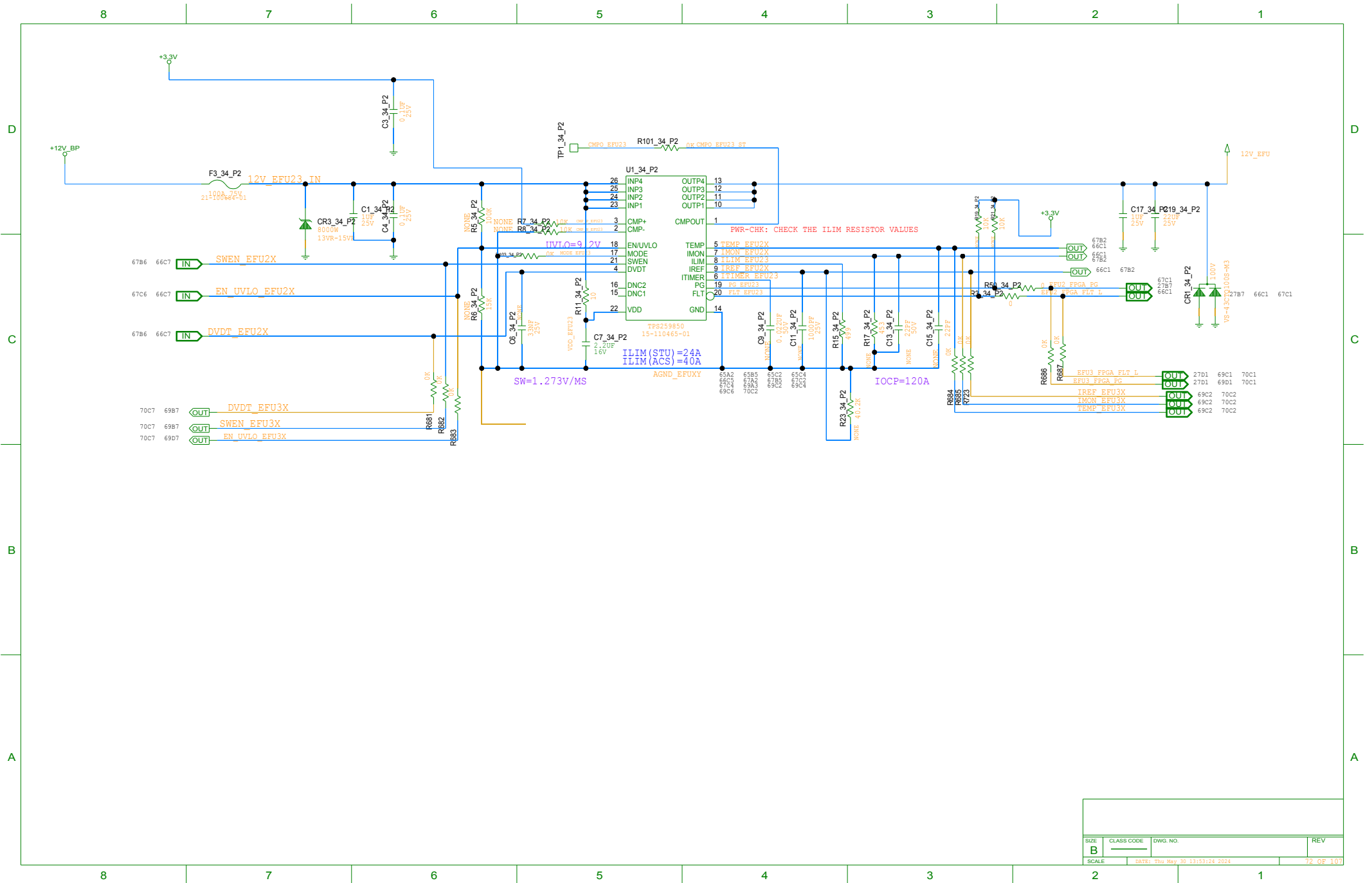


ALL RESISTORS AND CAPACITORS ARE 0402 SIZE UNLESS STATED OTHERWISE

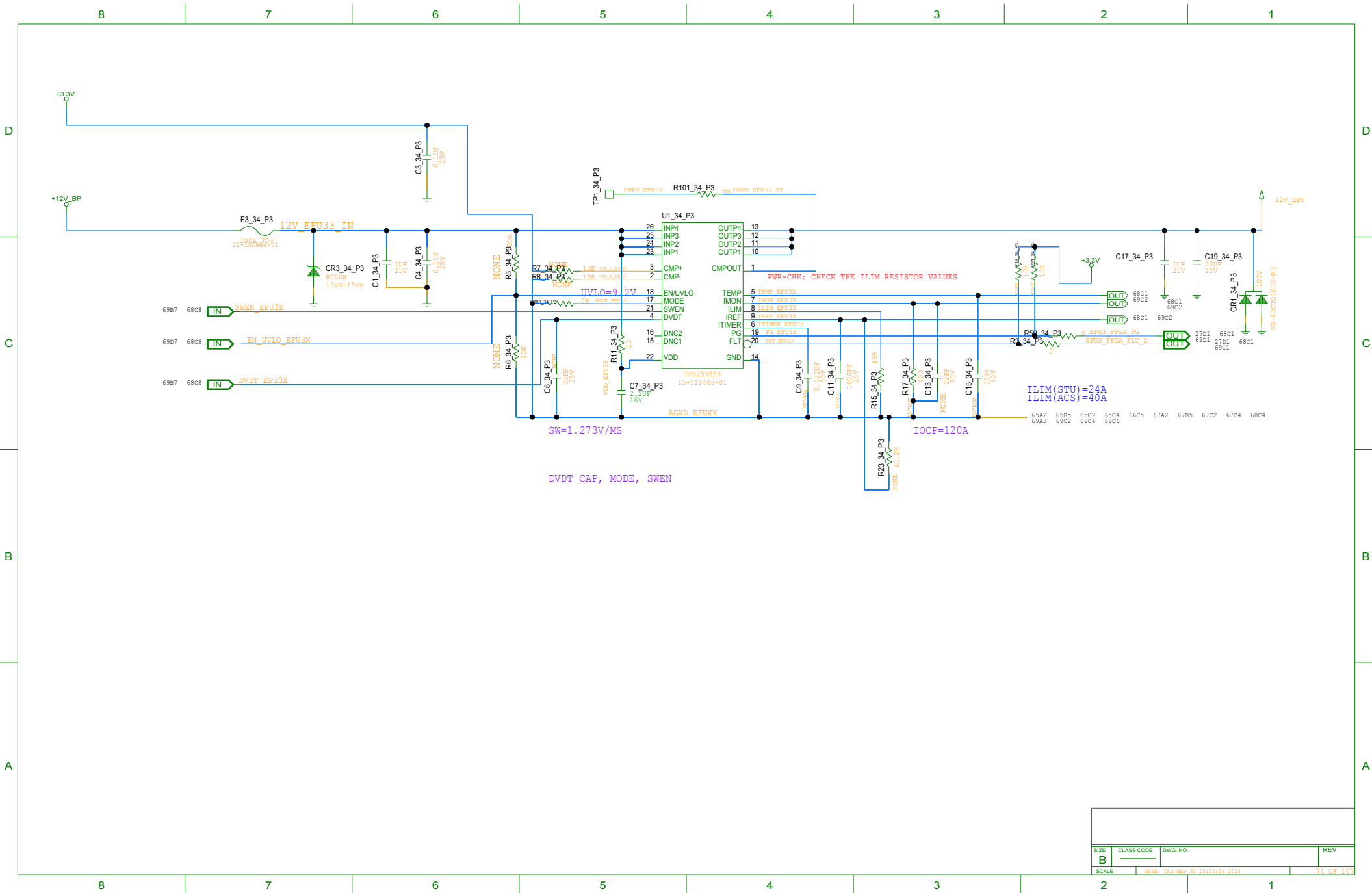
SIZE	CLASS CODE	DWG. NO.	REV
B	BFU2	FPGA PG	
SCALE	DATE: Thu May 30 13:53:24 2024		70_QP 107



SIZE	CLASS CODE	DWG. NO.	REV
B			
SCALE	DATE: Thu May 30 13:53:24 2024		71_OP_107



SIZE	CLASS CODE	DWG. NO.	REV
B			
SCALE	DATE: Thu May 30 13:53:24 2024		72_QP_107



SIZE	CLASS CODE	DWG. NO.	REV
B			
SCALE	DATE: Thu May 30 13:53:24 2024		14_OP_107