

(1) Can you confirm that the sink board supports PD?

Yes the sink board is the **USB-C-PD-DUO-EVM**

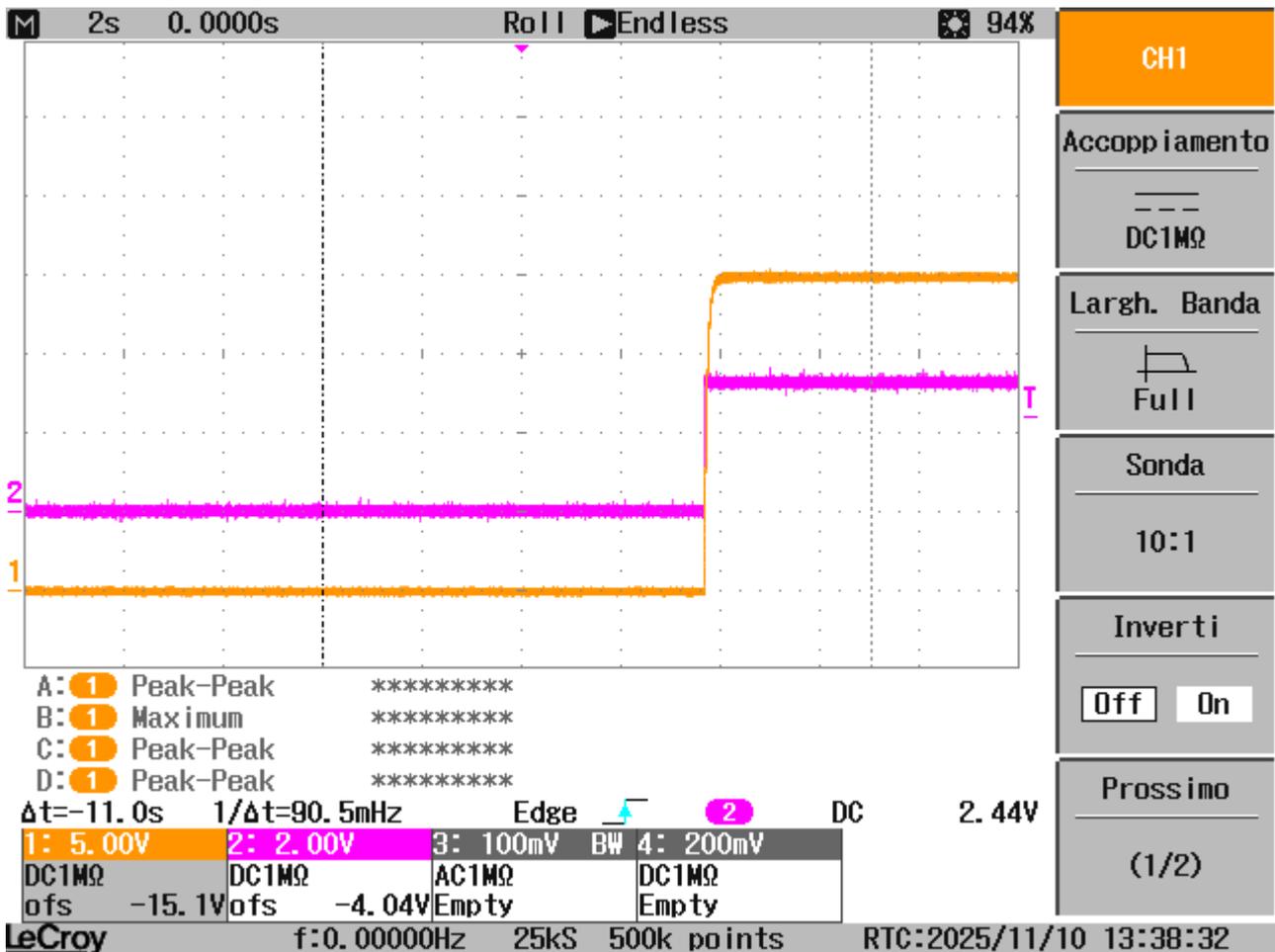
<https://www.ti.com/lit/ug/slvubj5/slvubj5.pdf>

(2) Is the Sink board DRP or sink only?

The USB-C-PD-DUO-EVM is only sink.



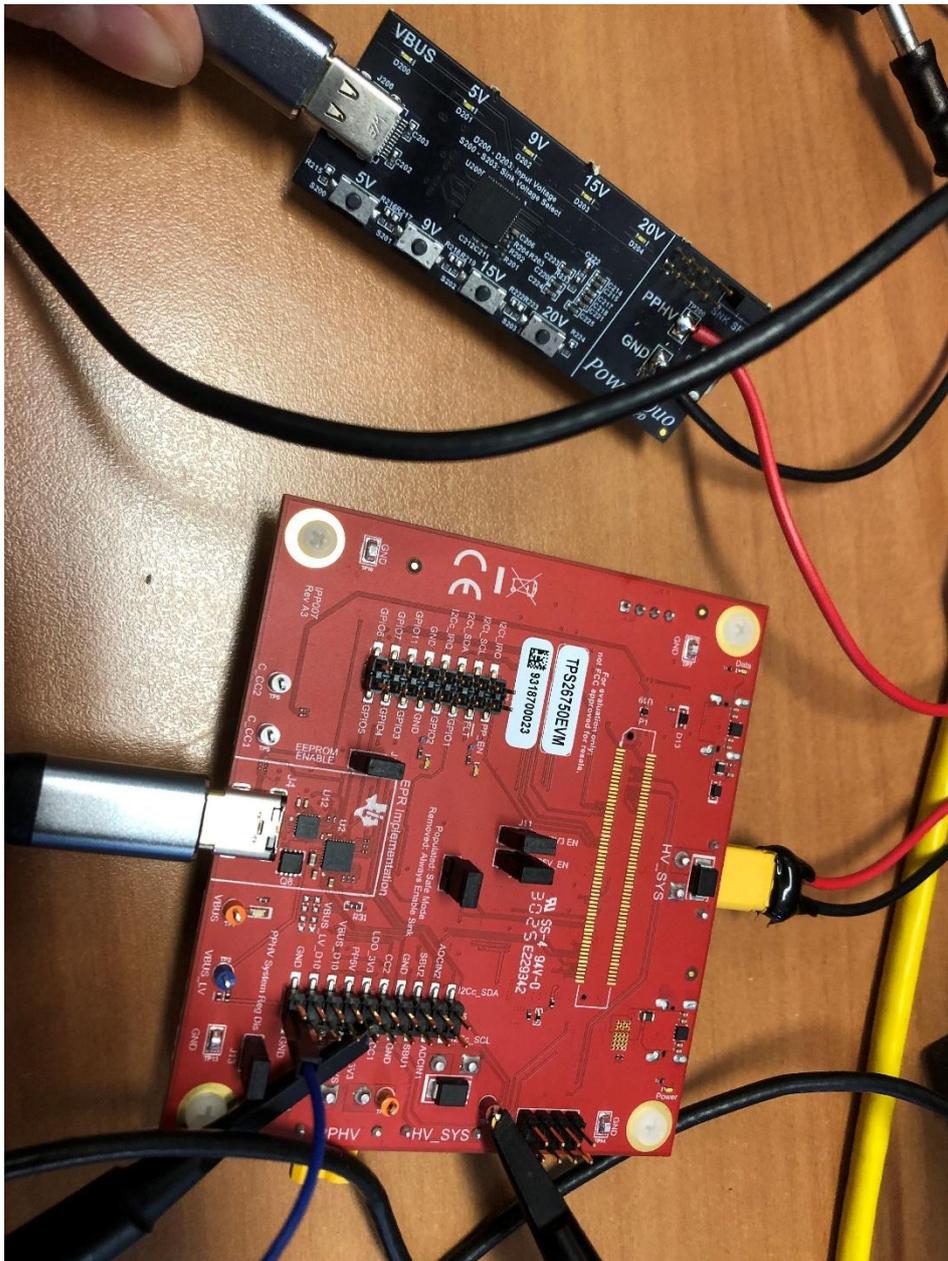
(3) What is the state of VIN\_3V3 when the type-C adapter is plugged in? (ie is the device in a dead battery state)?



The orange trace is the HV\_SYS and the purple trace is the VIN\_3V3 on pin B07 of J5.

(4) Do you have PP5V powered? (PDO1 is set to PP1 which is the PP5V and not PPHV).

PP5V is powered by U9 which is generated by J8 HV\_SYS power. (jumper J11,J12, J13, J14, J15 are populated).



(5) Does the system attempt to go to higher PD voltage 9,15, 20? How do you coordinate the voltage on PPHV to match what is negotiated in a timely manner?

the system tries to raise the voltage, in fact I see the vbus changing, it goes from 5v to almost 0v and then goes back to 5v.

On PPHV i see always ~0v, even if i send through the sink board a request of higher voltage.