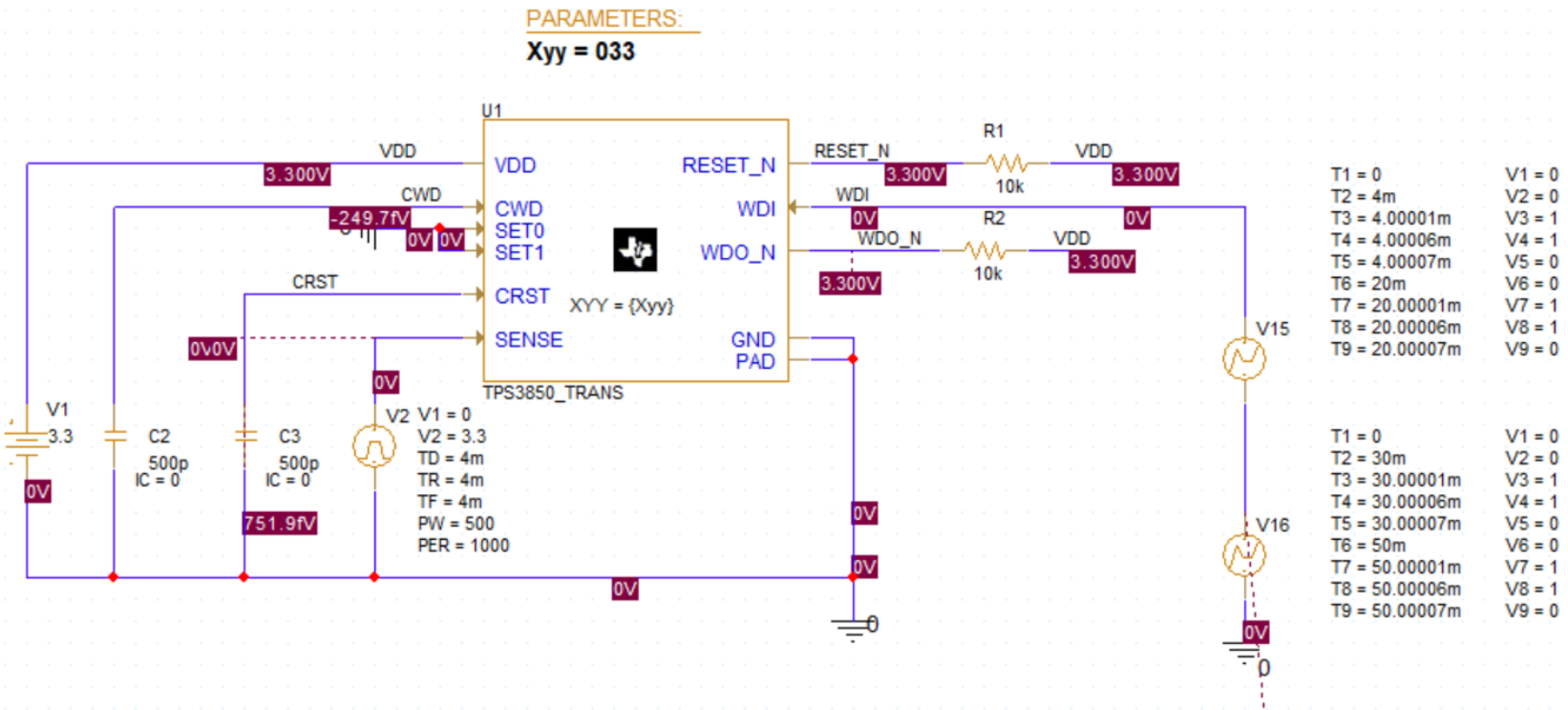
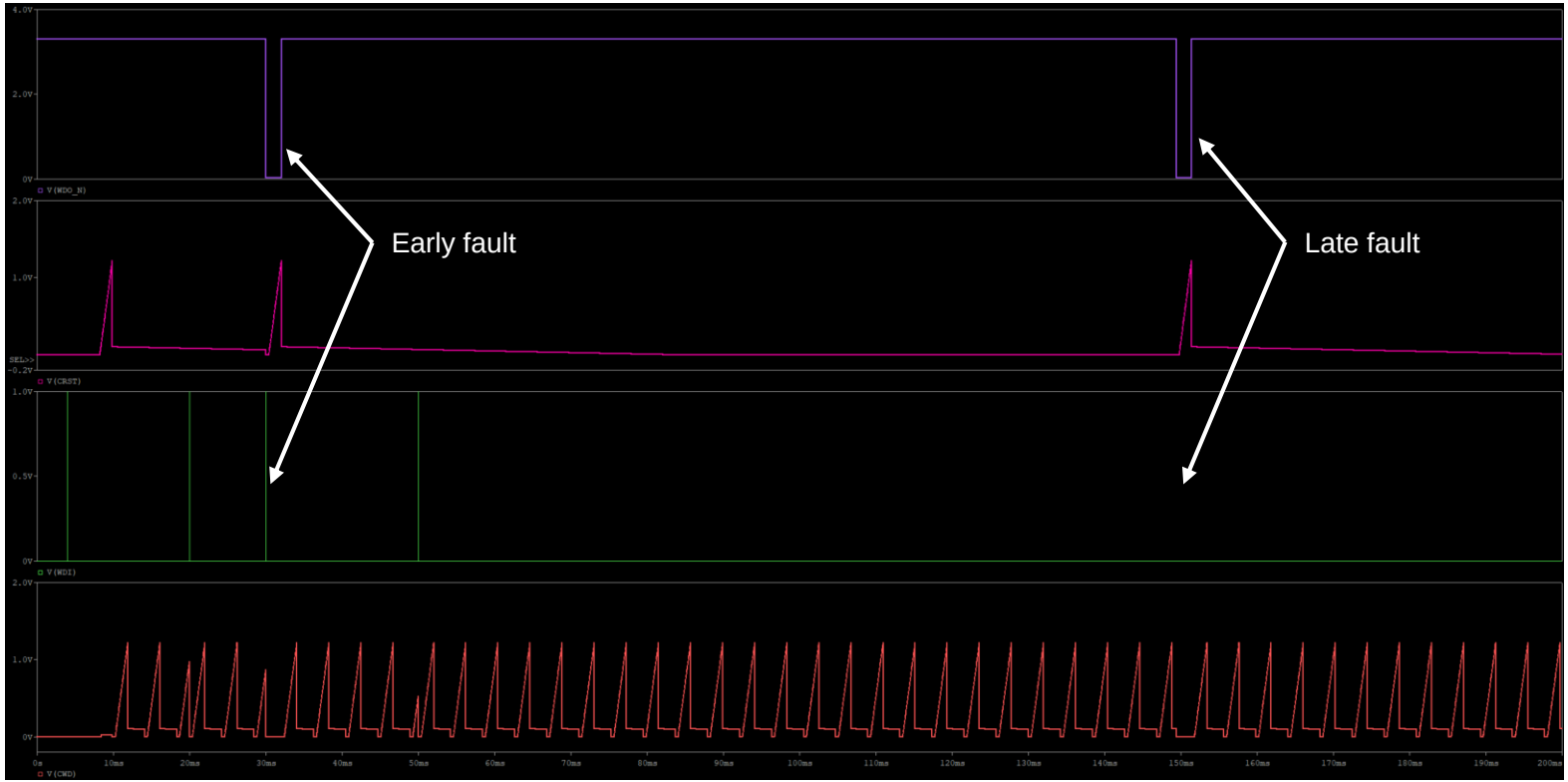
TPS3850 WDO Latch Simulation

2025/03/25

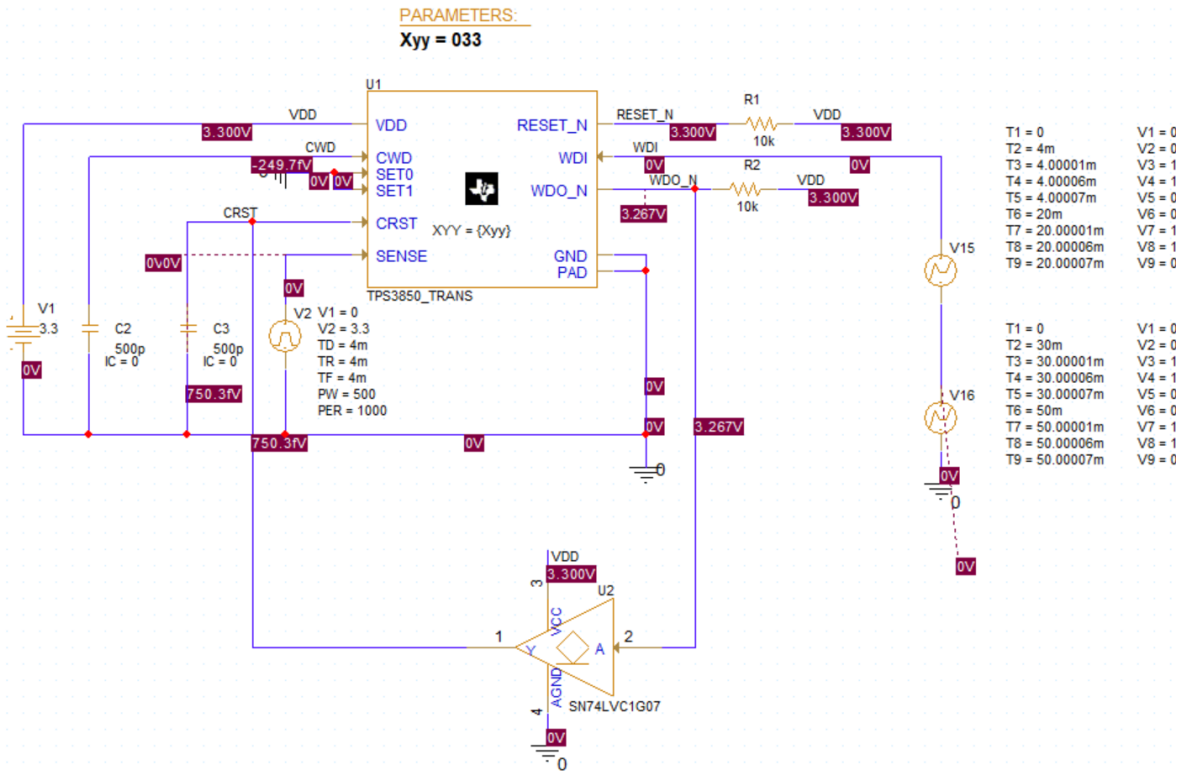
TPS3850 Normal Operation



TPS3850 Normal Operation



TPS3850 + Open Drain Buffer (SN74LVC1G07)



TPS3850

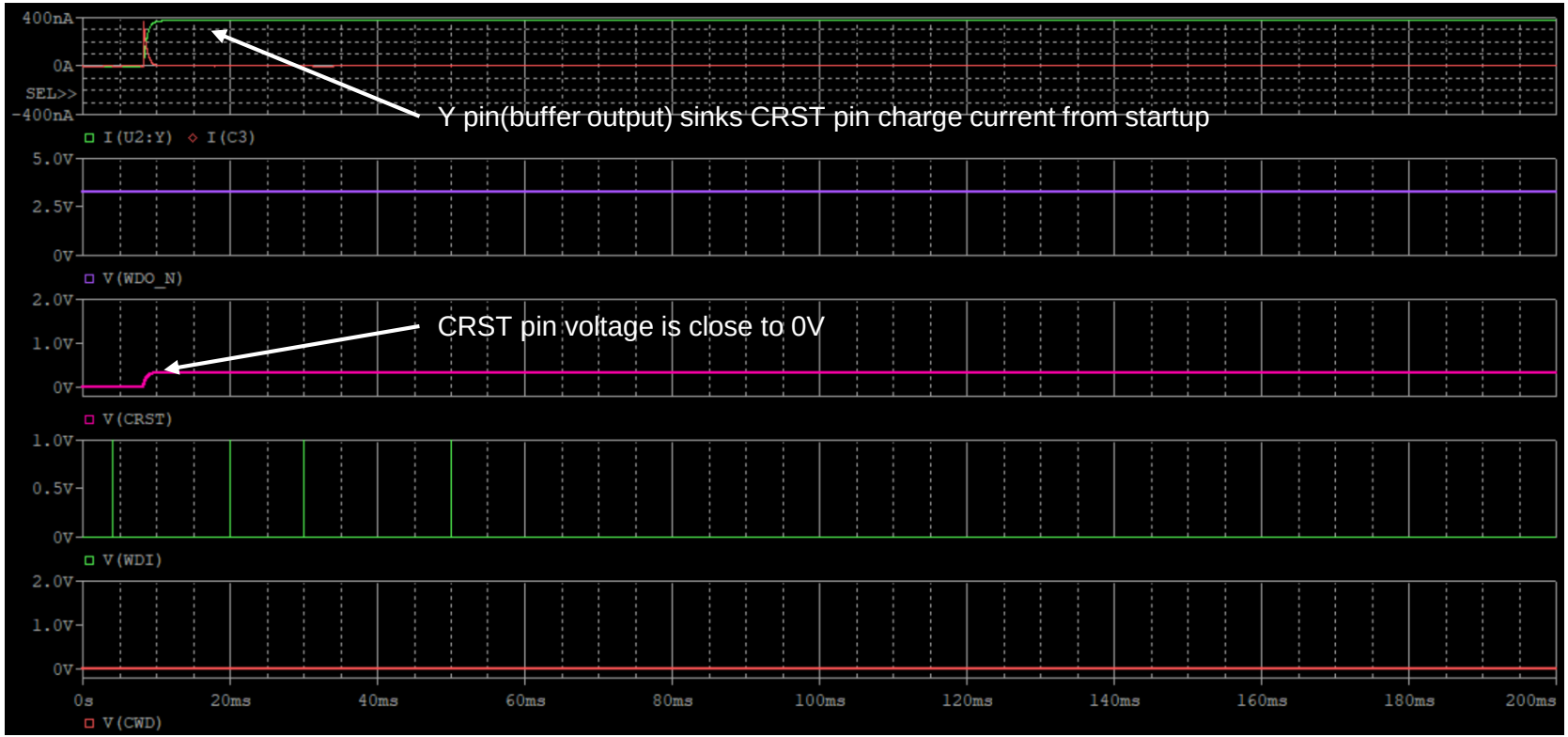
I_{CRST}	CRST pin charge current	CRST = 0.5 V	347	375	403	nA

SN74LVC1G07: Open drain buffer

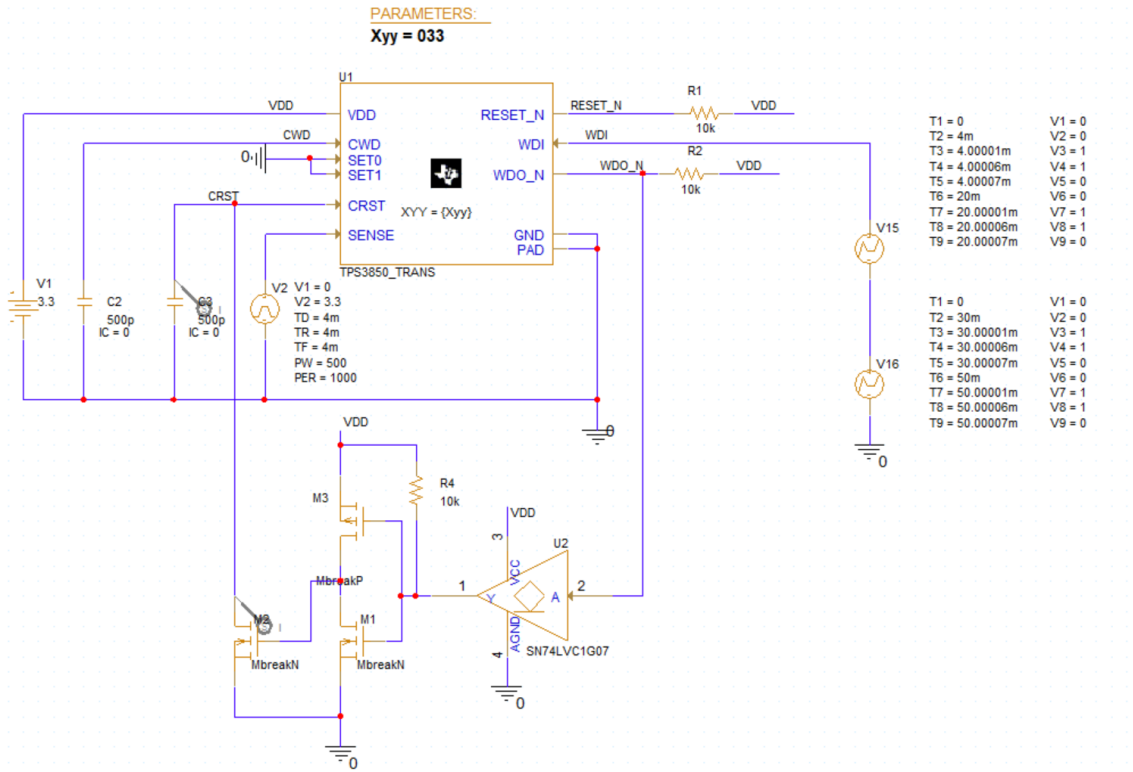
I_{off}	V_I or V_O = 5.5 V	0	±10	±10	µA

Open drain buffer might sink CRST pin charge current
=> Prevent correct operation

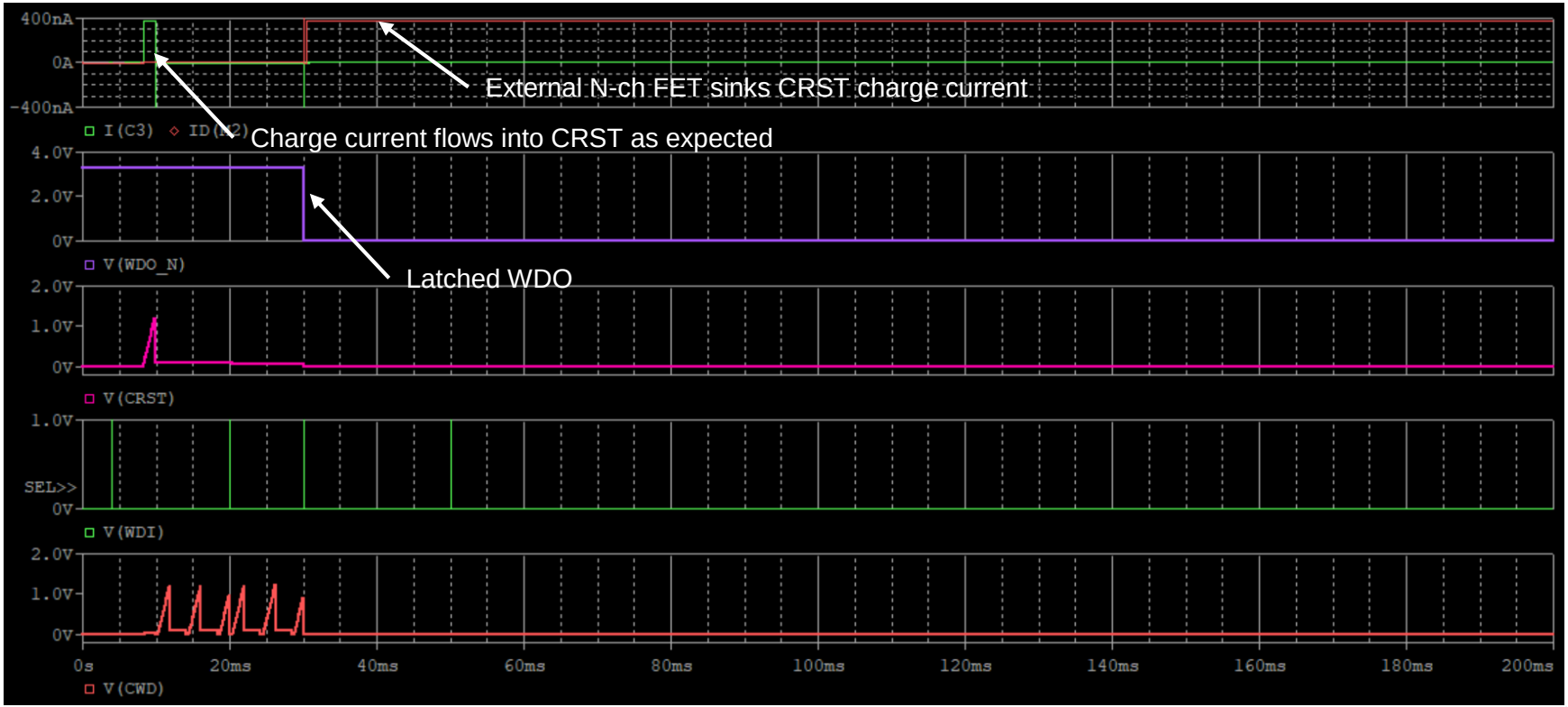
TPS3850 + Open Drain Buffer (SN74LVC1G07)



TPS3850 + SN74LVC1G07 + Nch FET

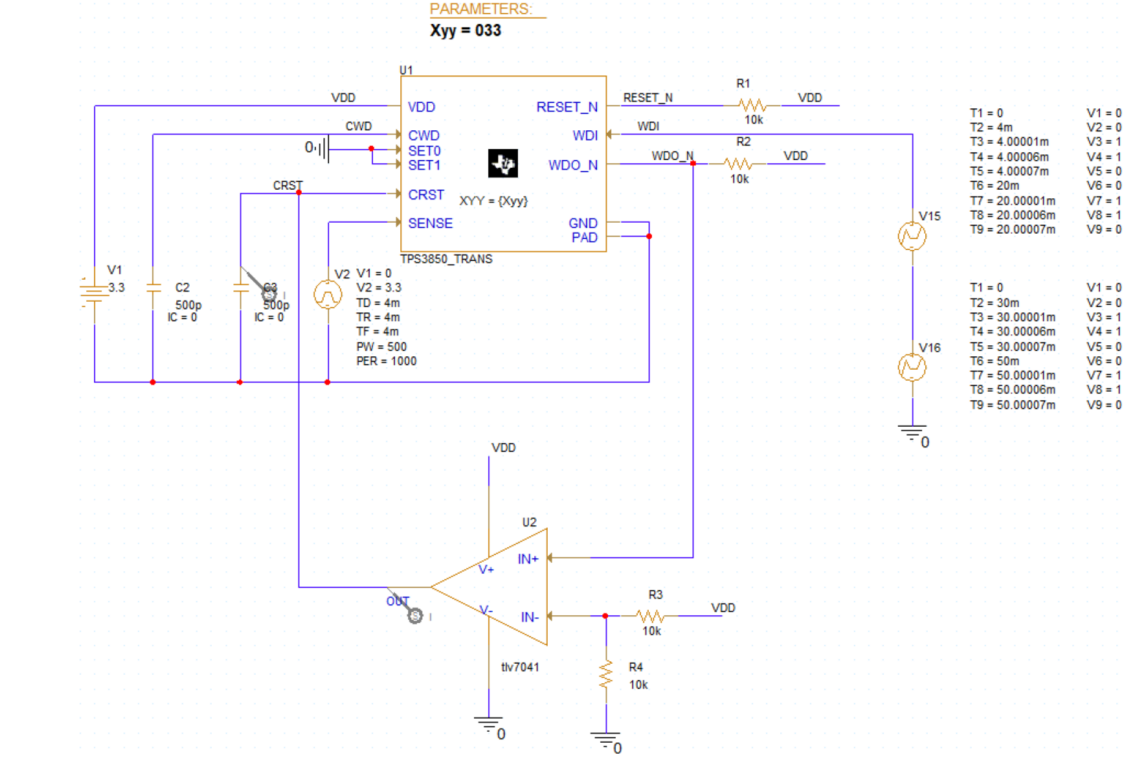


TPS3850 + SN74LVC1G07 + Nch FET



TPS3850 + TLV7041(low leakage comparator)

I_{LKG}	Open-drain output leakage current (TLV7041 only)	V_S = 5V, V_{ID} = +0.1V (output high), V_{PULLUP} = V_{CC}	100	pA



TPS3850 + TLV7041(low leakage comparator)