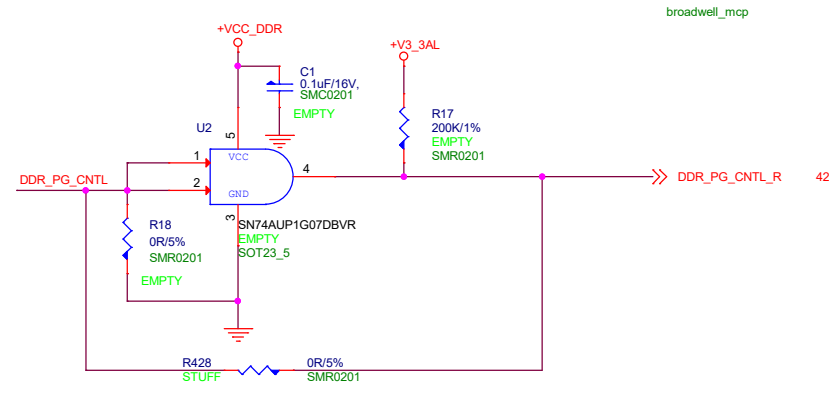
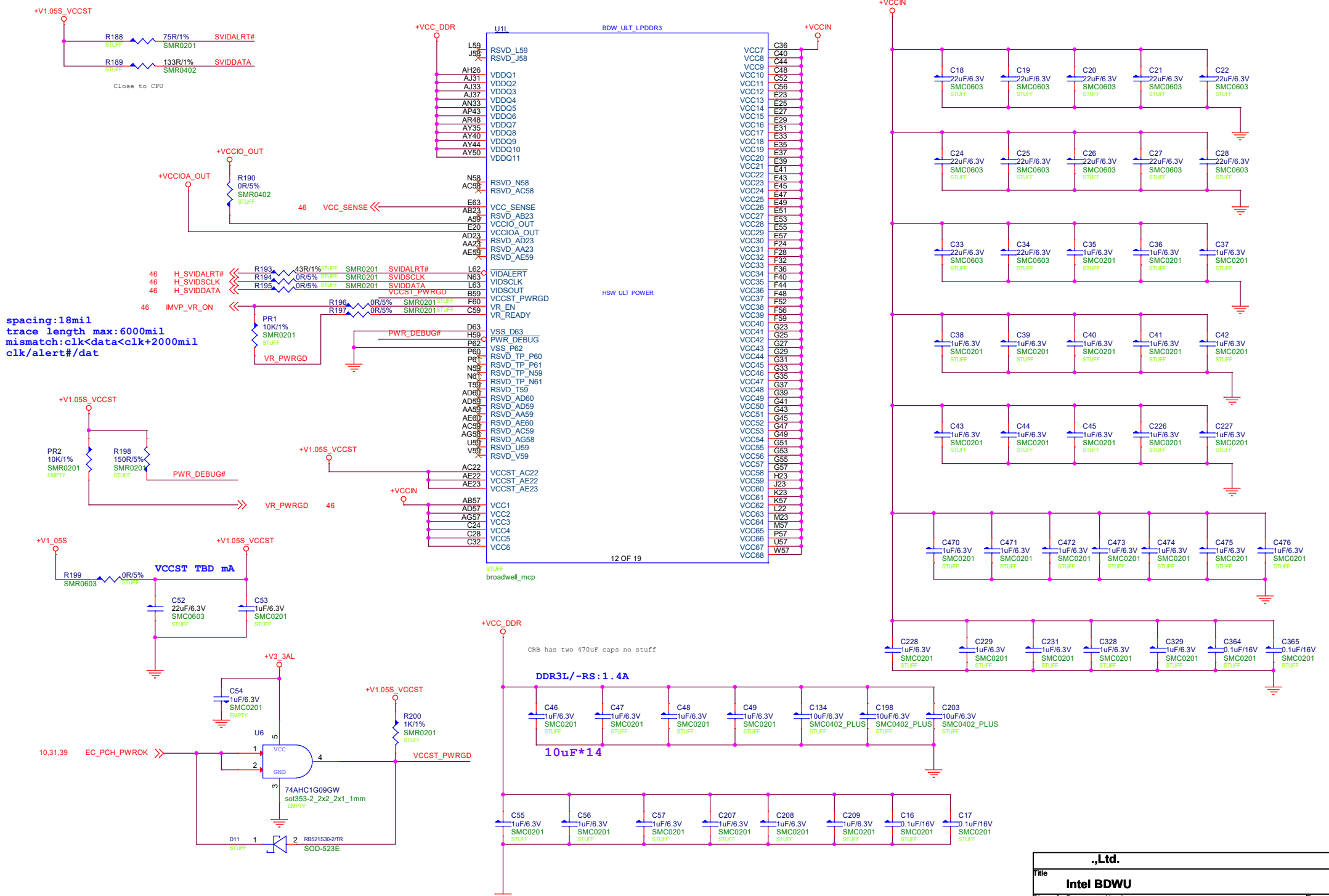


Check list use 120 CRB 75  
Intel confirm 120 Ohm



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spacing:18mil  
 trace length max:6000mil  
 mismatch:clk<data<clk+2000mil  
 clk/alert#/dat

CRB has two 470uF caps no stuff

DDR3L/-RS:1.4A

10uF\*14

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calculated 32A  
IccMax. Adjust  
F-IMAX divider  
for 40A.

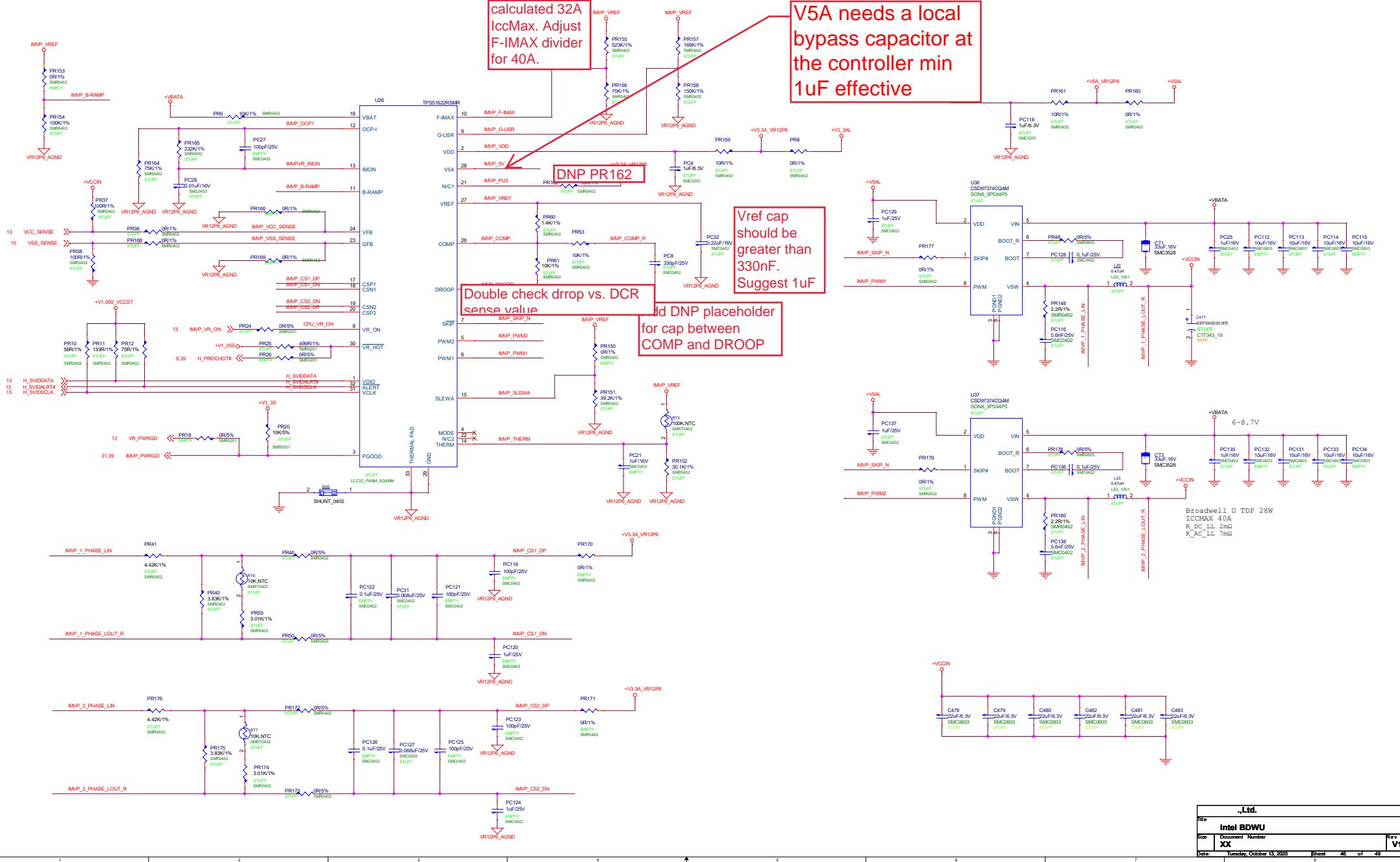
V5A needs a local  
bypass capacitor at  
the controller min  
1uF effective

DNP PR162

Vref cap should be  
greater than  
330nF.  
Suggest 1uF

Double check drop vs. DCR  
sense value

add DNP placeholder  
for cap between  
COMP and DROOP



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