Single Phase PWM Controller
with Light-Load Efficiency Optimization

TPS53211 PSPICE transient model
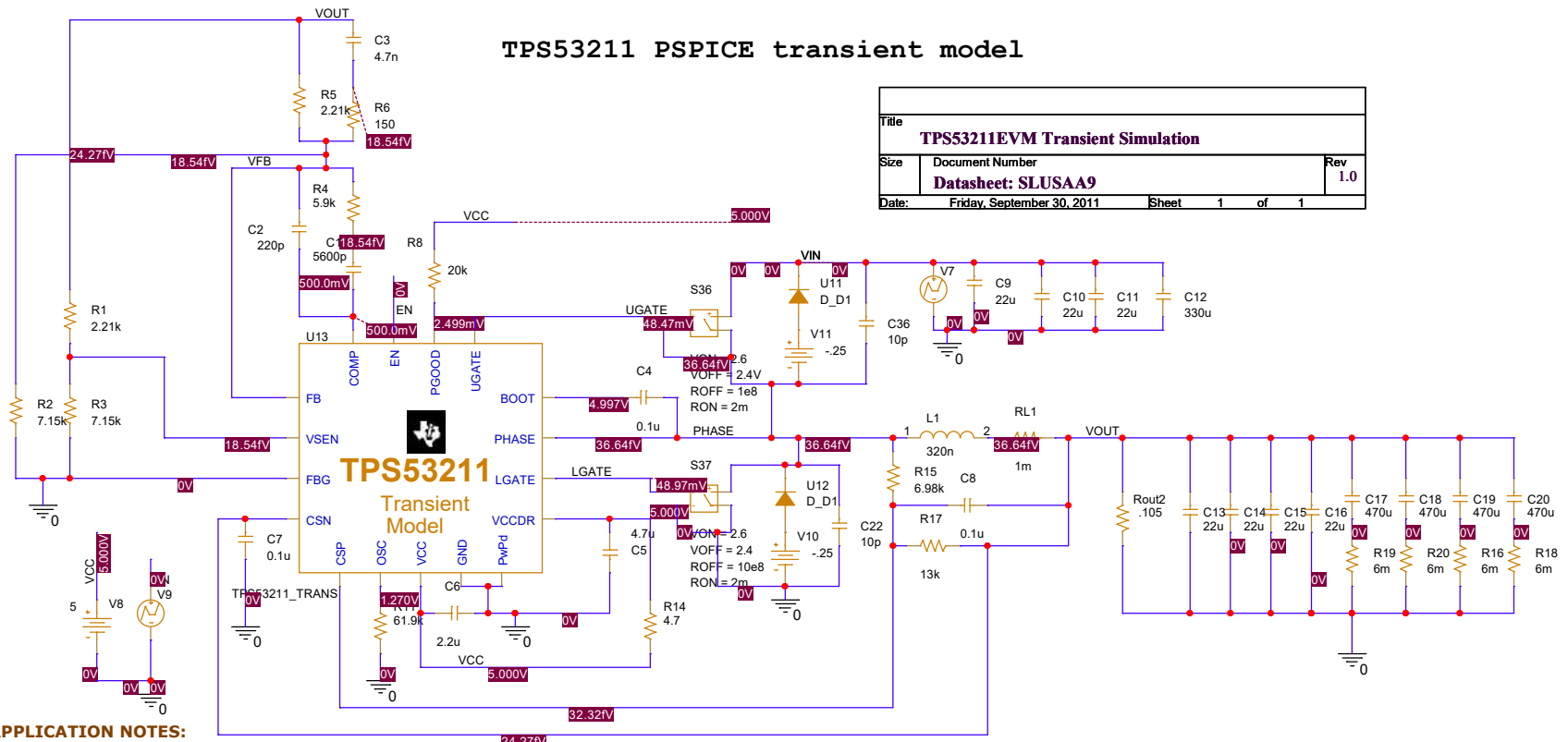
Title		
TPS53211EVM Transient Simulation		
Size	Document Number	Rev
	Datasheet: SLUSAA9	**1.0**
Date:	Friday, September 30, 2011	Sheet 1 of 1

U13 — TPS53211 Transient Model (TPS53211_TRANS)
Pins: COMP, EN, PGOOD, UGATE, FB, BOOT, VSEN, PHASE, FBG, LGATE, CSN, VCCDR, CSP, OSC, VCC, GND, PwPd

APPLICATION NOTES:

1. The model is encrypted and runs only in PSPICE version 15.7 and above.
2. Test setup: VIN - 12V, VOUT- 1.05 V Iout-10A
3. This application has been corner tested for input voltage ranges:
   5V to 12V and Iout: 10mA to 20A.
4. Click PSpice--->Run(F11) to run the simulation.
5. Operating current and shutdown current have not been modeled.
6. Thermal characteristics of the part have not been modeled.
7. The startup simulation of 1.8ms takes approximately 10 mins on a 3GHZ machine.