1. Original setting

Voltage Limits

Vout Max:	1.520 V	Output Voltage Setting and Limits
Vout Command:	0.860 V	When allowing PMBus to control Vout
Margin High:	0.000 V	When allowing PMBus to control Vout
Margin Low:	0.000 V	When allowing PMBus to control Vout
Vout Min:	0.000 V	
VBOOT (MFR_11):	0.860 V	
Vout Scale Monitor/Loop:	1.000	
Vout Transition Rate:	10.0000 mV/µs	
Vout Droop:	0.000 mΩ	
Vout Offset:	0.00 + 0 = 0mV	
SVID_PMBUS_SEL (MFR_02):	10: SVID controls Vout, and PMBus cannot ...	

MFR_SPECIFIC_04	0xD4	0.858 V	0xB36F
MFR_SPECIFIC_08	0xD8	CF_CPU...	0x2D
READ_IIN	0x89	0.00 A	0x8071
READ_IOUT_1	0x8C	5.98 A	0xCAFE
READ_IOUT_2	0x8C	5.86 A	0xCAEE
READ_IOUT_ALL	0x8C	11.41 A	0xD2DA
READ_IOUT_ALL	0x8C	11.41 A	0xD2DA
READ_PIN	0x97	0.02 W	0x8A11
READ_POUT	0x96	9.88 W	0xD278
READ_TEMPERATURE_1	0x8D	35 °C	0xE22B
READ_VIN	0x88	12.000 V	0xD300
READ_VOUT	0x8B	0.860 V	0x007B

2. Adjust Vboot and Vout_command and program into NVM

Voltage Limits

Vout Max:	1.520 V	Output Voltage Setting and Limits
Vout Command:	0.880 V	When allowing PMBus to control Vout
Margin High:	0.000 V	When allowing PMBus to control Vout
Margin Low:	0.000 V	When allowing PMBus to control Vout
Vout Min:	0.000 V	
VBOOT (MFR_11):	0.880 V	
Vout Scale Monitor/Loop:	1.000	
Vout Transition Rate:	10.0000 mV/µs	
Vout Droop:	0.000 mΩ	
Vout Offset:	0.00 + 0 = 0mV	
SVID_PMBUS_SEL (MFR_02):	10: SVID controls Vout, and PMBus	

On/Off Configuration

On/Off Config: 0x17

Mode: CONTROL Pin Only; Control: Active High; Turn off Immediately

Store Configuration to NVM – TPS53622 @ PMBus Address 99d

⚠ **Confirm Storing configuration to non-volatile memory**

This operation will store all configuration values to non-volatile memory on selected devices. Click "Store to NVM" button to proceed.

Timestamp	Message
14:08:13.844	Store configuration to non-volatile memory on TPS53622 @ PMBus Address 99d (63h)...
14:08:13.858	Saving device configuration (RAM) to flash memory ...
14:08:13.858	Wait 100 ms after executing Store_Default...
14:08:13.982	STORE_DEFAULT_ALL: 124 msec elapsed since command is executed
14:08:14.768	TPS53622 @ PMBus Address 99d: Successfully store configuration to NVM

[Store to NVM] [Close]

Power Limit

PIN_MAX: 120 W

Operation

On/Off: ☐ On — Immedia

Margin Fault Action: ○ Act on Fault ○ Ignore Fault

Margining: ◉ None ○ Low ○ High

Iout

IOUT_MAX: 22 A

MFR_SPECIFIC_11	0xDB	VBOOT:...	0x7F
MFR_SPECIFIC_12	0xDC	IIN_SH...	0.880 V
MFR_SPECIFIC_13	0xDD	NVM_PI...	

MFR_SPECIFIC_03	0xD3	Num_A...	0x0002
MFR_SPECIFIC_04	0xD4	0.858 V	0xB36F
MFR_SPECIFIC_08	0xD8	CF_CPU...	0x2D
READ_IIN	0x89	0.00 A	0x8096
READ_IOUT_1	0x8C	5.86 A	0xCAEE
READ_IOUT_2	0x8C	5.86 A	0xCAEE
READ_IOUT_ALL	0x8C	11.59 A	0xD2E6
READ_IOUT_ALL	0x8C	11.59 A	0xD2E6
READ_PIN	0x97	0.00 W	0x8800
READ_POUT	0x96	9.78 W	0xD272
READ_TEMPERATURE_1	0x8D	38 °C	0xE25D
READ_VIN	0x88	12.000 V	0xD300
READ_VOUT	0x8B	0.860 V	0x007B
STATUS_BYTE	0x78	00000010	0x02

But the resister 0xD4 and 0x8B didn't change until we restart power.