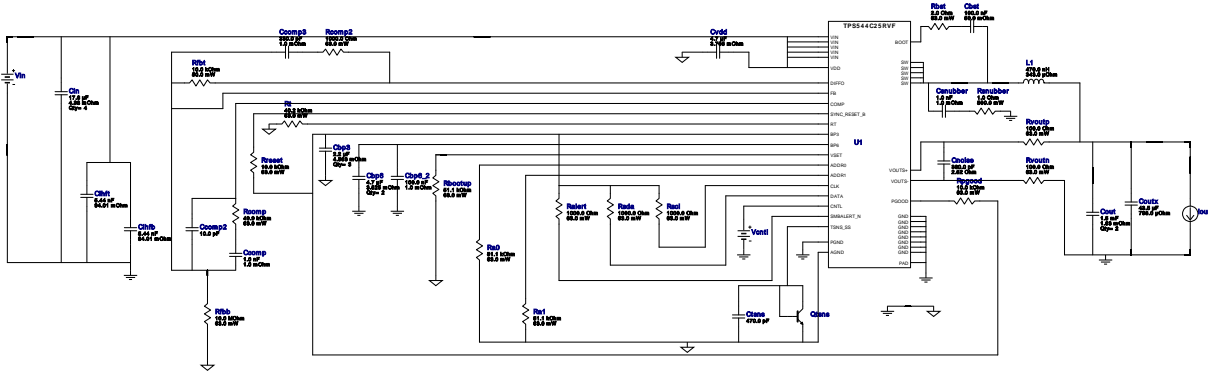


VinMin = 4.5V
 VinMax = 18.0V
 Vout = 1.0V
 Vout Sch = 1.0V
 Iout = 12.36A

Device = TPS544C25RVFR
 Topology = Buck
 Created = 2023-07-24 05:17:49.513
 BOM Cost = NA
 BOM Count = 43
 Total Pd = 2.96W

WEBENCH® Design Report

Design : 7 TPS544C25RVFR
 TPS544C25RVFR 4.5V-18V to 1.00V @ 12.358A




















Design Alerts







Component Selection Information

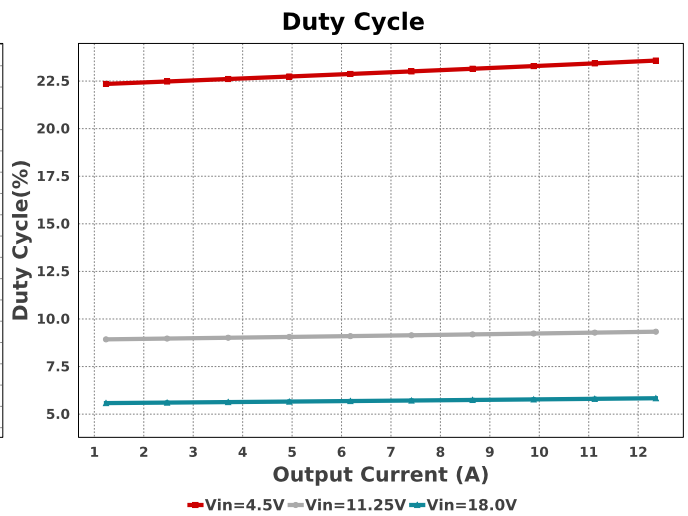
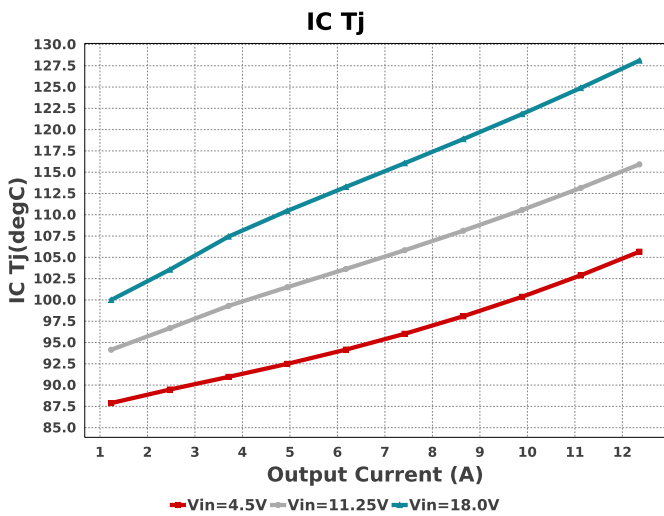
The TPS544C25 is a PMBus(TM) device with key features listed below. PMBus(TM) features marked with * are included in WEBENCH(R) Power Designer. - Adaptive Voltage Scaling (AVS) through VOUT_COMMAND * - Programmable output transition rate* - UVLO*, Soft-Start*, and Soft-Stop - Output voltage and current monitoring - External temperature sensing and reporting - Programmable over current protection with Thermal Compensation - OV, UV, OT Levels - Turn-On and Turn-Off Delays Use the Advanced Options on the left side to set the PMBus(TM) commands. Please refer to the TPS544C25 datasheet and visit <http://www.ti.com/pmbus> for more information.

Electrical BOM

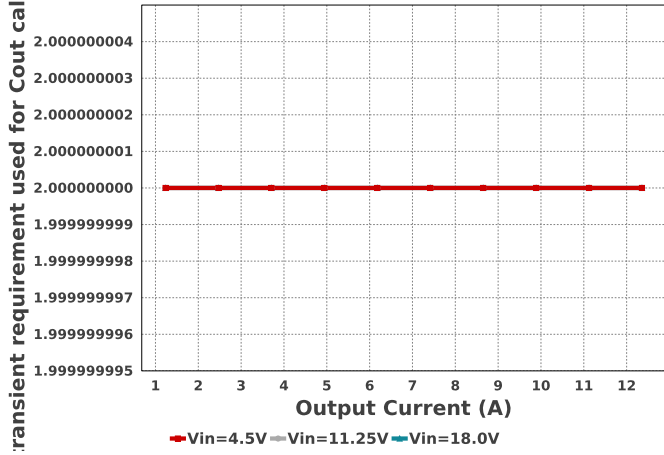
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Cbp3	TDK	C2012X7R1C225K125AB Series= X7R	Cap= 2.2 uF ESR= 4.863 mOhm VDC= 16.0 V IRMS= 0.0 A	3	NA	0805 7 mm ²
Cbp6	MuRata	GRM31CR61E475KA88L Series= X5R	Cap= 4.7 uF ESR= 3.525 mOhm VDC= 25.0 V IRMS= 2.97852 A	2	\$0.10	1206_190 11 mm ²
Cbp6_2	MuRata	GRM155R71C104KA88D Series= X7R	Cap= 100.0 nF ESR= 1.0 mOhm VDC= 16.0 V IRMS= 0.0 A	1	\$0.01	0402 3 mm ²
Cbst	AVX	06033C104KAT2A Series= X7R	Cap= 100.0 nF ESR= 50.0 mOhm VDC= 25.0 V IRMS= 0.0 A	1	\$0.01	0603 5 mm ²
Ccomp	MuRata	GRM155R71H102KA01D Series= X7R	Cap= 1.0 nF ESR= 1.0 mOhm VDC= 50.0 V IRMS= 0.0 A	1	\$0.01	0402 3 mm ²
Ccomp2	Kemet	C0402C100J3GACTU Series= C0G/NP0	Cap= 10.0 pF VDC= 25.0 V IRMS= 0.0 A	1	\$0.01	0402 3 mm ²
Ccomp3	MuRata	GRM155R71H331KA01D Series= X7R	Cap= 330.0 pF ESR= 1.0 mOhm VDC= 50.0 V IRMS= 0.0 A	1	\$0.01	0402 3 mm ²
Cihfb	AVX	04025C682KAT2A Series= C0G/NP0	Cap= 5.44 nF ESR= 94.01 mOhm VDC= 50.0 V IRMS= 0.0 A	1	NA	0805 0 mm ²

Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Cihft	AVX	04025C682KAT2A Series= C0G/NP0	Cap= 5.44 nF ESR= 94.01 mOhm VDC= 50.0 V IRMS= 0.0 A	1	NA	 0805 0 mm ²
Cin	AVX	1206ZC226KAT2A Series= X5R	Cap= 17.6 uF ESR= 4.38 mOhm VDC= 50.0 V IRMS= 4.9 A	4	NA	 1206_180 0 mm ²
Cnoise	AVX	04025C331K4T2A Series= C0G/NP0	Cap= 330.0 pF ESR= 2.52 Ohm VDC= 50.0 V IRMS= 0.0 A	1	NA	 0805 0 mm ²
Cout	Kemet	T530X158M2R5ATE005 x2 Series= ?	Cap= 1.5 mF ESR= 1.63 mOhm VDC= 10.0 V IRMS= 1.058 A	2	NA	CUSTOM 0 mm ²
Coutx	AVX	1206ZC226KAT2A x2 + 0603ZC224KAT2A x2 Series= X7R	Cap= 43.5 uF ESR= 798.0 uOhm VDC= 6.3 V IRMS= 5.302 A	1	NA	 1206_180 0 mm ²
Csubber	MuRata	GRM1885C1H102JA01D Series= C0G/NP0	Cap= 1.0 nF ESR= 1.0 mOhm VDC= 50.0 V IRMS= 0.0 A	1	\$0.01	 0603 5 mm ²
Ctsns	Samsung Electro-Mechanics	CL21C471JBANNNC Series= C0G/NP0	Cap= 470.0 pF VDC= 50.0 V IRMS= 0.0 A	1	\$0.01	 0805 7 mm ²
Cvdd	MuRata	GRM31CR71C475KA01L Series= X7R	Cap= 4.7 uF ESR= 3.705 mOhm VDC= 16.0 V IRMS= 3.0989 A	1	\$0.08	 1206_190 11 mm ²
L1	Würth Elektronik	744301047	L= 470.0 nH 343.0 uOhm	1	\$1.36	WE-HCM_1190 173 mm ²
Qtsns	Fairchild Semiconductor	MMBT3904	Bipolar Transistor	1	\$0.06	 SOT-23 14 mm ²
Ra0	Vishay-Dale	CRCW040251K1FKED Series= CRCW..e3	Res= 51.1 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm ²
Ra1	Vishay-Dale	CRCW040251K1FKED Series= CRCW..e3	Res= 51.1 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm ²
Ralert	Vishay-Dale	CRCW04021K00FKED Series= CRCW..e3	Res= 1000.0 Ohm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm ²
Rbootup	Vishay-Dale	CRCW040251K1FKED Series= CRCW..e3	Res= 51.1 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm ²
Rbst	Vishay-Dale	CRCW04022R00FKED Series= CRCW..e3	Res= 2.0 Ohm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm ²
Rcomp	Vishay-Dale	CRCW040249K9FKED Series= CRCW..e3	Res= 49.9 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm ²
Rcomp2	Vishay-Dale	CRCW04021K00FKED Series= CRCW..e3	Res= 1000.0 Ohm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm ²
Rfbb	Vishay-Dale	CRCW040210M0FKED Series= CRCW..e3	Res= 10.0 MOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm ²
Rfbt	Yageo	RC0201FR-0710KL Series= ?	Res= 10.0 kOhm Power= 50.0 mW Tolerance= 1.0%	1	\$0.01	 0201 2 mm ²

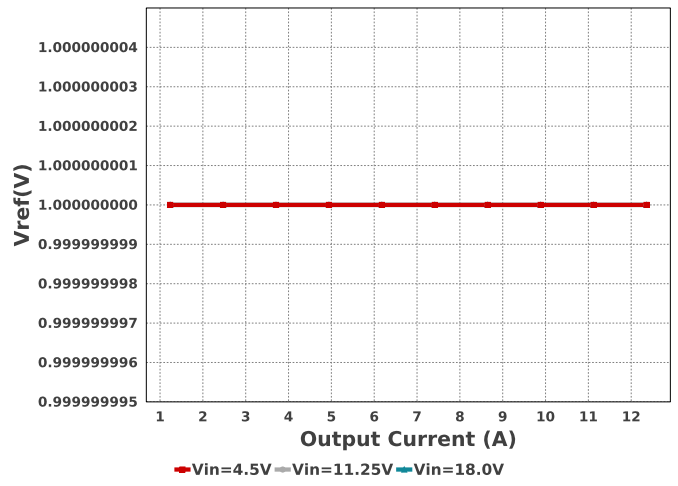
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Rpgood	Vishay-Dale	CRCW040210K0FKED Series= CRCW..e3	Res= 10.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm ²
Rreset	Vishay-Dale	CRCW040210K0FKED Series= CRCW..e3	Res= 10.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm ²
Rsc1	Vishay-Dale	CRCW04021K00FKED Series= CRCW..e3	Res= 1000.0 Ohm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm ²
Rsda	Vishay-Dale	CRCW04021K00FKED Series= CRCW..e3	Res= 1000.0 Ohm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm ²
Rsubber	Stackpole Electronics Inc	CSR1206FT1R00 Series= ?	Res= 1.0 Ohm Power= 500.0 mW Tolerance= 1.0%	1	\$0.04	 1206 11 mm ²
Rt	Vishay-Dale	CRCW040240K2FKED Series= CRCW..e3	Res= 40.2 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm ²
Rvoutn	Vishay-Dale	CRCW0402100RFKED Series= CRCW..e3	Res= 100.0 Ohm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm ²
Rvoutp	Vishay-Dale	CRCW0402100RFKED Series= CRCW..e3	Res= 100.0 Ohm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm ²
U1	Texas Instruments	TPS544C25RVFR	Switcher	1	\$3.96	 RVF0040A 63 mm ²



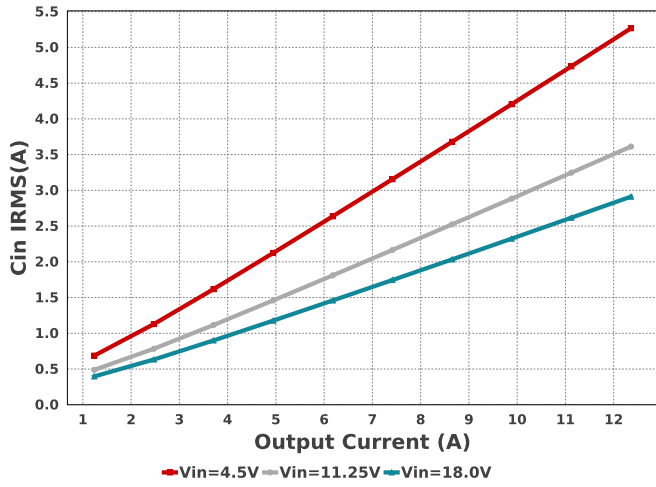
Vout transient requirement used for Cout calculations



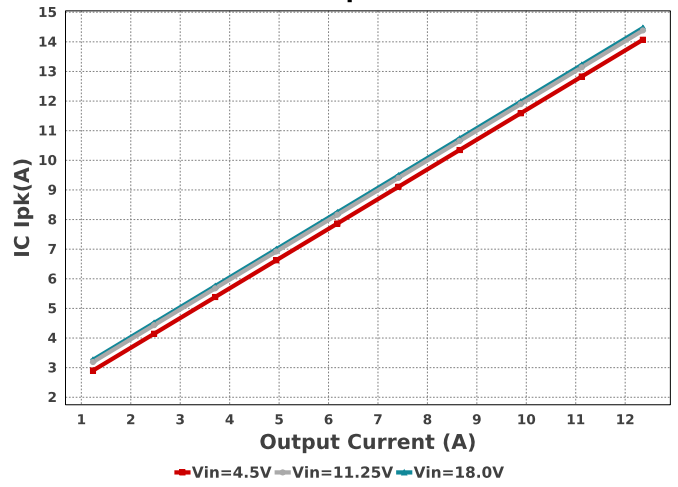
Vref



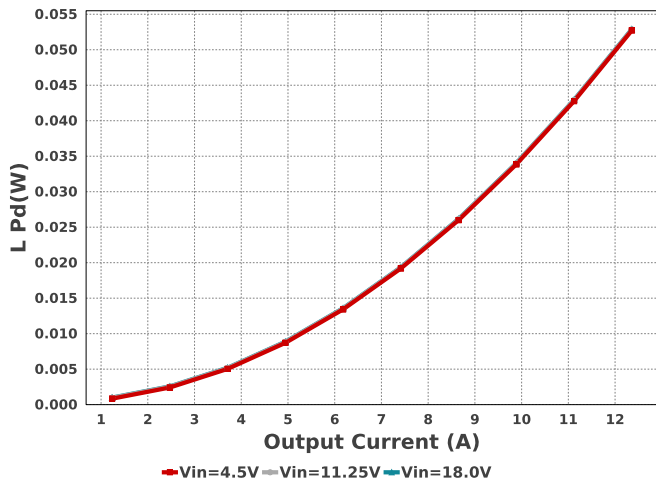
Cin IRMS



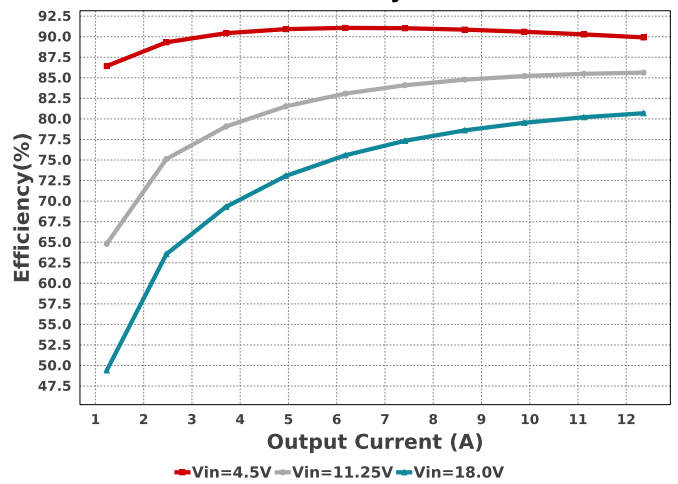
IC Ipk



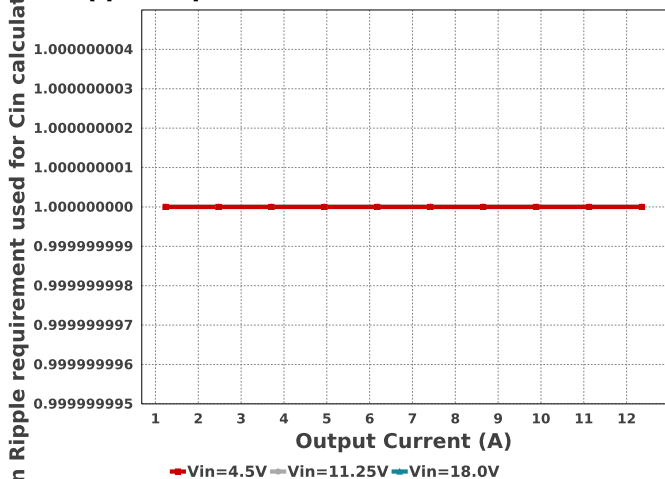
L Pd



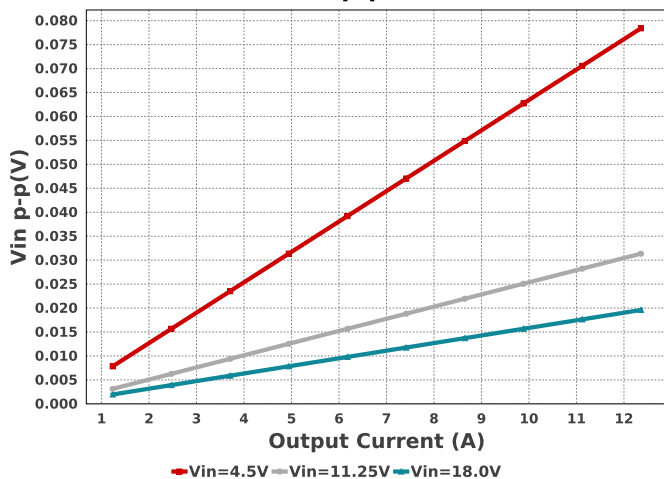
Efficiency



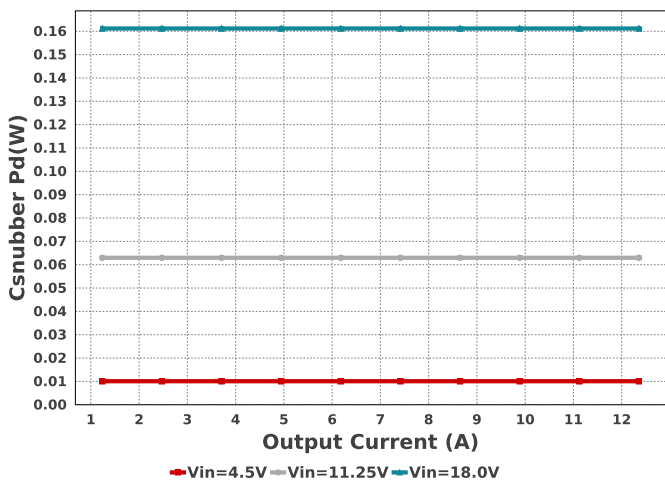
Vin Ripple requirement used for Cin calculations



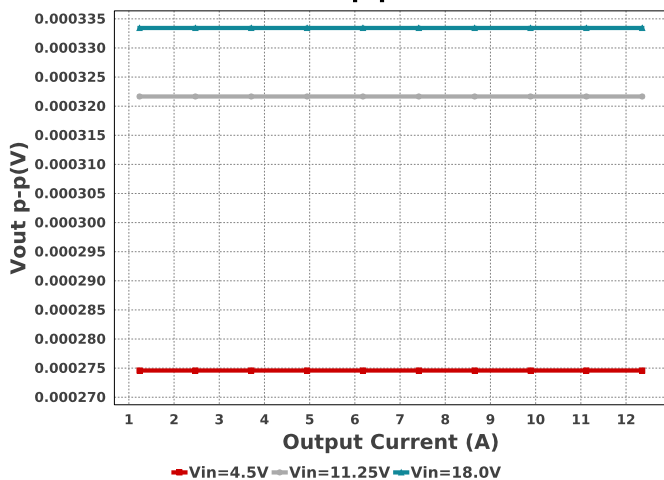
Vin p-p



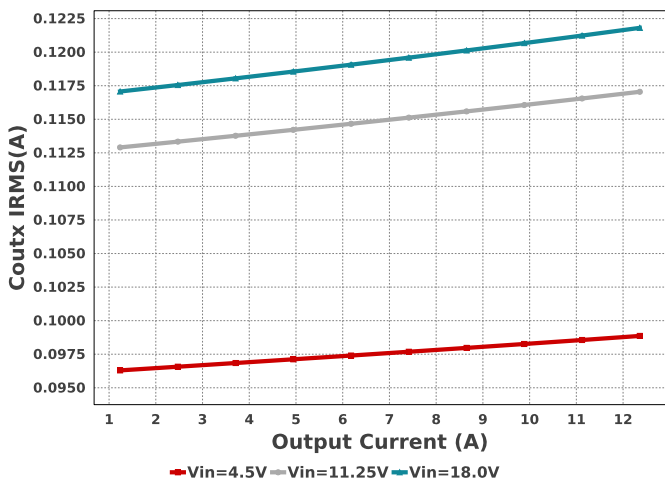
Csnober Pd



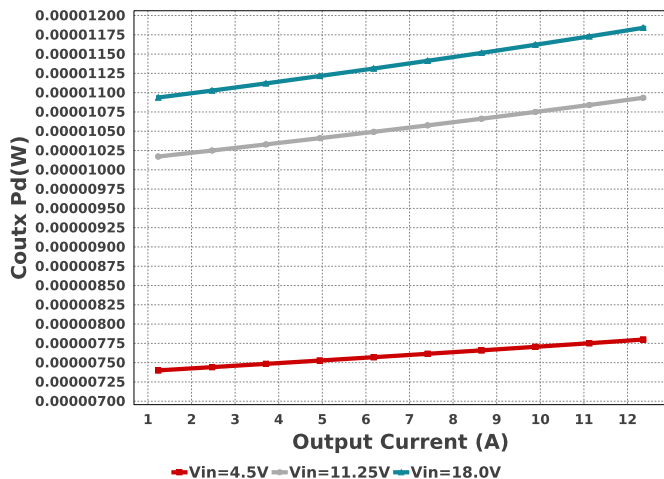
Vout p-p

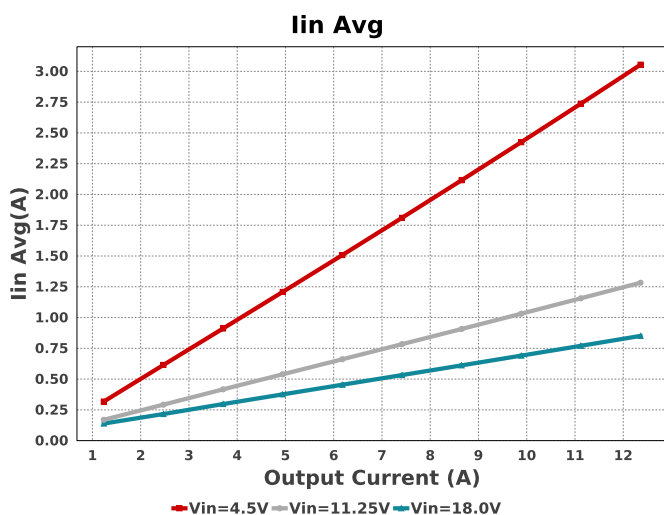
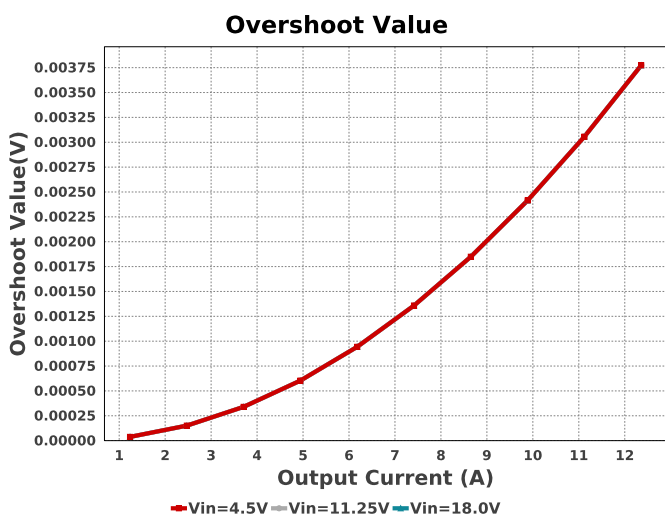
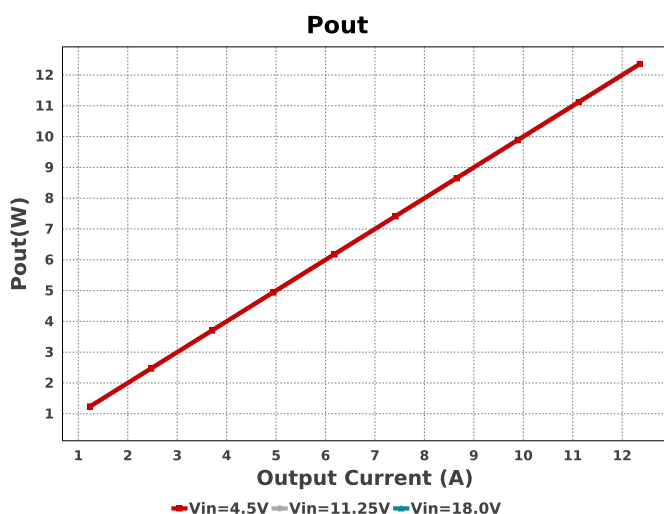
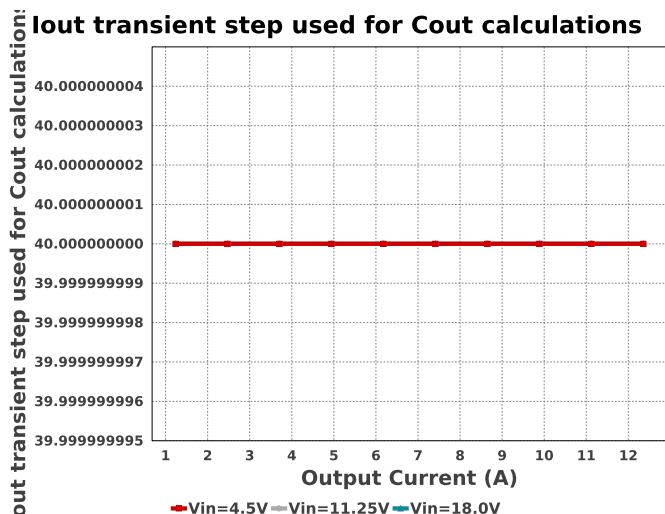
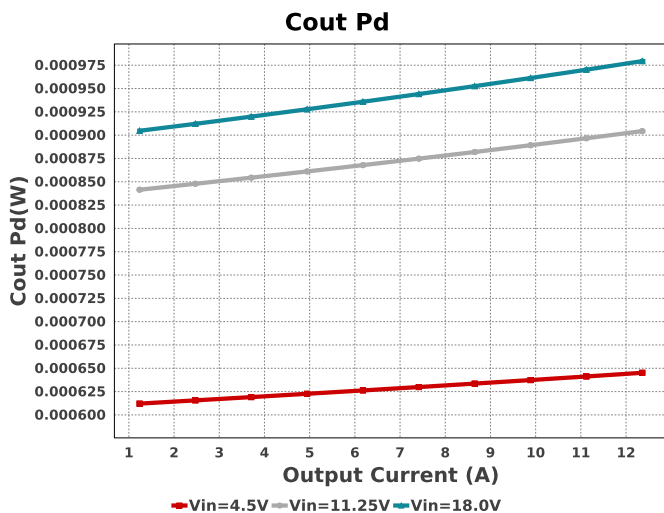
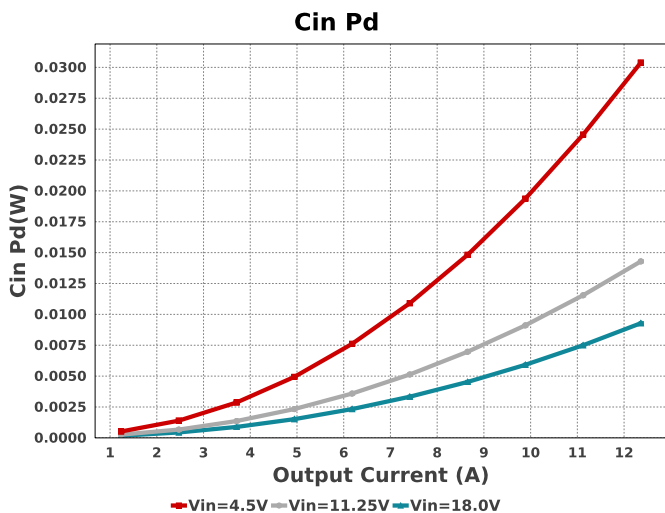


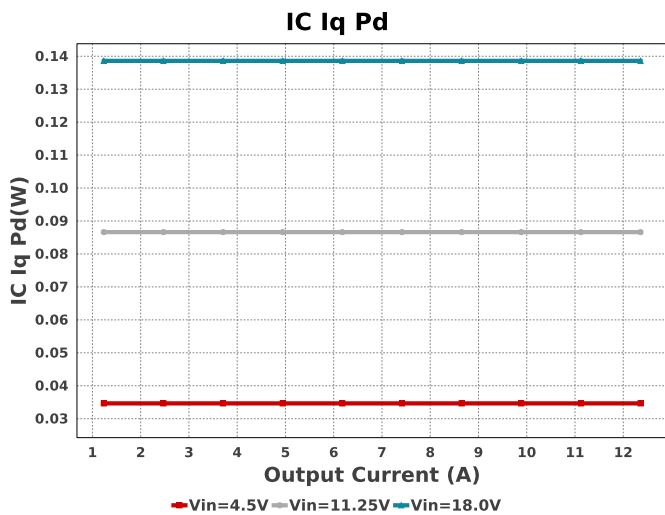
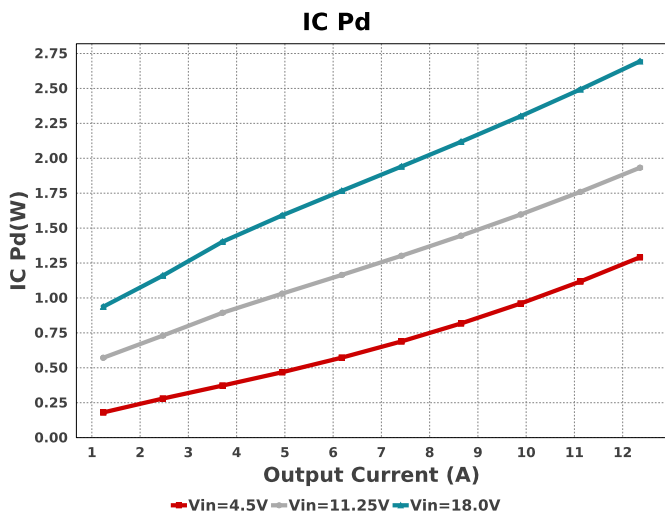
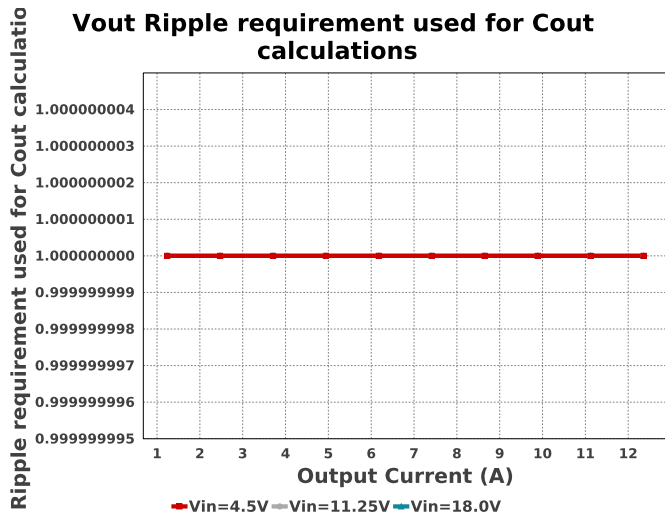
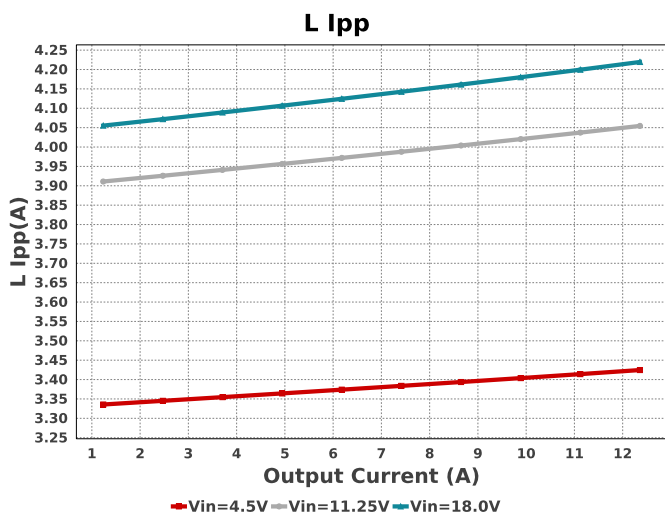
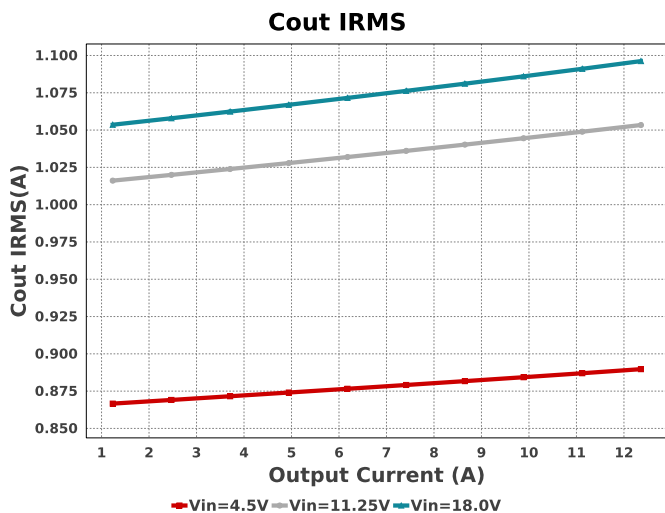
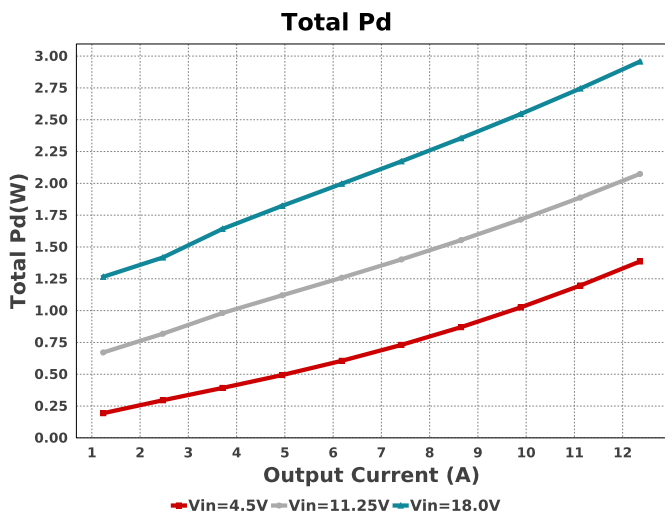
Coutx IRMS

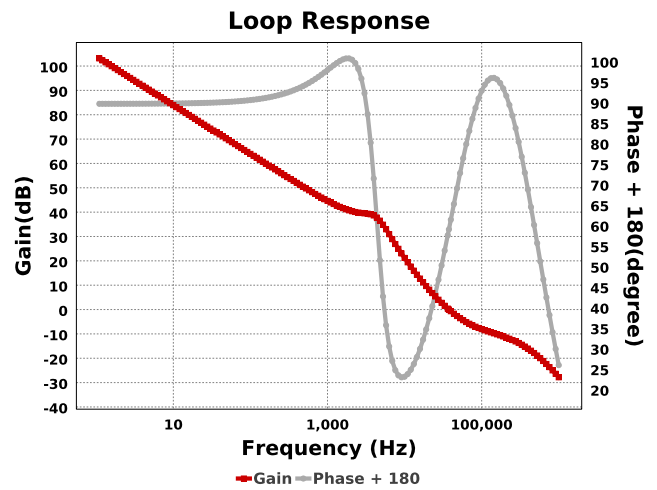
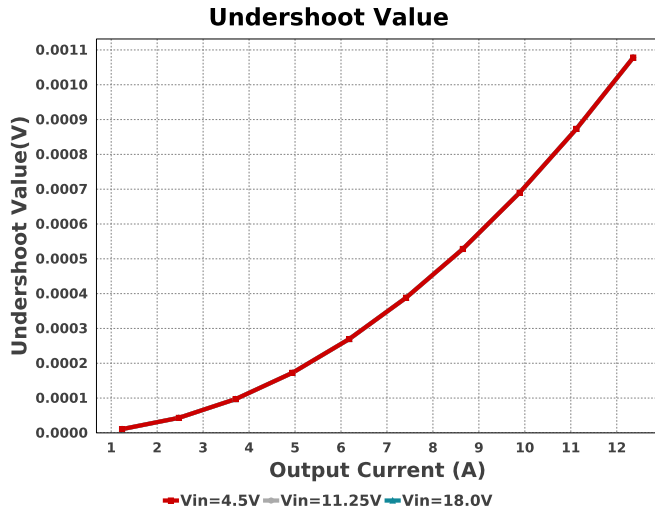


Coutx Pd









Operating Values

#	Name	Value	Category	Description
1.	BOM Count	43		Total Design BOM count
2.	Total BOM	NA		Total BOM Cost
3.	Cin IRMS	2.912 A	Capacitor	Input capacitor RMS ripple current
4.	Cin Pd	9.284 mW	Capacitor	Input capacitor power dissipation
5.	Cout IRMS	1.096 A	Capacitor	Output capacitor RMS ripple current
6.	Cout Pd	979.41 μ W	Capacitor	Output capacitor power dissipation
7.	Coutx IRMS	121.809 mA	Capacitor	Output capacitor_x RMS ripple current
8.	Coutx Pd	11.84 μ W	Capacitor	Output capacitor_x power loss
9.	Csnubber Pd	161.19 mW	Capacitor	Snubber Power Dissipation
10.	IC Ipk	14.468 A	IC	Peak switch current in IC
11.	IC Iq Pd	138.6 mW	IC	IC Iq Pd
12.	IC Pd	2.694 W	IC	IC power dissipation
13.	IC Tj	128.107 degC	IC	IC junction temperature
14.	ICThetaJA Effective	16.0 degC/W	IC	Effective IC Junction-to-Ambient Thermal Resistance
15.	Iin Avg	850.85 mA	IC	Average input current
16.	L Ipp	4.219 A	Inductor	Peak-to-peak inductor ripple current
17.	L Pd	52.892 mW	Inductor	Inductor power dissipation
18.	PMBus Vout Command	512.0	PMBus	PMBus Vout Command
19.	PMBus Vout Scale Loop	1.0	PMBus	PMBus Vout Scale Loop
20.	Cin Pd	9.284 mW	Power	Input capacitor power dissipation
21.	Cout Pd	979.41 μ W	Power	Output capacitor power dissipation
22.	Coutx Pd	11.84 μ W	Power	Output capacitor_x power loss
23.	Csnubber Pd	161.19 mW	Power	Snubber Power Dissipation
24.	IC Pd	2.694 W	Power	IC power dissipation
25.	L Pd	52.892 mW	Power	Inductor power dissipation
26.	Total Pd	2.957 W	Power	Total Power Dissipation
27.	Cross Freq	37.599 kHz	System	Bode plot crossover frequency
28.	Duty Cycle	5.835 %	System	Duty cycle
29.	Efficiency	80.692 %	System	Steady state efficiency
30.	FootPrint	564.0 mm ²	System	Total Foot Print Area of BOM components
31.	Frequency	497.512 kHz	System	Switching frequency
32.	Gain Marg	-35.74 dB	System	Bode Plot Gain Margin
33.	Iout	12.358 A	System	Iout operating point
34.	Iout transient step used for Cout calculations	40.0 %	System	Custom Transient current step requirement that was used for Cout selection (A).
35.	Low Freq Gain	103.12 dB	System	Gain at 1Hz
36.	Mode	CCM	System	Conduction Mode
37.	Overshoot Value	3.773 mV	System	Theoretical Vout Overshoot Value
38.	Phase Marg	59.391 deg	System	Bode Plot Phase Margin

#	Name	Value	Category	Description
39.	Pout	12.358 W	System Information	Total output power
40.	Undershoot Value	1.078 mV	System Information	Theoretical Vout Undershoot Value
41.	Vin	18.0 V	System Information	Vin operating point
42.	Vin Ripple requirement used for Cin calculations	1.0 %	System Information	Custom maximum input ripple requirement that was used for Cin selection(% of Minimum Vin).
43.	Vin p-p	19.602 mV	System Information	Peak-to-peak input voltage
44.	Vout	1.0 V	System Information	Operational Output Voltage
45.	Vout Actual	1.001 V	System Information	Vout Actual calculated based on selected voltage divider resistors
46.	Vout Ripple requirement used for Cout calculations	1.0 %	System Information	Custom maximum output ripple requirement that was used for Cout selection(% of Vout).
47.	Vout Tolerance	902.036 m%	System Information	Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable
48.	Vout p-p	333.432 μ V	System Information	Peak-to-peak output ripple voltage
49.	Vout transient requirement used for Cout calculations	2.0 %	System Information	Custom Transient voltage change requirement that was used for Cout selection (% of Vout).
50.	Vref	1.0 V	System Information	Reference voltage

Design Inputs

Name	Value	Description
Iout	12.358	Maximum Output Current
VinMax	18.0	Maximum input voltage
VinMin	4.5	Minimum input voltage
VinTyp	12.0	Typical input voltage
Vout	1.0	Output Voltage
base_pn	TPS544C25	Base Product Number
source	DC	Input Source Type
Ta	85.0	Ambient temperature
UserFsw	498.0 k	Customer Selected Frequency
1. Vout Sch	1.0	Output voltage selected

WEBENCH® Assembly

Component Testing

Some published data on components in datasheets such as Capacitor ESR and Inductor DC resistance is based on conservative values that will guarantee that the components always exceed the specification. For design purposes it is usually better to work with typical values. Since this data is not always available it is a good practice to measure the Capacitance and ESR values of C_{in} and C_{out} , and the inductance and DC resistance of L1 before assembly of the board. Any large discrepancies in values should be electrically simulated in WEBENCH to check for instabilities and thermally simulated in WebTHERM to make sure critical temperatures are not exceeded.

Soldering Component to Board

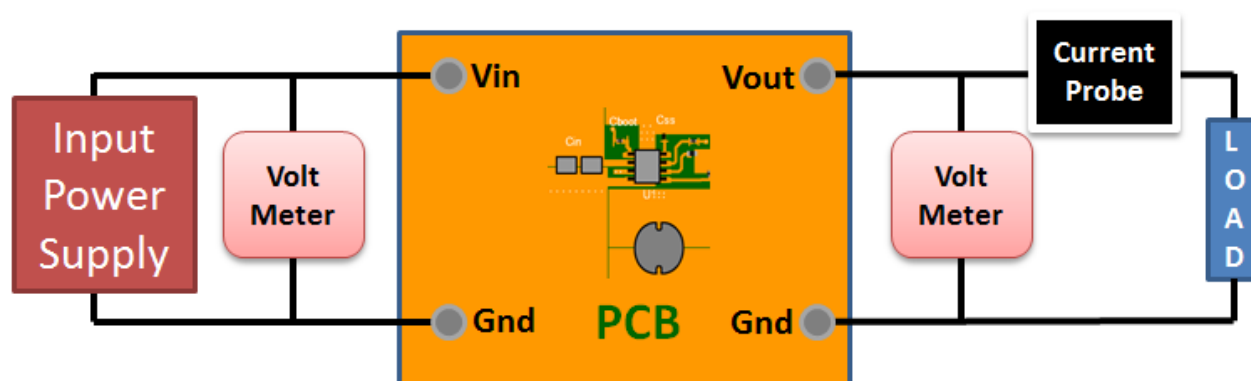
If board assembly is done in house it is best to tack down one terminal of a component on the board then solder the other terminal. For surface mount parts with large tabs, such as the DPAK, the tab on the back of the package should be pre-tinned with solder, then tacked into place by one of the pins. To solder the tab down to the board place the iron down on the board while resting against the tab, heating both surfaces simultaneously. Apply light pressure to the top of the plastic case until the solder flows around the part and the part is flush with the PCB. If the solder is not flowing around the board you may need a higher wattage iron (generally 25W to 30W is enough).

Initial Startup of Circuit

It is best to initially power up the board by setting the input supply voltage to the lowest operating input voltage 4.5V and set the input supply's current limit to zero. With the input supply off connect up the input supply to V_{in} and GND. Connect a digital volt meter and a load if needed to set the minimum load of the design from V_{out} and GND. Turn on the input supply and slowly turn up the current limit on the input supply. If the voltage starts to rise on the input supply continue increasing the input supply current limit while watching the output voltage. If the current increases on the input supply, but the voltage remains near zero, then there may be a short or a component misplaced on the board. Power down the board and visually inspect for solder bridges and recheck the diode and capacitor polarities. Once the power supply circuit is operational then more extensive testing may include full load testing, transient load and line tests to compare with simulation results.

Load Testing

The setup is the same as the initial startup, except that an additional digital voltmeter is connected between V_{in} and GND, a load is connected between V_{out} and GND and a current meter is connected in series between V_{out} and the load. The load must be able to handle at least rated output power + 50% (7.5 watts for this design). Ideally the load is supplied in the form of a variable load test unit. It can also be done in the form of suitably large power resistors. When using an oscilloscope to measure waveforms on the prototype board, the ground leads of the oscilloscope probes should be as short as possible and the area of the loop formed by the ground lead should be kept to a minimum. This will help reduce ground lead inductance and eliminate EMI noise that is not actually present in the circuit.



Design Assistance

1. Master key : 892177D0ECBDD44413F0965774ADE44F[v1]
2. **TPS544C25** Product Folder : <http://www.ti.com/product/TPS544C25> : contains the data sheet and other resources.

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