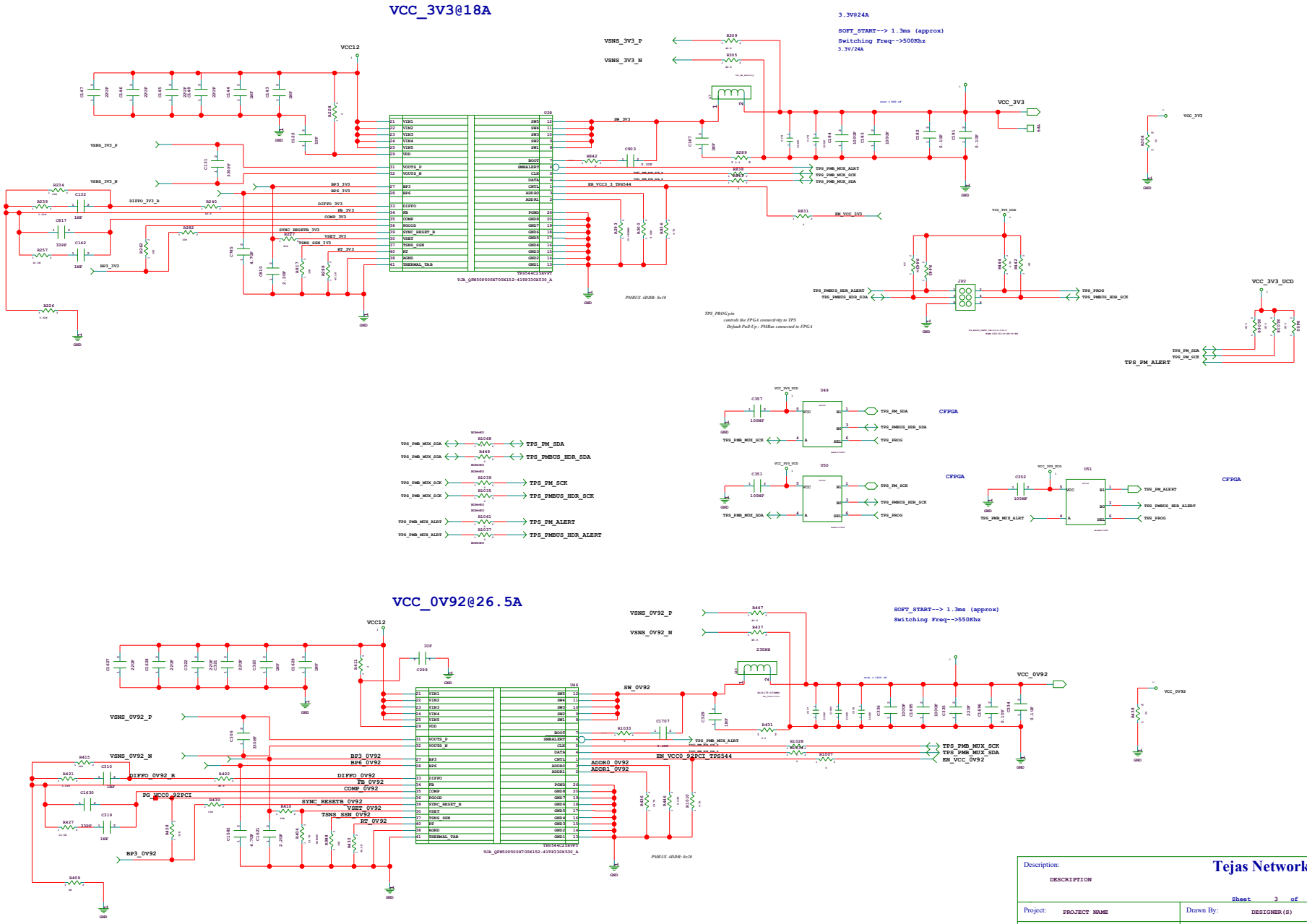
VCC_3V3@18A
3.3V@24A
SOFT_START--> 1.3ms (approx)
Switching Freq-->500Khz
3.3V/24A
VCC12
VSNS_3V3_P
VSNS_3V3_N
VCC_3V3
VCC_3V3_UCD
TPS_PM_ALERT
TPS_PM_SDA
TPS_PM_SCK
TPS_PMBUS_HDR_SDA
TPS_PMBUS_HDR_SCK
TPS_PMBUS_HDR_ALERT
TPS_PROG
CFPGA
VCC_0V92@26.5A
SOFT_START--> 1.3ms (approx)
Switching Freq-->550Khz
VSNS_0V92_P
VSNS_0V92_N
VCC_0V92
SW_0V92
EN_VCC0_92PCI_TPS544
ADDR0_0V92
ADDR1_0V92
PMBUS ADDR: 0x10
PMBUS ADDR: 0x20
TPS_PROG pin controls the FPGA connectivity to TPS Default Pull-Up : PMBus connected to FPGA
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Description:
DESCRIPTION
Sheet 3 of 6
Project: PROJECT NAME
Drawn By: DESIGNER(S)
Date: 10-31-2025_11:01
Rev: 1.0
Reviewed By: REVIEWER(S)
PART NUMBER: PART_NUMBER