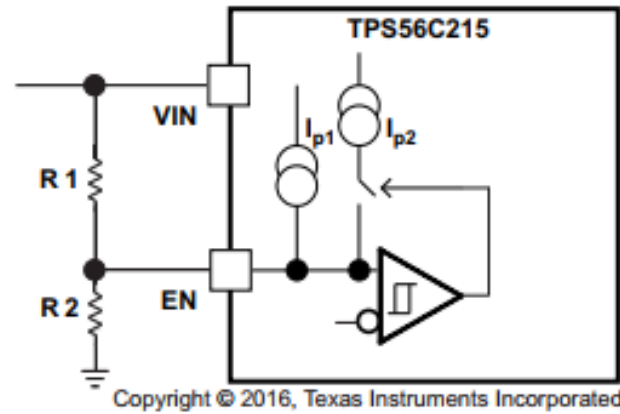
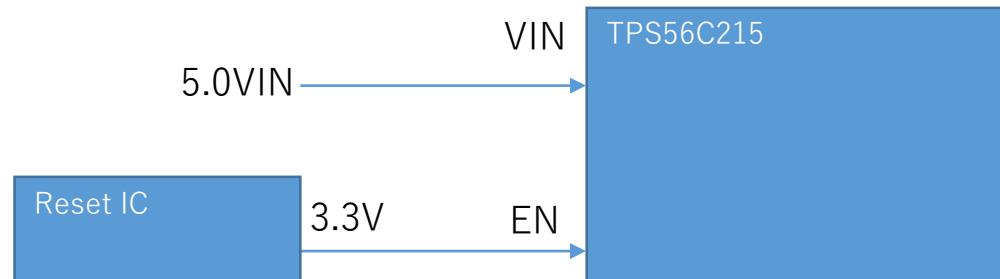


In the datasheet p.18,
Are I_{p1} and I_{p2} pull-up from V_{in} ?



Is it possible to input 3.3 V of CMOS to EN pin
without using dividing resistance between V_{in} to EN ?



And is it better to change the level by OpenDrain?

