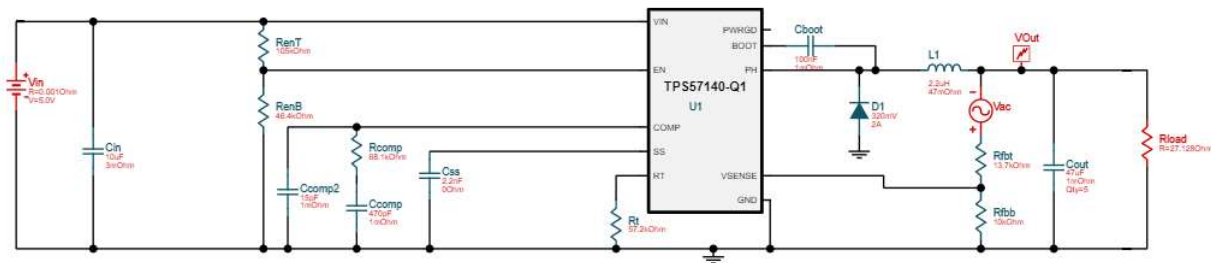
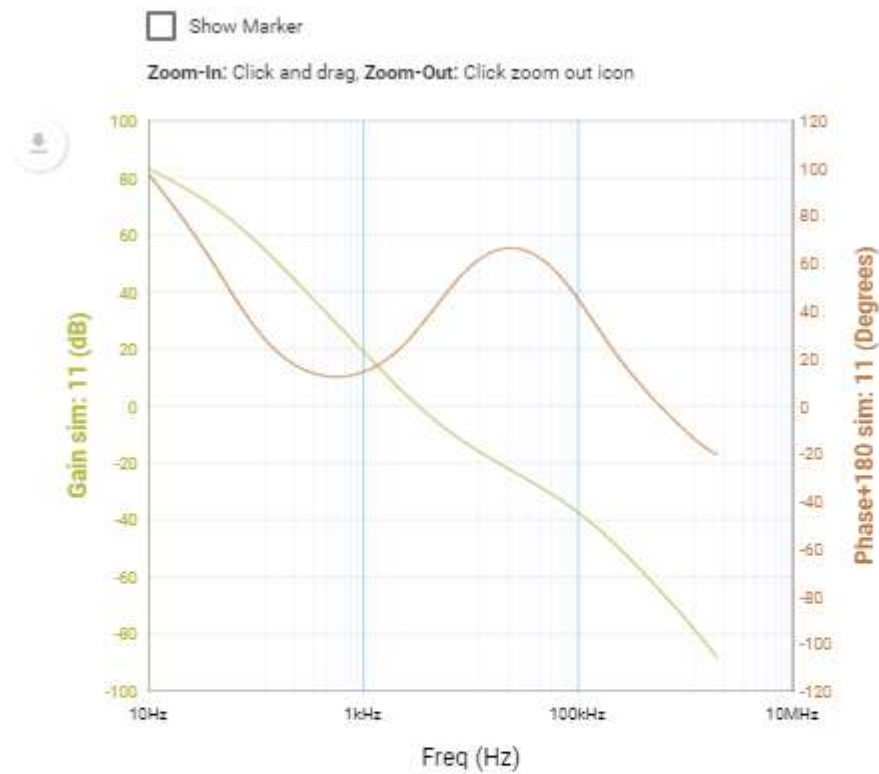


Here is the circuit.



In the Bode Plot page, it shows that the phase margin is **34 degree**.



### Performance Summary

Sim ID	Low Freq Gain	Cross Freq	Phase Marg	Gain Marg
11	83.20 dB	3.23 kHz	32.42 Deg	-64.75 dB

Whereas in the Customize page, it showed **57 degree**.

OPERATING VALUES

CHARTS

Vin (V)

5.5

(4.5 - 8.5)

V

Iout (A)

0.07

(0 - 0.892)

A

RECALCULATE

Categories

System Information

Capacitor

IC

Inductor

Diode

Power

All

Name	Value	Description
Phase Marg	57.31 °	Bode Plot Phase Margin
Vout Actual	1.9 V	Vout Actual calculated based on selected voltage divider resistors
Vout Tolerance	2.18%	Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable
Total BOM	NA	Total BOM Cost