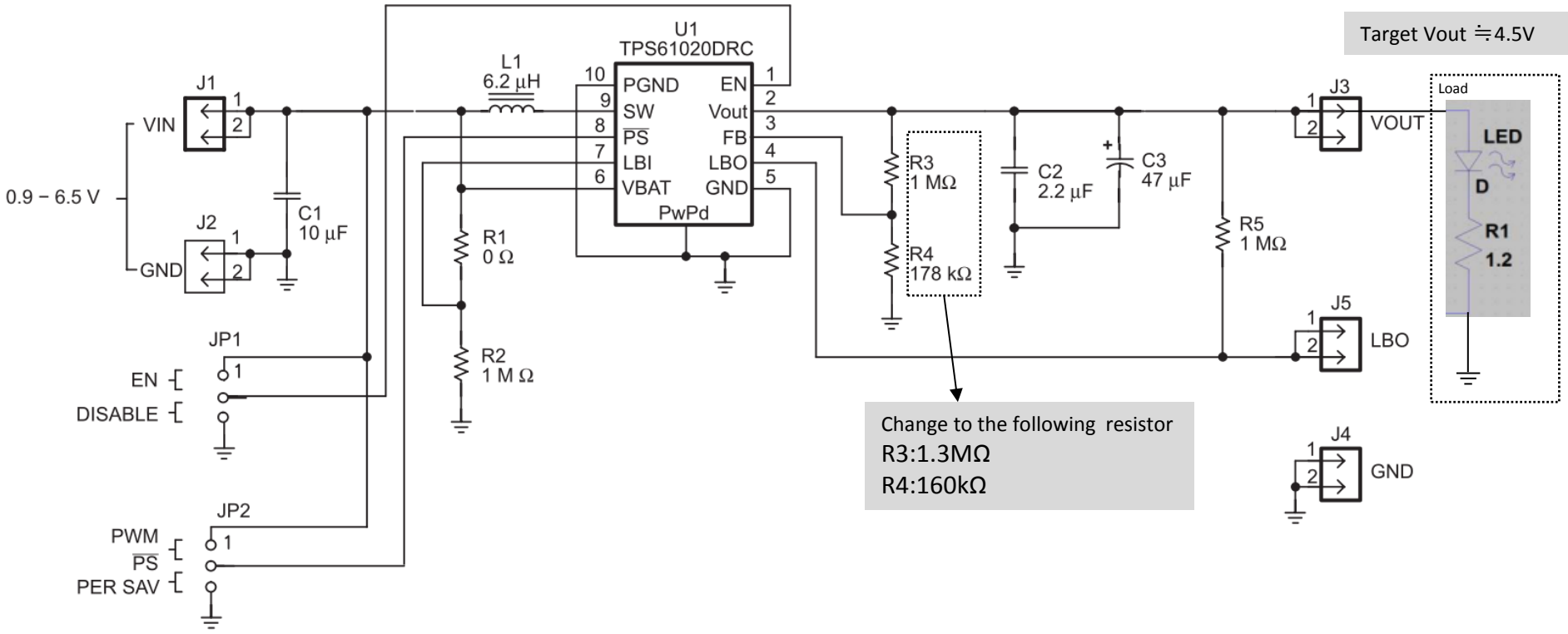
Test circuit (TPS61020 EVM)



Test circuit output voltage

Vin(V)	Input current	Vout(V)	lord(A)
0.84	-	shutdown	No lord
1.6	-	3	No lord
1.7	-	4.5	No lord
↓	-	↓	↓
6.5	-	4.5	No lord

In the no-load condition, the EVM outputs the expected voltage.

Vin(V)	Input current	Vout(V)	lord(A)
1.5	0.4	3.1	0.129
2	0.4	3.25	0.166
3	0.4	3.52	0.248
3.5	0.4	3.66	0.29
4	0.43	3.66	0.335
4.3	0.16	3.16	0.365
4.4	0.17	3.25	0.167
4.5	0.17	3.21	0.17
5	0.19	3.33	0.191
6	0.24	3.49	0.235

In the case of loading,
Output voltage falls below the target voltage.
Is there something that is considered to be the cause of that?
(It seems that overcurrent limitation is not activated)