

Schematic

5.2 Schematic and Bill of Materials

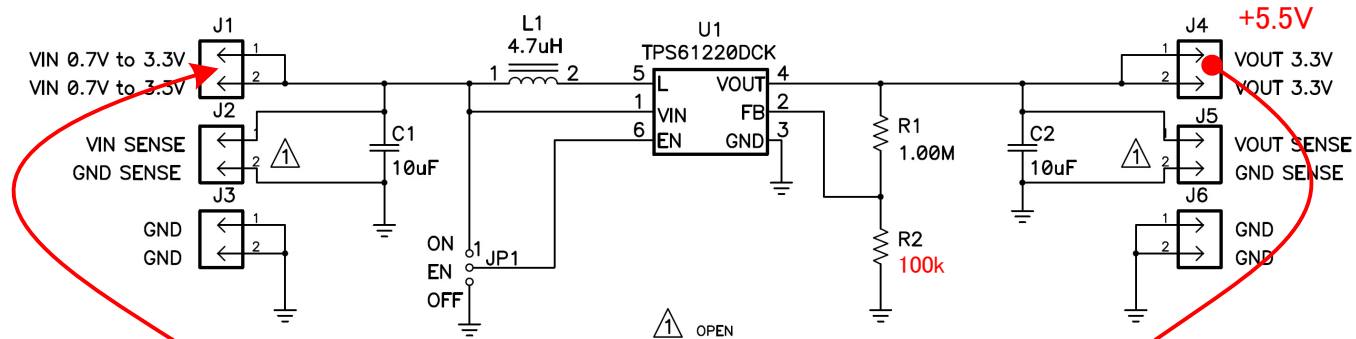
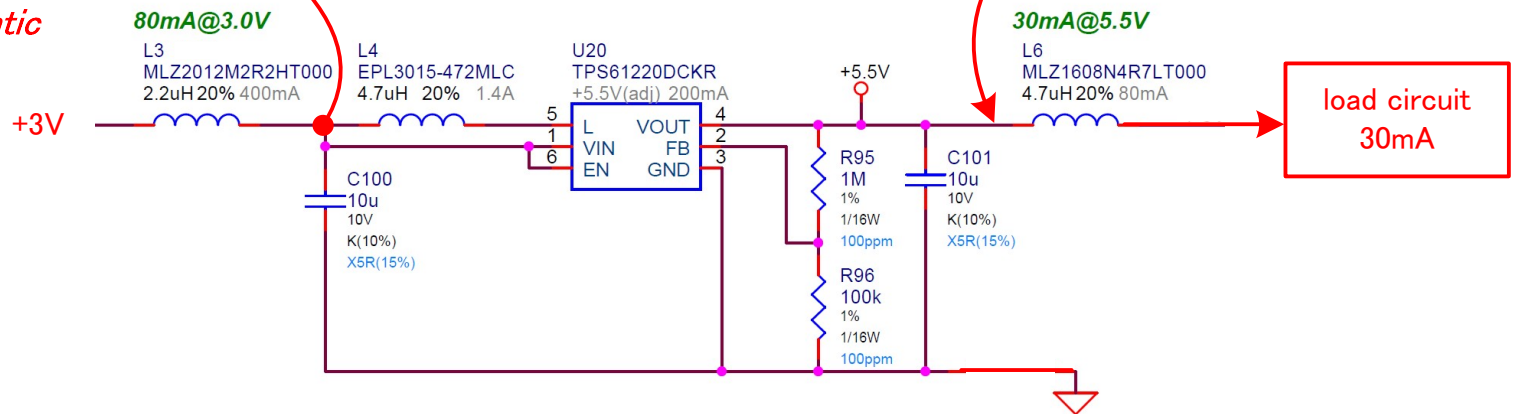


Figure 4. TPS61220EVM-319 Schematic

Our schematic



Layout

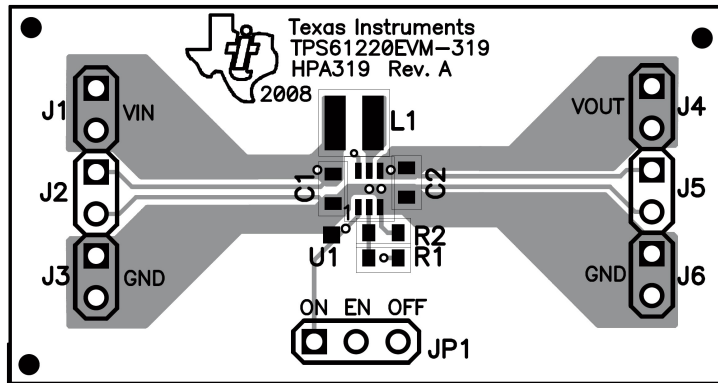


Figure 1. Top Assembly Layer

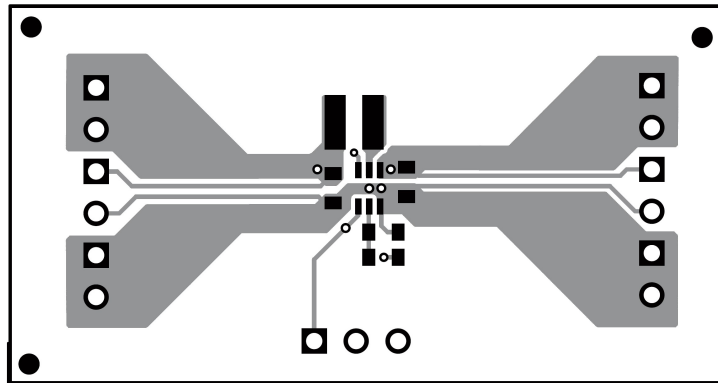
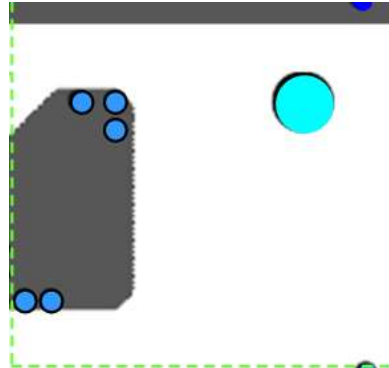
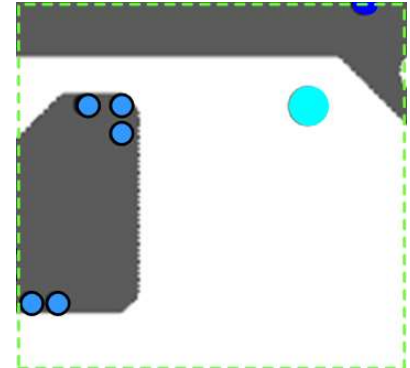


Figure 2. Top Layer

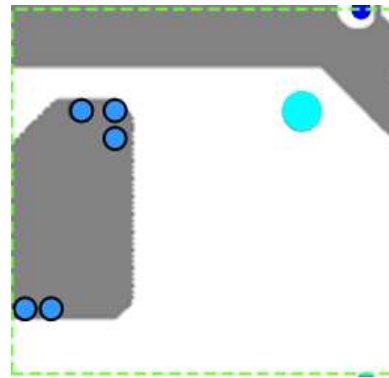
L1



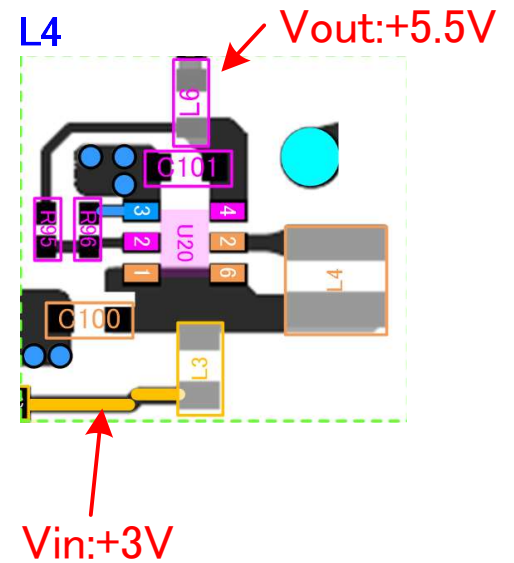
L2



L3



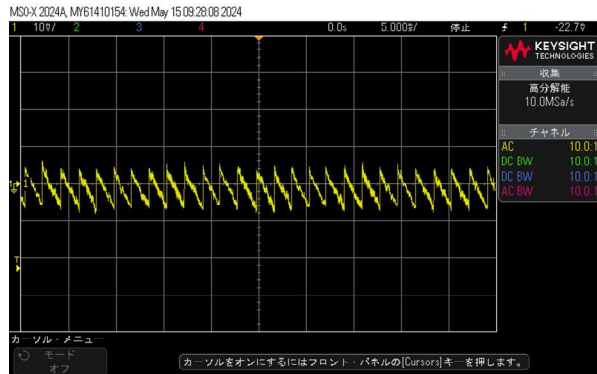
L4



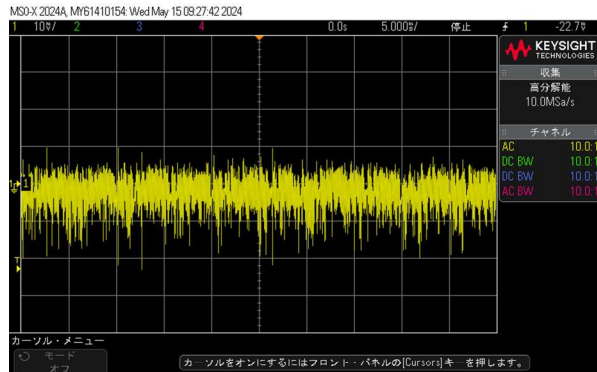
Waveform

TPS61220EVM-319

No load

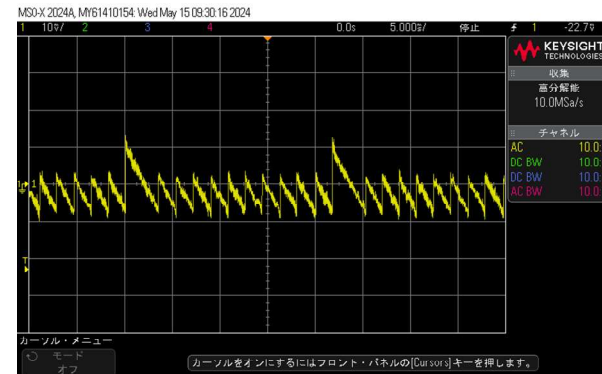


Actual load



our circuit

No load



Actual load

