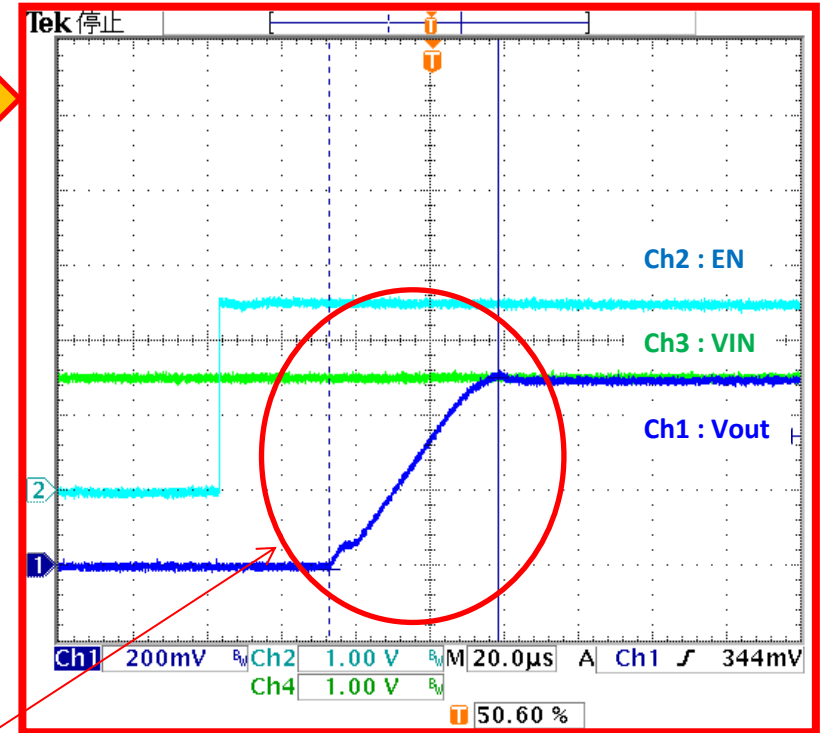
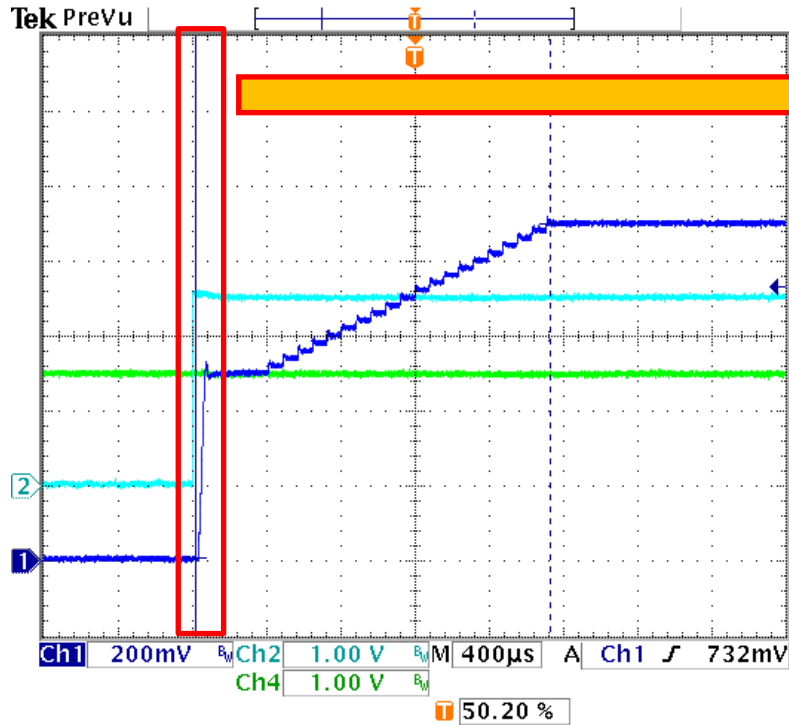


Q : Ramp rate before initiating softstart



I understand that this is correct operation at start-up, but our customer want to reduce ramp rate of this duration. Do you have any idea to achieve it?

Test Condition

DUT

- TPS62361BEVM-655

External Component

- $C_{in} = 10\mu F$
- $C_{out} = 22\mu F$
- $L = 1\mu H$
- $C_{AVIN}, C_{VDD} = 0.1\mu F$

Input

- $V_{IN} = AV_{IN} = 2.5V$
- $V_{EN} = 2.5V$
- $V_{DD} = 3.3V$

Load

- No load (Open)

Pin configuration

- $VSELO = 0$
- $VSEL1 = 0$

GUI Capture

Texas Instruments - TPS6236x EVM - GUI v1.2.0.0

File Help

READ WRITE I2C Activity: W I=60 A=00 D=A8 C=110 C

SET0 - SET3

VSEL1=0 VSEL0=0	SET0	Output Voltage	Operation Mode
		900.00 mV	Forced PWM
VSEL1=0 VSEL0=1	SET1	Output Voltage	Operation Mode
		1400.00 mV	PFM/PWM
VSEL1=1 VSEL0=0	SET2	Output Voltage	Operation Mode
		1160.00 mV	PFM/PWM
VSEL1=1 VSEL0=1	SET3	Output Voltage	Operation Mode
		1160.00 mV	PFM/PWM

CONTROLS

- Enable EN internal pull down resistor
- Enable VSELO internal pull down resistor
- Enable VSEL1 internal pull down resistor
- Disable Temperature shutdown feature
- Enable Active output capacitor discharge at shutdown

0.25 mV/us Output Voltage ramp timing

Cap discharged by load Ramp behavior if in PFM

REGISTERS

	7	6	5	4	3	2	1	0	
SET0 (0x00)	R	W	1	0	1	0	1	0	0
SET1 (0x01)	R	W	0	1	0	1	1	0	1
SET2 (0x02)	R	W	0	1	0	0	0	0	1
SET3 (0x03)	R	W	0	1	0	0	0	0	1
Ctrl (0x04)	R	W	1	1	1	0	0	1	0
Temp (0x05)	R	W	0	0	0	0	0	0	0
RmpCtrl (0x06)	R	W	1	1	1	0	0	1	0
Reserved (0x07)	R	W	0	0	0	0	0	0	0
Chip_ID (0x08)	R	W	1	0	0	0	0	1	1
Chip_ID (0x09)	R	W	1	0	0	0	0	1	1

STATUS

:Tj early warning Tj < 120 degrees C

:Tj Temp shutdown Die Temp within valid range

Reset Temp shutdown bit

TPS62361B or TPS62365 USB Bridge Connected (ver. 49.2.2) I2C 100kHz Texas Instruments