

【Additional Information 17th, July】

1, The customer's schematics is same circuit of previous page.
Customer was designed with reference to the block diagram of the data sheet.

2, The BOM is following ;

R1,R2 --- Not connect

R3 ----- 10k

R4 ----- 10k

R5 ----- 20k

R6 ----- 20k

Input capacitor ----- GRM188B31A106ME69D

Output capacitor ----- GRM188B31A106ME69D

inductor ----- NRS4018T2R2MDGJV