TPS650250 VDCDC1 DCDCI (VO) STEP-DOWN EDCOCH CONVERTER ENABLE - DE EN_DCDC1 1600 mA PGND1 25V/1.8V or adjustable DCDC (memory) STEP-DOWN EDCDC2 EN_DCDC2 CONVERTER 800 mA VINDCDC3 DCDC3(core) STEP-DOWN 22 pF DEFDCDC3 CONVERTER ENABLE ---PGND3 WN LDO VLD01 EN/BLE EN_LDO 200 mA LDO VLD02 200 mA LDO EN_Vdd_alive ENABLE-**VLD03** 2.2 pF 30 mA LDO R11 I/O voltage PWRFAL R125 Vref = 1 V (PAGND2 AGND1

[Customer Conditions]

1, Vbat : 5V

Measuring point

2, VDCDC1 : 3.3V / 250mA(Typ) VDCDC2 : 1.8V / 50mA(Typ) VDCDC3 : 1.2V / 50mA(Typ)

3, Capacitors and coils are the same value as the data sheet.

4, Power sequence

Vbat : 5V --- 500ms ---> Enable

[Waveform of DCDC1]



[Additional Information 17th,July]

1, The customer's schematics is same circuit of previous page.

Customer was designed with reference to the block diagram of the data sheet.

2, The BOM is following;

```
R1,R2 --- Not connect
R3 ----- 10k
R4 ----- 10k
R5 ----- 20k
R6 ----- 20k
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Input capacitor ---- GRM188B31A106ME69D Output capacitor ---- GRM188B31A106ME69D inductor ---- NRS4018T2R2MDGJV
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