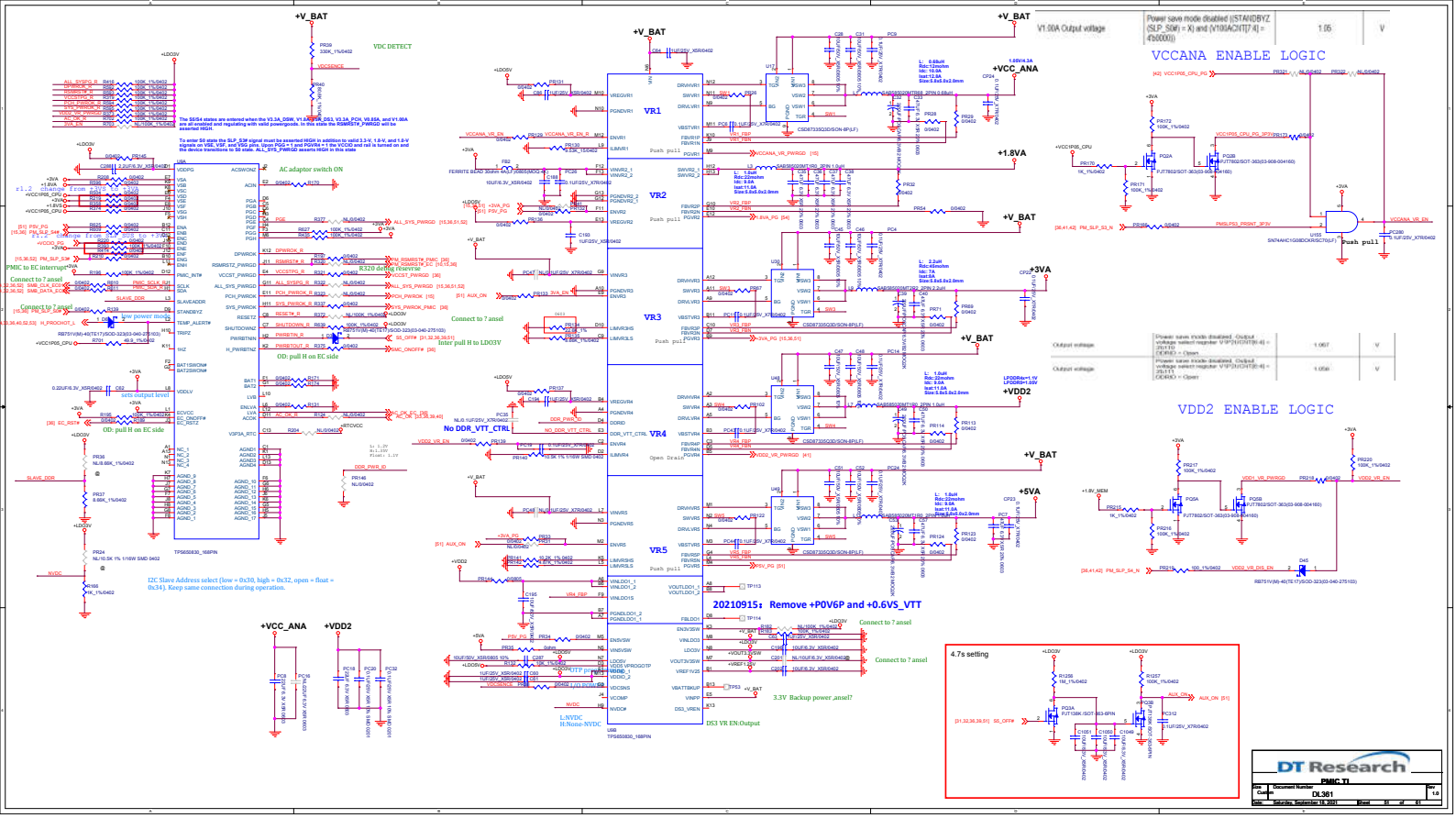
VCCANA ENABLE LOGIC
VDD2 ENABLE LOGIC
VR1
VR2
VR3
VR4
VR5
TPS650830_168PIN
20210915：Remove +P0V6P and +0.6VS_VTT
No DDR_VTT_CTRL
VDC DETECT
AC adaptor switch ON
R320 debug
Inter pull H to LDO3V
OD: pull H on EC side
Connect to 7 ansel
Low power mode
PMIC to EC interrupt
I2C Slave Address select (low = 0x30, high = 0x32, open = float = 0x34). Keep same connection during operation.
L:NVDC
H:None-NVDC
DS3 VR EN:Output
3.3V Backup power ansel?
4.7s setting
DT Research
PMIC TI
DL361
Saturday, September 18, 2021