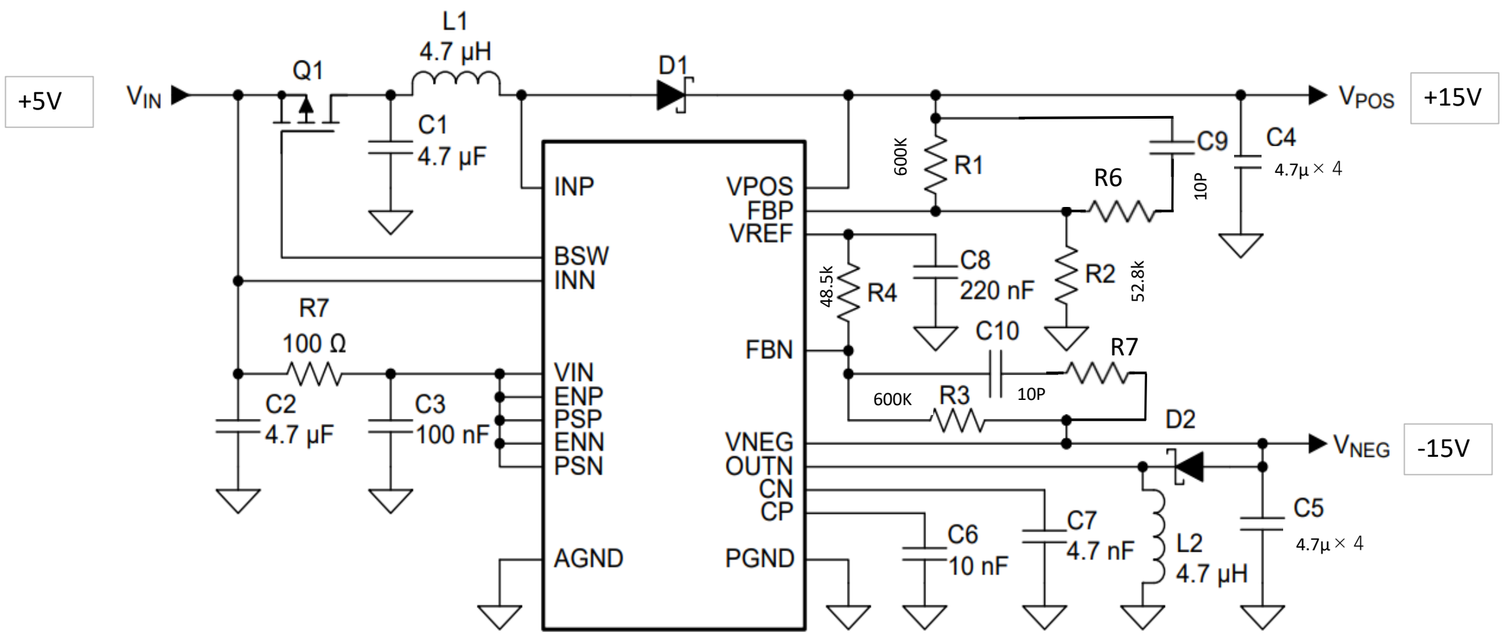
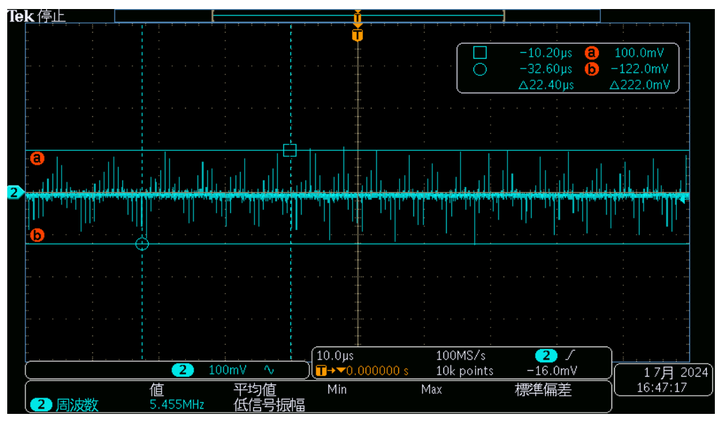
schematic



●Test 1
When R6 and R7 are 0Ω　　VPOS　　＋15V
Load　　VNEG　　－23V　　(Error output　　Waveforms are not saved.)
Positive side 100Ω
Negative side 100Ω

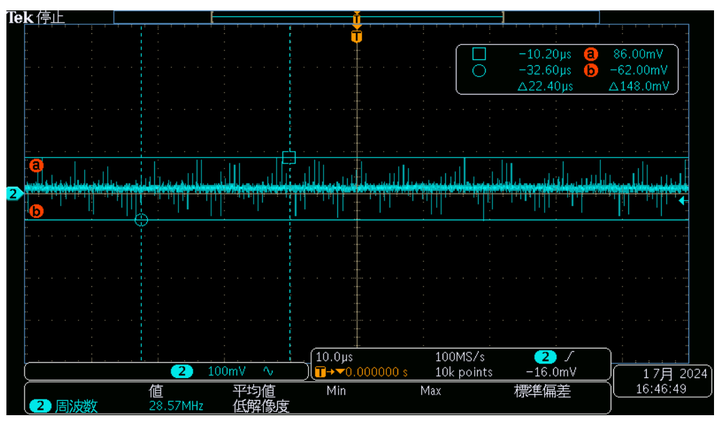
●Test 2
When R6 and R7 are 2kΩ
Load　　VNEG　　－15V　　(Ripple is small.)
Positive side 100Ω
Negative side 100Ω

Negative side: ripple waveform (AC Measurement)



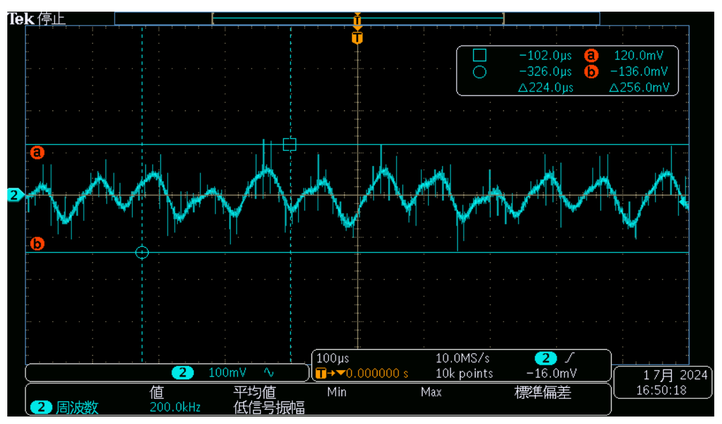
●Test 3
When R6 and R7 are 2kΩ　　VPOS　　＋15V　　(Ripple is small.)
Load
Positive side 100Ω
Negative side 100Ω

Positive side: ripple waveform (AC Measurement)



●Test 4
When R6 and R7 are 2kΩ
Load　　VNEG　　－15V　　(The output is oscillating.)
Positive side 100Ω　(Not pulse-skip)
Negative side 140Ω　(Not pulse-skip)

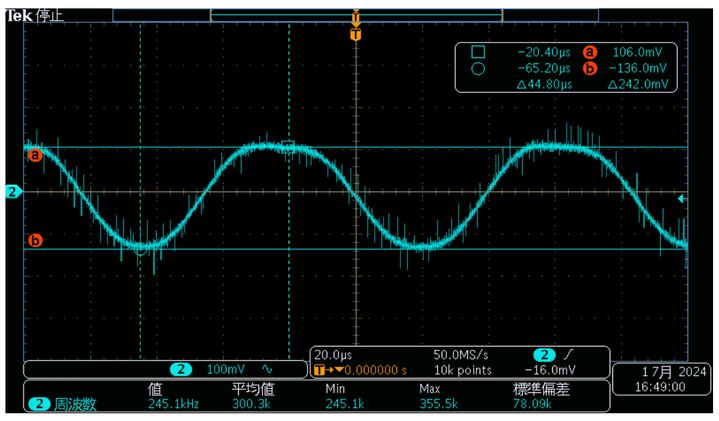
Negative side: ripple waveform (AC Measurement)



about 10kHz centered at -15V, 200mVP-P, triangular wave

●Test 5
When R6 and R7 are 2Ω　　VPOS　　＋15V　　(Large ripple due to pulse skip)
Load
Positive side 120Ω　(pulse-skip)
Negative side 100Ω　(Not pulse-skip)

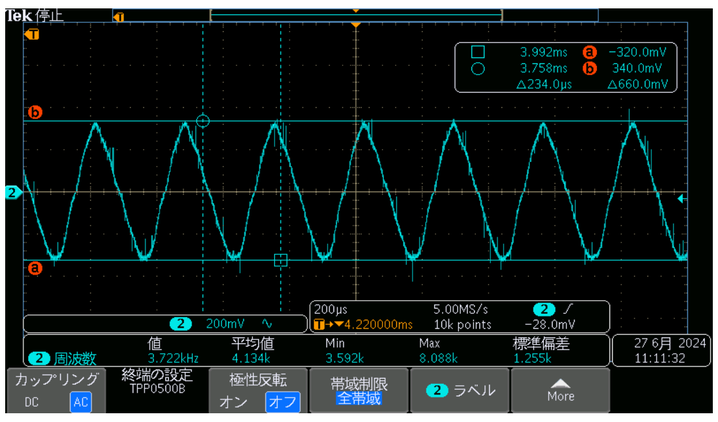
Positive side: ripple waveform (AC Measurement)



Is this waveform OK?

●Test 6
When R6 are 56KΩ
When R7 are 100KΩ
Load　　VNEG　　－15V　　(The output is oscillating.)
Positive side 100Ω　(Not pulse-skip)
Negative side 100Ω　(Not pulse-skip)

Negative side: ripple waveform (AC Measurement)



about 10kHz centered at -15V, 600mVP-P, triangular wave