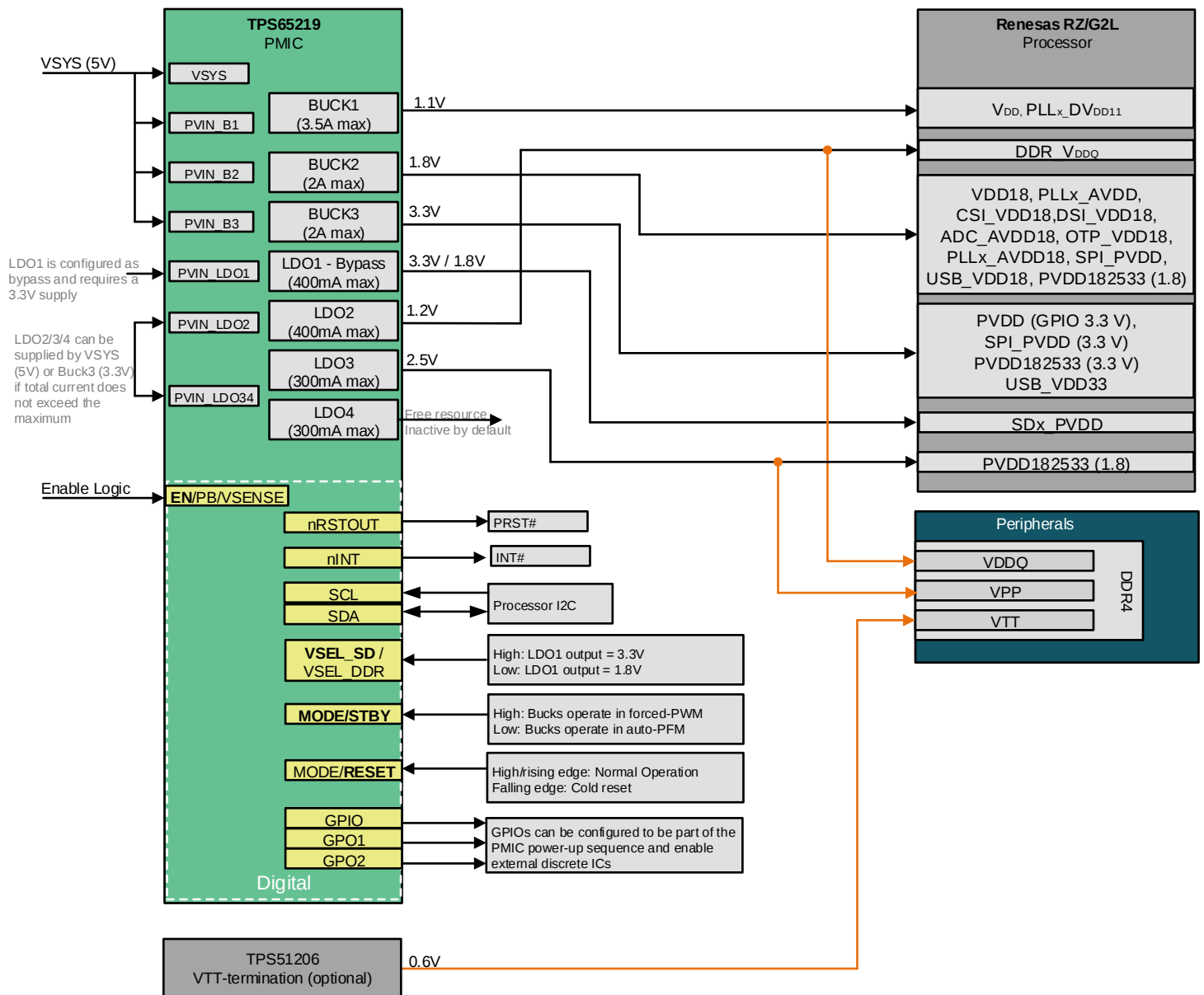
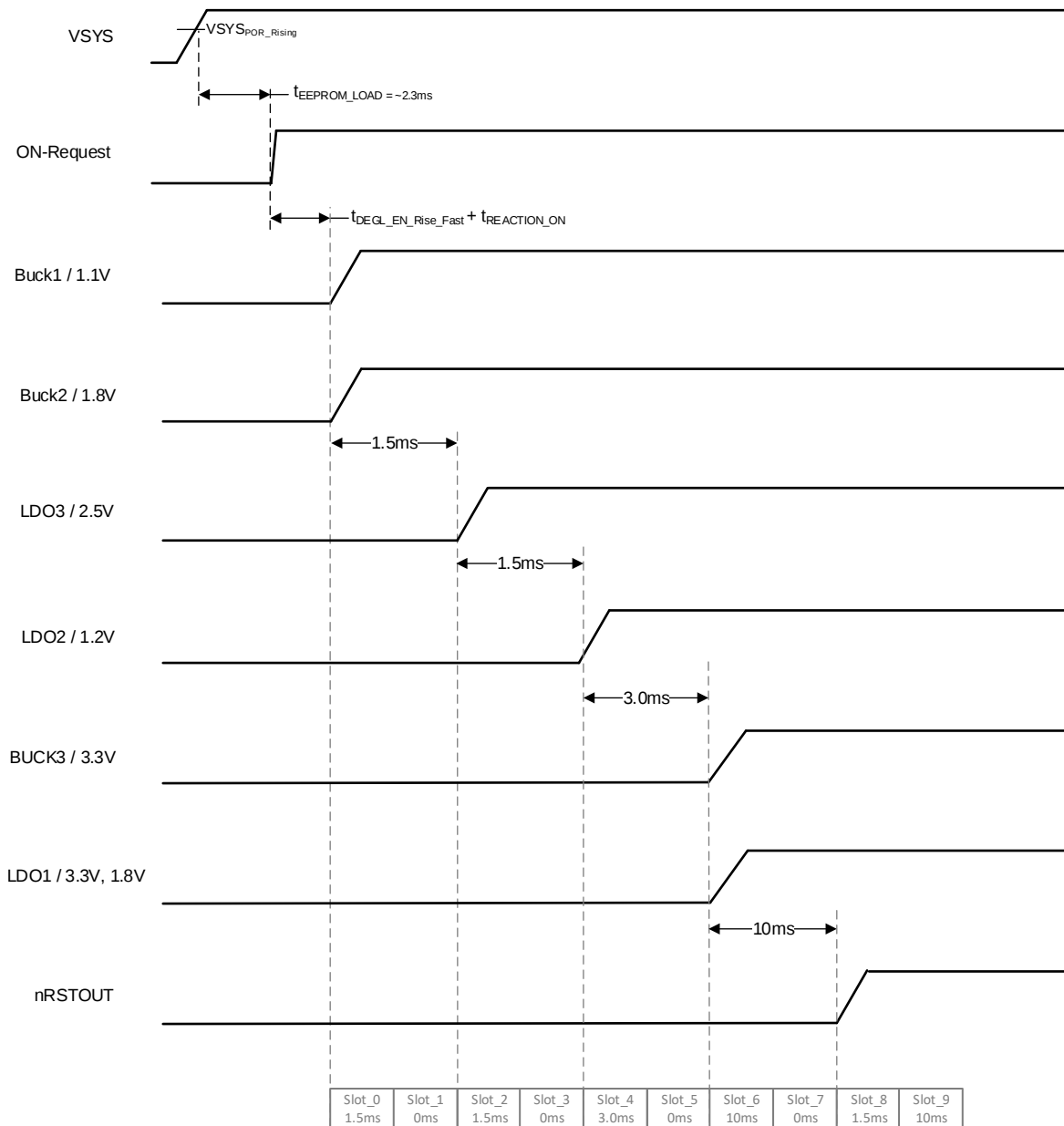




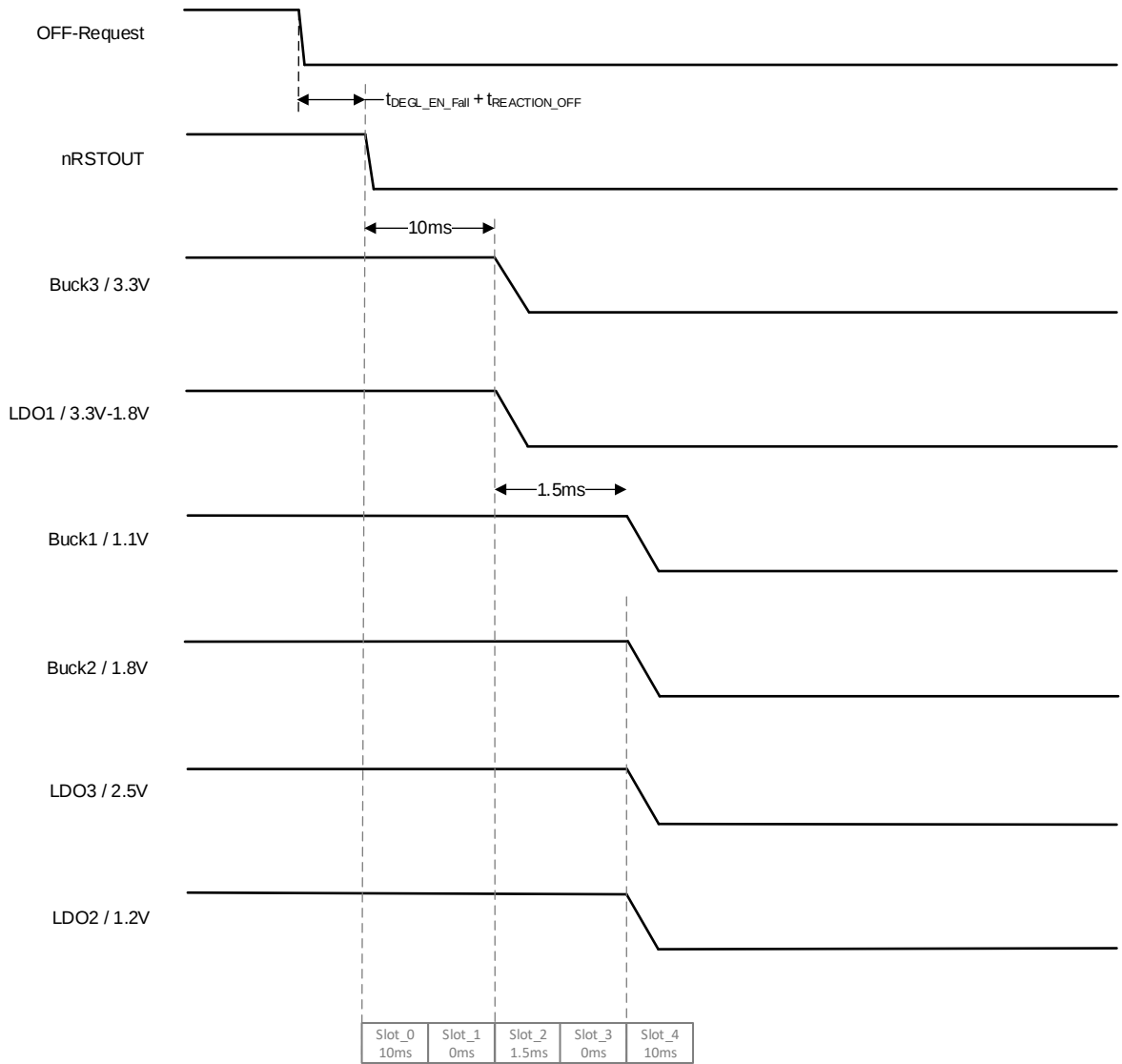
PMIC Rail

Sequence



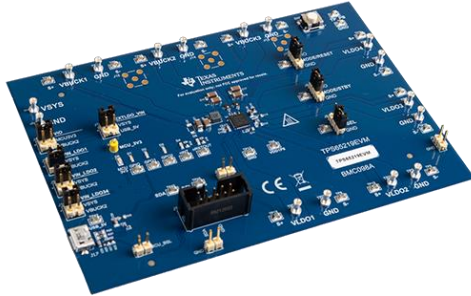
PMIC Rail

Sequence

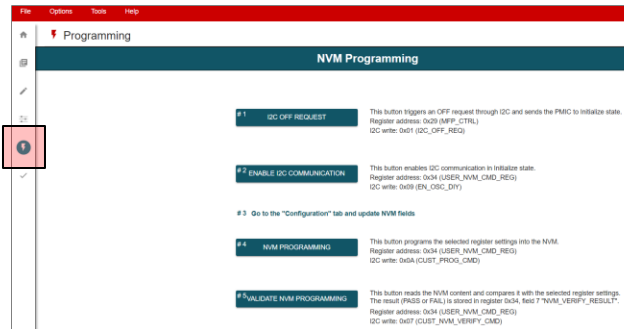


Loading NVM configuration file to TPS65219 PMIC

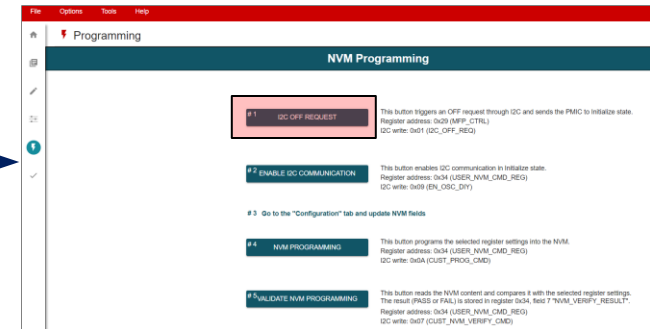
Supply VSYS on
TPS65219EVM with 5V



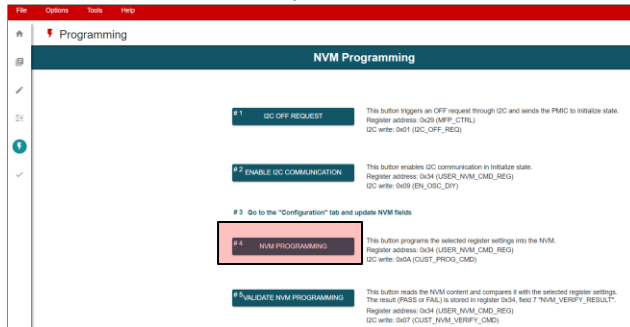
Go to the “Programming tab”



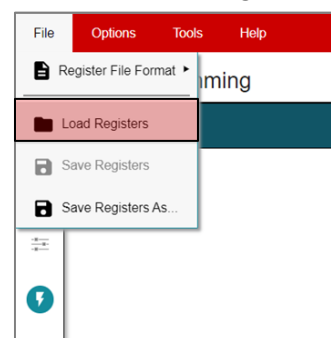
Send an I2C OFF request



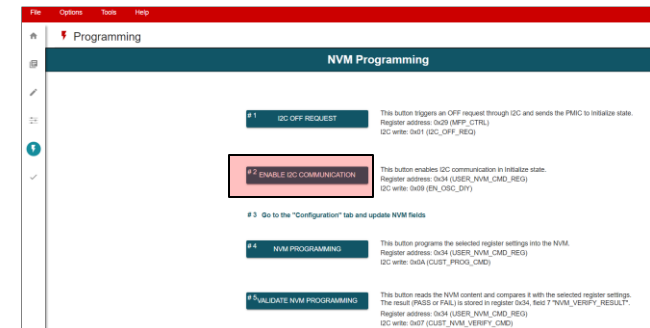
Once the file is loaded, click on “NVM PROGRAMMING” to save the register settings into the PMIC NVM memory.



Load the CSV (or JSON) file with the register settings
(File → Load Registers).



Click on “ENABLE I2C COMMUNICATION”



Optional: The “VALIDATE NVM PROGRAMMING” button is optional. It can be used to compare register settings with content saved in the NVM memory.

Perform a power cycle on VSYS and validate new NVM settings (TI recommend collecting at the minimum data for output voltages and power-up/power-down waveforms).