

# TPS65262's LDOs delay and BUCK external delay

## 1. BOM change from EVM

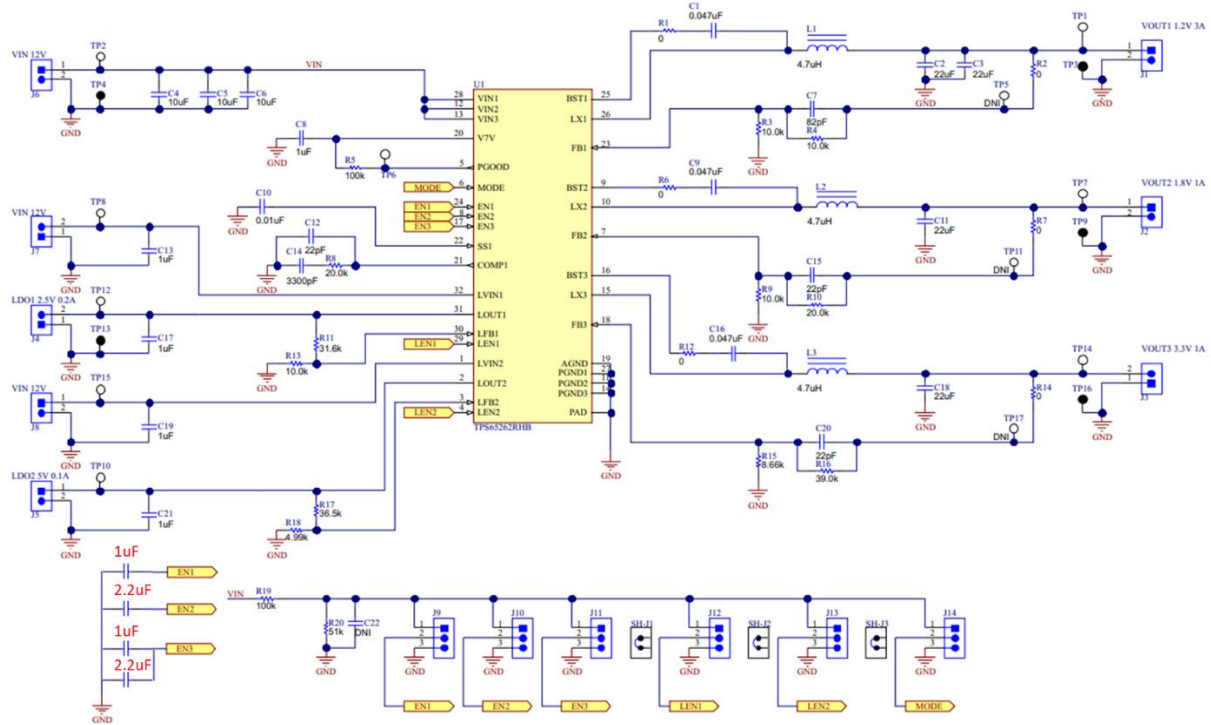


Figure 1. TPS65262 Schematic

## 2. Test method

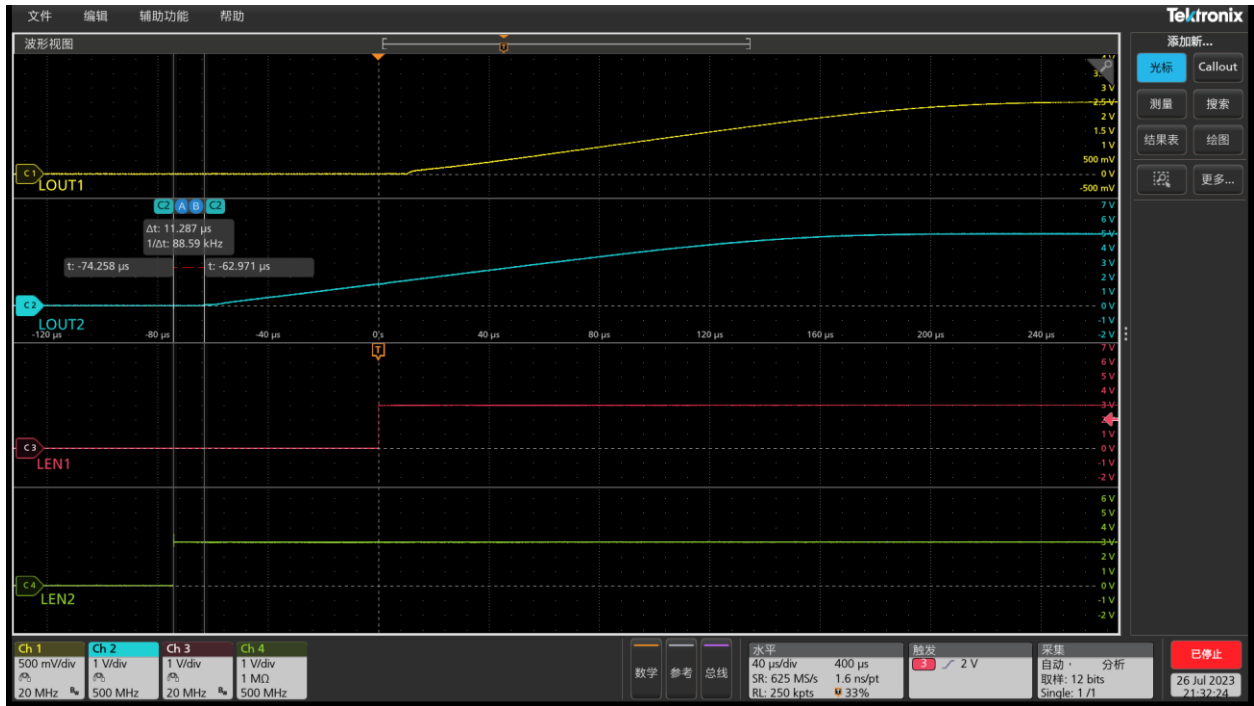
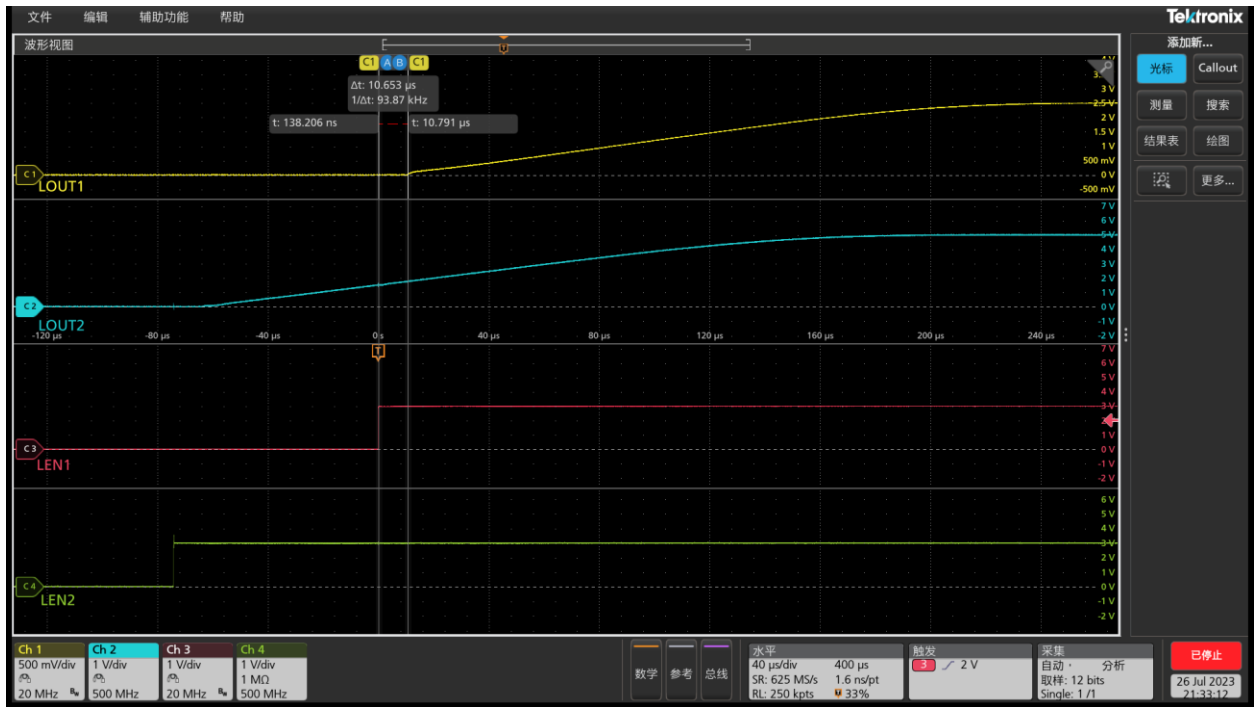
LEN1 and LEN2 are triggered by signal generator in square mode High=3V, Low=0V.  
Mode is connected to ground

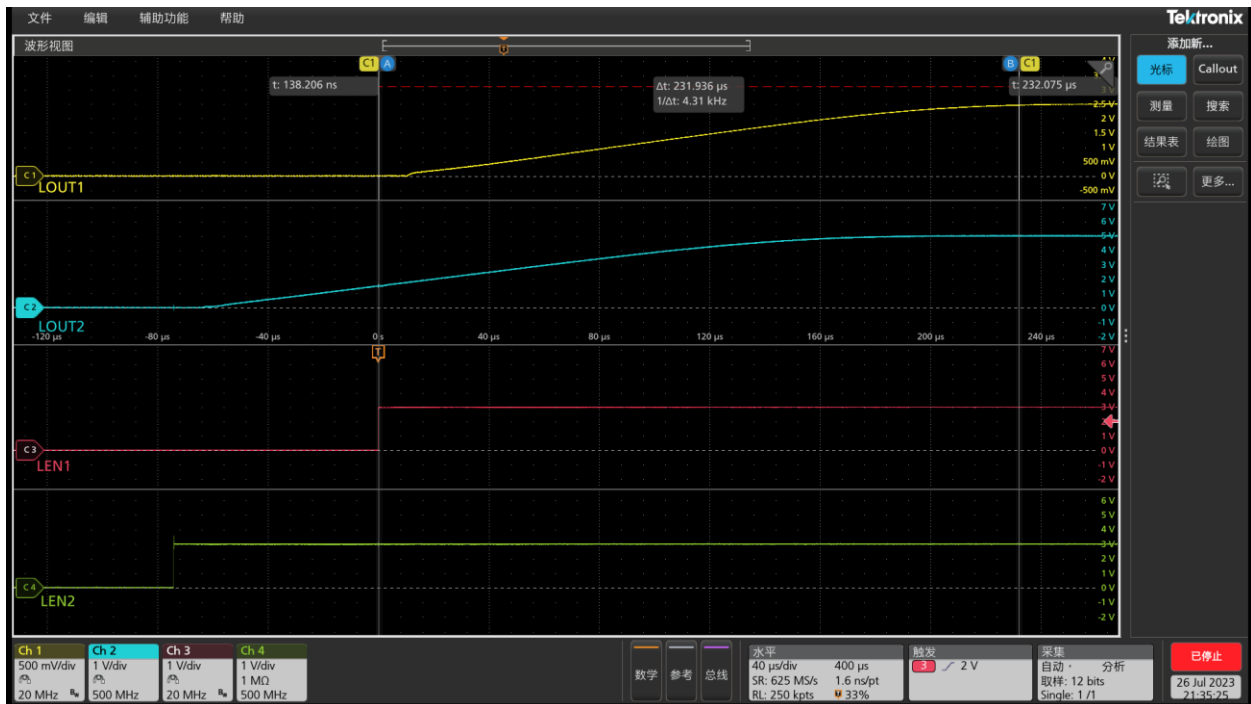
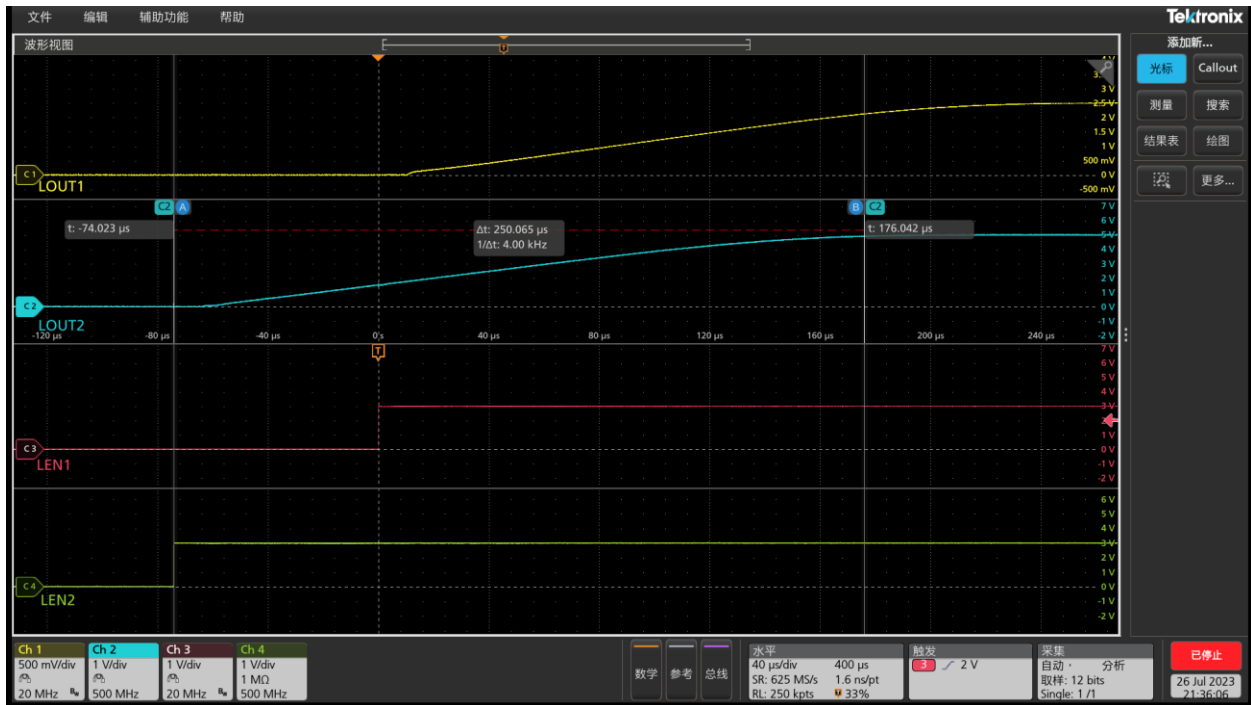
## 3. Test results

### 3.1. LDOs delay

Delay from LEN1 to LOUT1 is 10us

Delay from LEN2 to LOUT2 is 11us





### 3.2. BUCK delay

As I connect Mode to ground and attach CEN1=1μF, CEN2=2.2μF and CEN3=3.2μF, start delay of

VOUT1 should be  $CEN1 \cdot (0.4V/1.4\mu A + 0.8V/3.6\mu A) = 1 \cdot (0.4/1.4 + 0.8/3.6) = 0.5s$ ;

VOUT2 delay should be  $0.5 \cdot 2.2s$ .

VOUT3 delay should be  $0.5 \cdot 3.2s$ .

The VOUT1 delay on EVM is 330ms

The VOUT2 delay on EVM is 608ms

The VOUT3 delay on EVM is 926ms

The difference between calculated delay and actual delay is caused by the difference of Mode pull-up current.

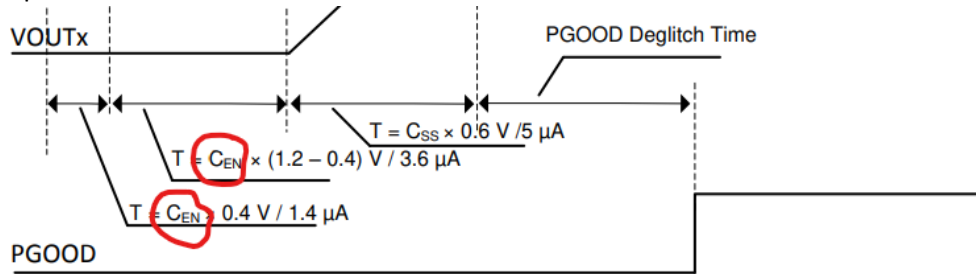


Figure 8-3. Startup Power Sequence



