TPS65931211-Q1 NVM history

NVM Rev	Changes	Impact
1	First revision	
2	Suspend To RAM power-down sequence updated to mask NSLEEP2 at the beginning of the sequence and unmask it at the end.	• PMIC to ignore changes to GPIO3 until sequence is complete • **No HW or SW Changes required**
3	Internal settings of Buck convertor updated to improve performance	• **No HW or SW Changes required**
4	GPIO6 Input to set the voltage on Buck123 with the following polarity: GPIO6 LOW → BUCK123 = 0.75V GPIO6 FLOATING → BUCK123 = 0.75V GPIO6 HIGH → BUCK123 = 0.85V	• **GPIO6 is now an input**
5	VCCA_PG_SET = 0x1	• PMIC can accept 3.3V or 5V VCCA input • VCCA_OVP will trigger at ~5.7V

TEXAS INSTRUMENTS