

A

B

C

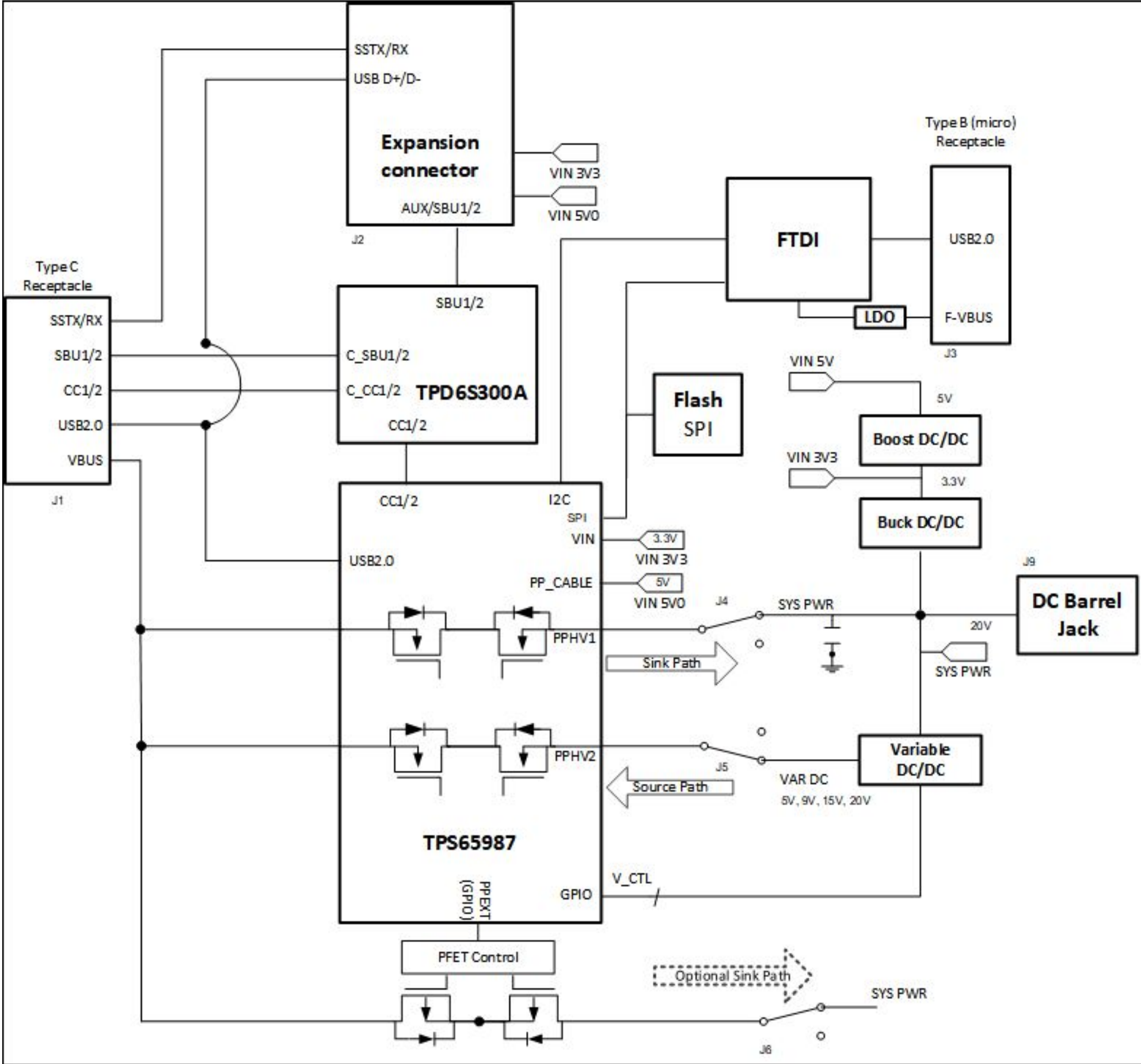
D

A

B

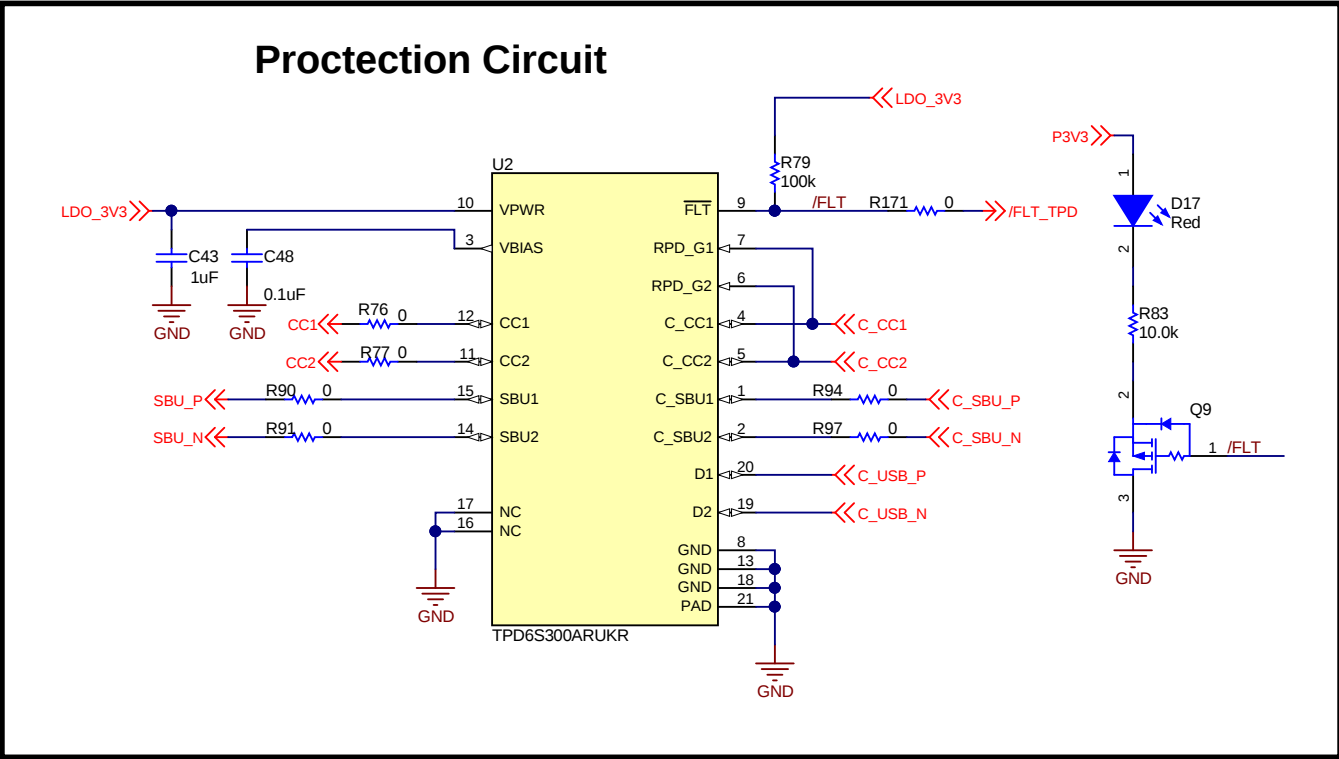
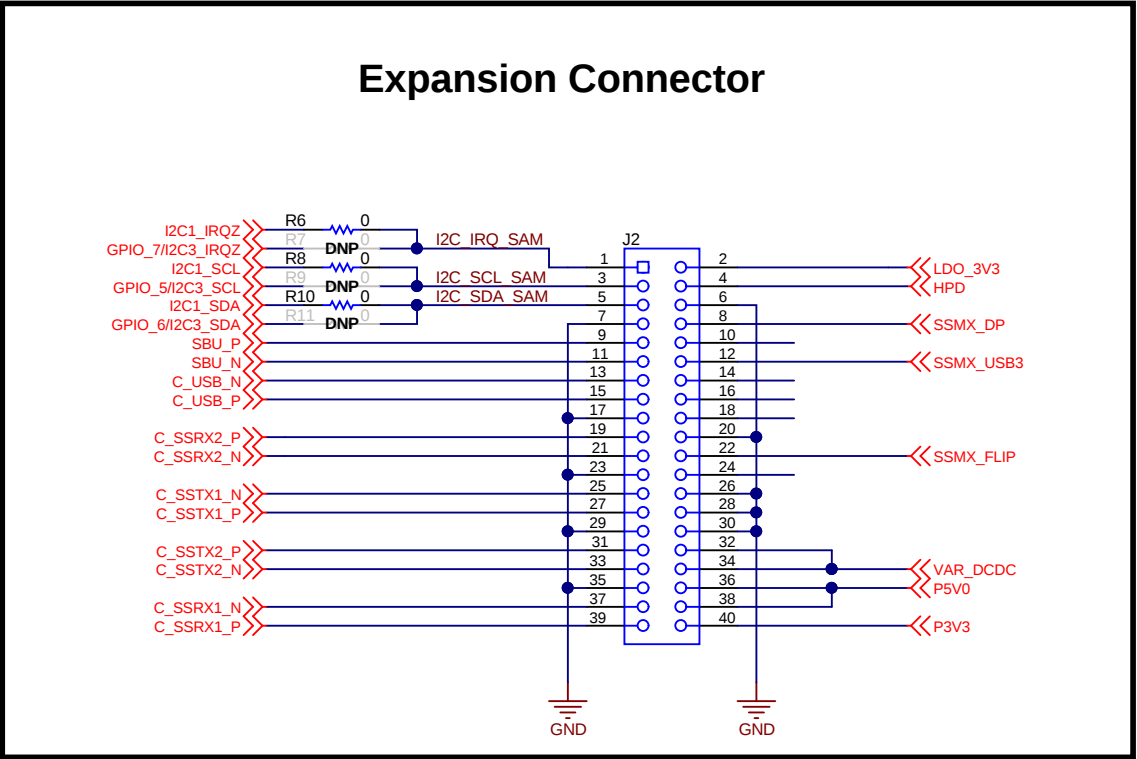
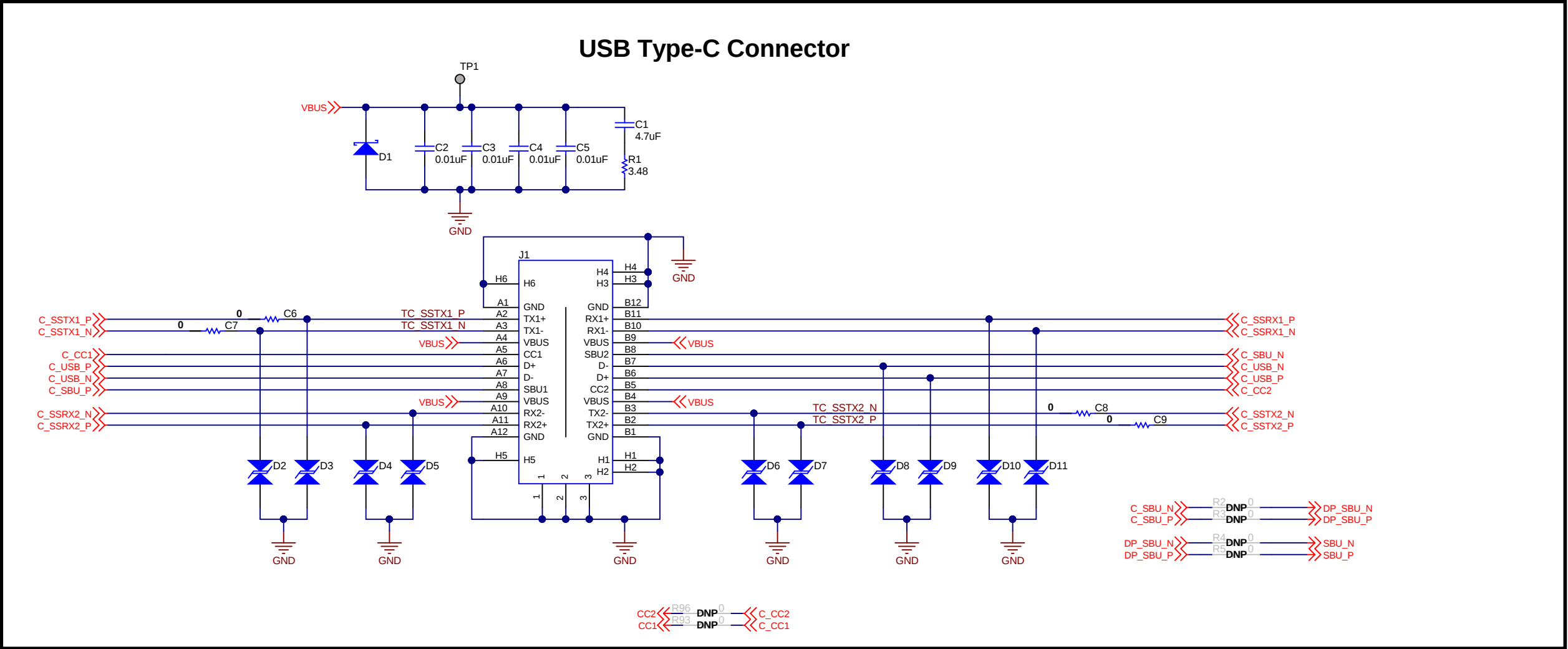
C

D



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|                                                 |                                                    |                                               |
|-------------------------------------------------|----------------------------------------------------|-----------------------------------------------|
| Orderable: <a href="#">TPS65987EVM</a>          | Designed for: <a href="#">Public Release</a>       | Mod. Date: <a href="#">10/15/2018</a>         |
| TID #: <a href="#">N/A</a>                      | Project Title: <a href="#">TPS65987EVM</a>         |                                               |
| Number: <a href="#">PSIL060</a>                 | Rev: <a href="#">A</a>                             | Sheet Title: <a href="#">Cover Sheet</a>      |
| SVN Rev: <a href="#">Not in version control</a> | Assembly Variant: <a href="#">001</a>              | Sheet: <a href="#">1</a> of <a href="#">9</a> |
| Drawn By: <a href="#">Ghouse M Mohiuddin</a>    | File: <a href="#">PSIL060A_CoverSheet_SchDoc</a>   | Size: <a href="#">B</a>                       |
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| TID #: <a href="#">N/A</a>                      | Project Title: <a href="#">TPS65987EVM</a>         |                                               |
| Number: <a href="#">PSIL060</a>                 | Rev: <a href="#">A</a>                             | Sheet Title: <a href="#">Connectors</a>       |
| SVN Rev: <a href="#">Not in version control</a> | Assembly Variant: <a href="#">001</a>              | Sheet: <a href="#">2</a> of <a href="#">9</a> |
| Drawn By: <a href="#">Ghouse M Mohiuddin</a>    | File: <a href="#">PSIL060A_Connectors.SchDoc</a>   | Size: <a href="#">B</a>                       |
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A

B

C

D

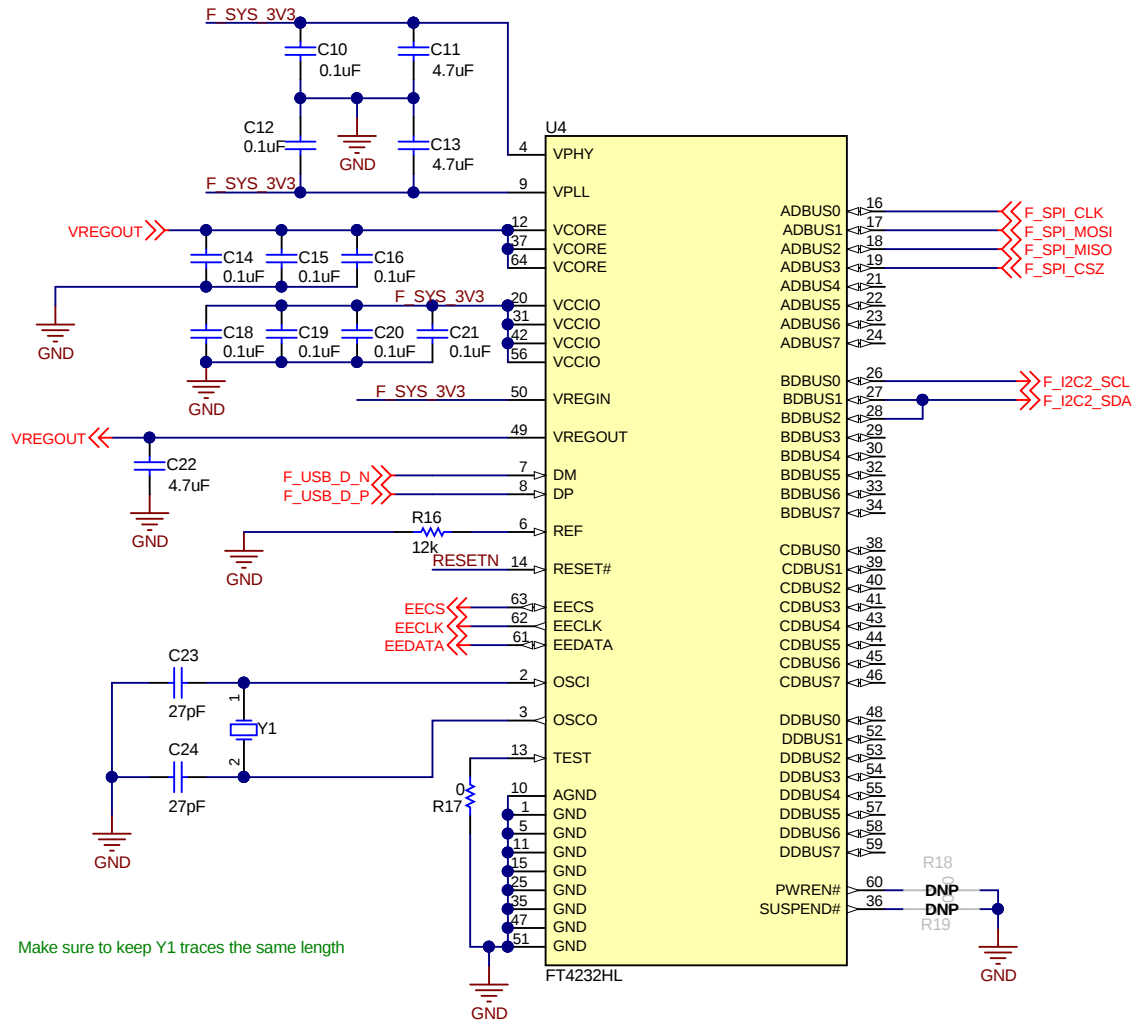
A

B

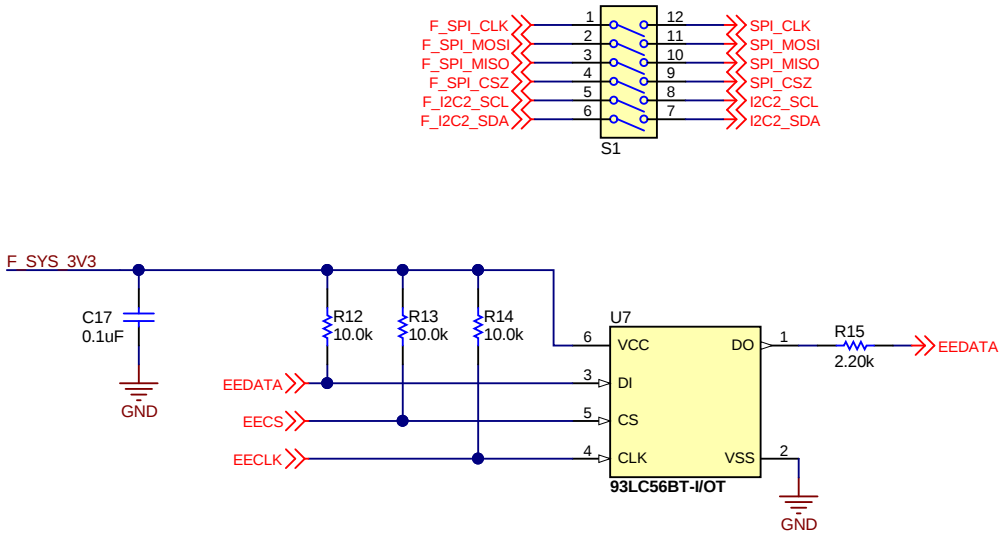
C

D

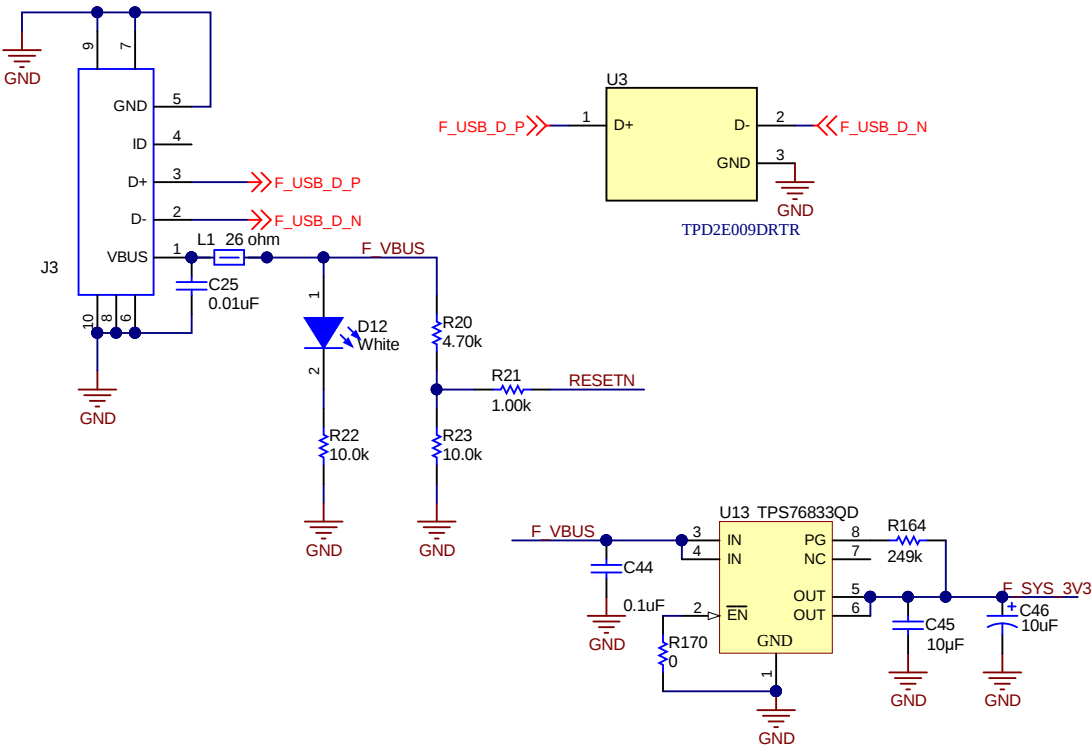
FTDI Chip



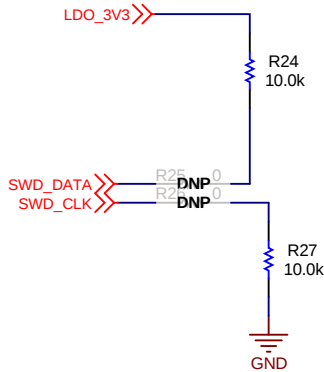
FTDI Switches and Memory



Micro-B Connector



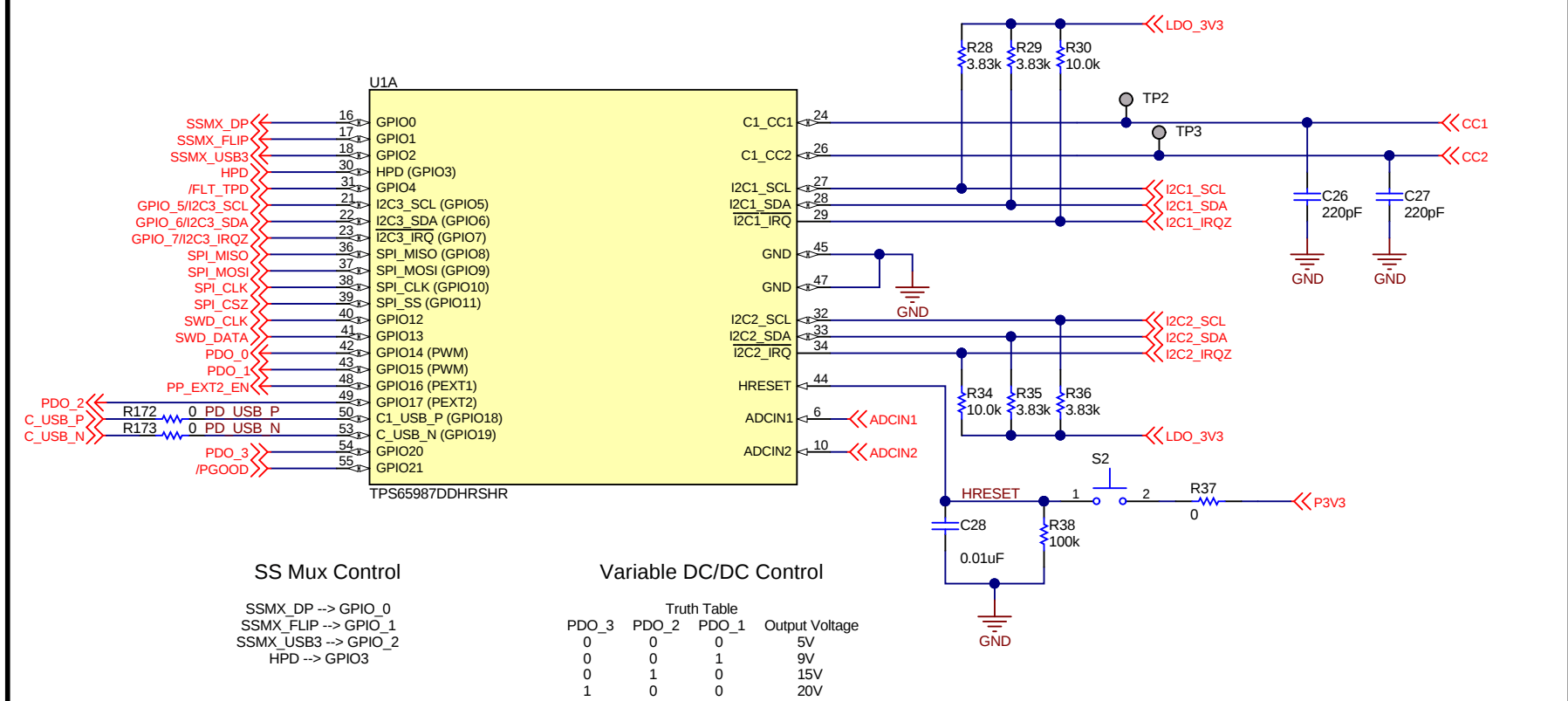
ULINK2 Debugger



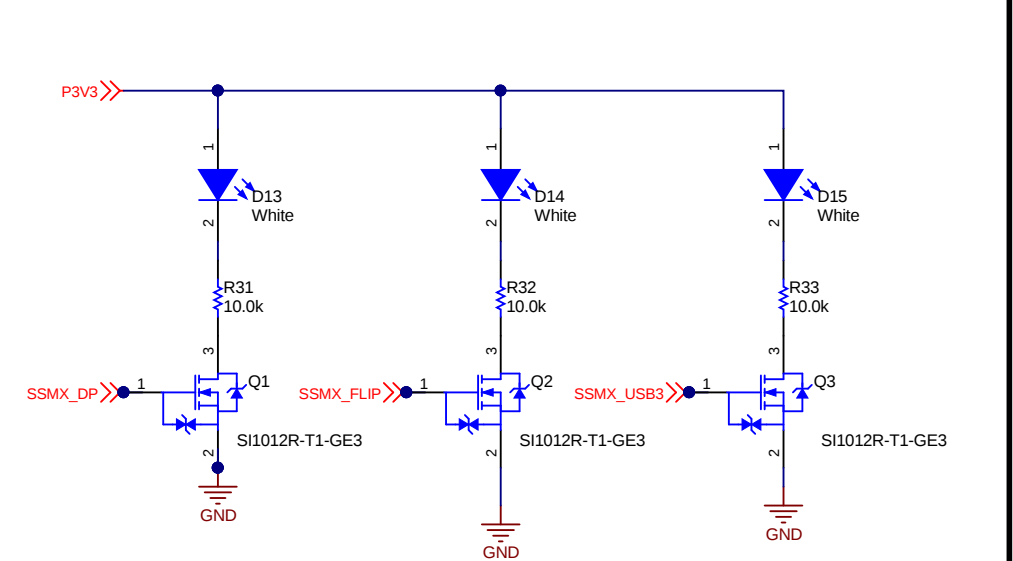
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|                                 |                                                                            |                      |
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| TID #: N/A                      | Project Title: TPS65987EVM                                                 |                      |
| Number: PSIL060                 | Rev: A                                                                     | Sheet Title: FTDI    |
| SVN Rev: Not in version control | Assembly Variant: 001                                                      | Sheet: 3 of 9        |
| Drawn By: Ghouse M Mohiuddin    | File: PSIL060A_FTDI_SchDoc                                                 | Size: B              |
| Engineer: Ghouse M Mohiuddin    | Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a> |                      |

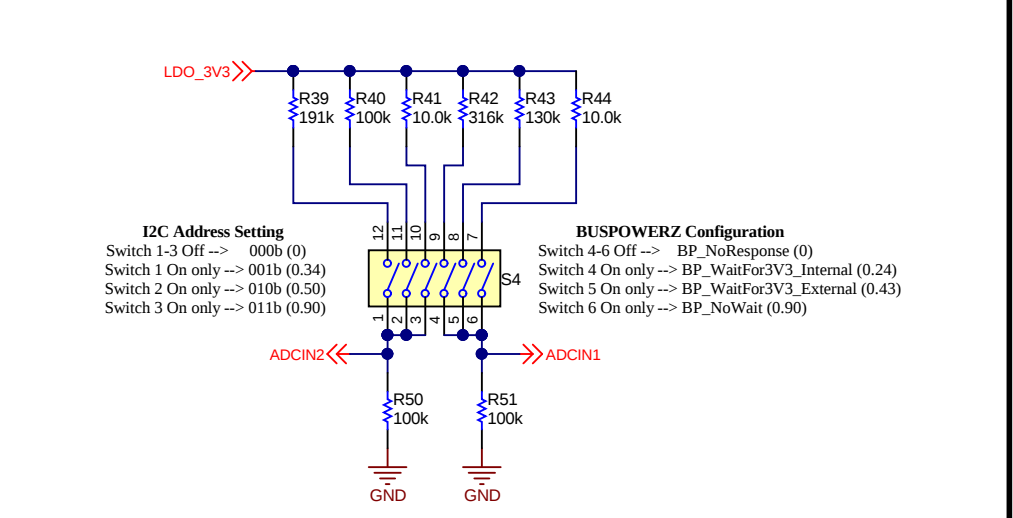
USB PD Controller



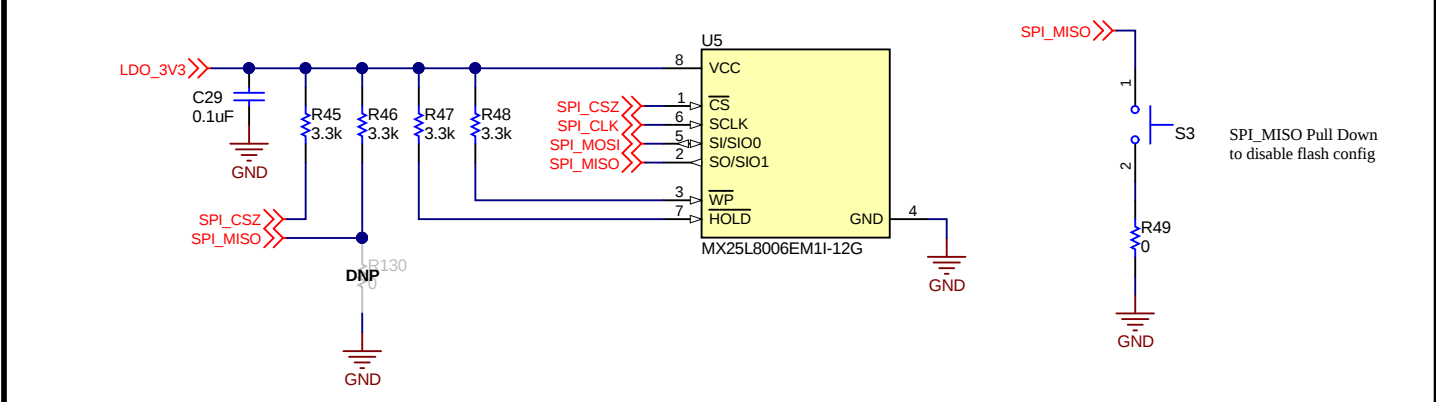
SS Mux Control LED's

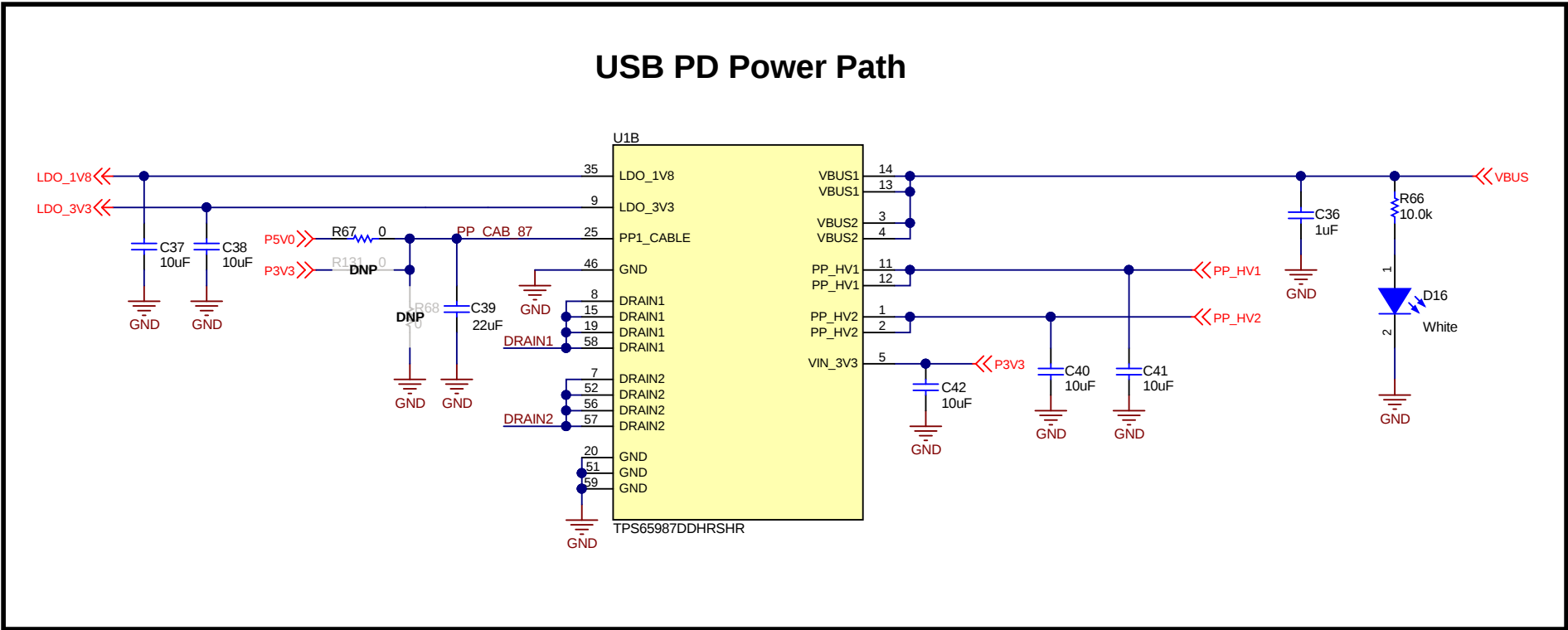
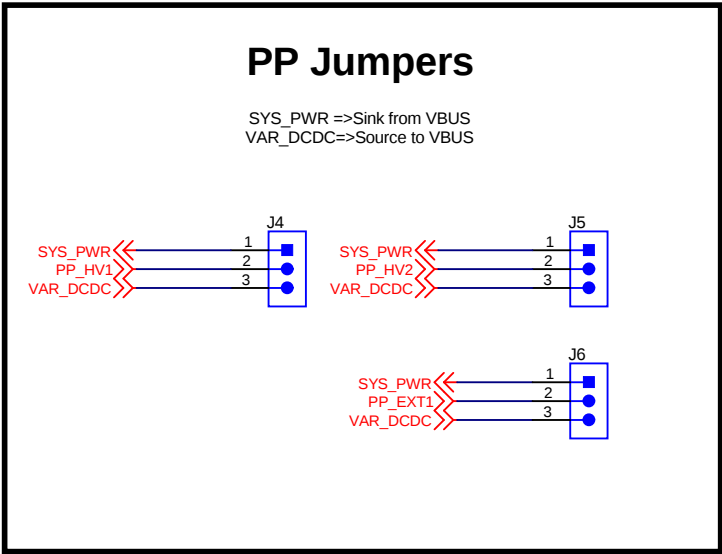
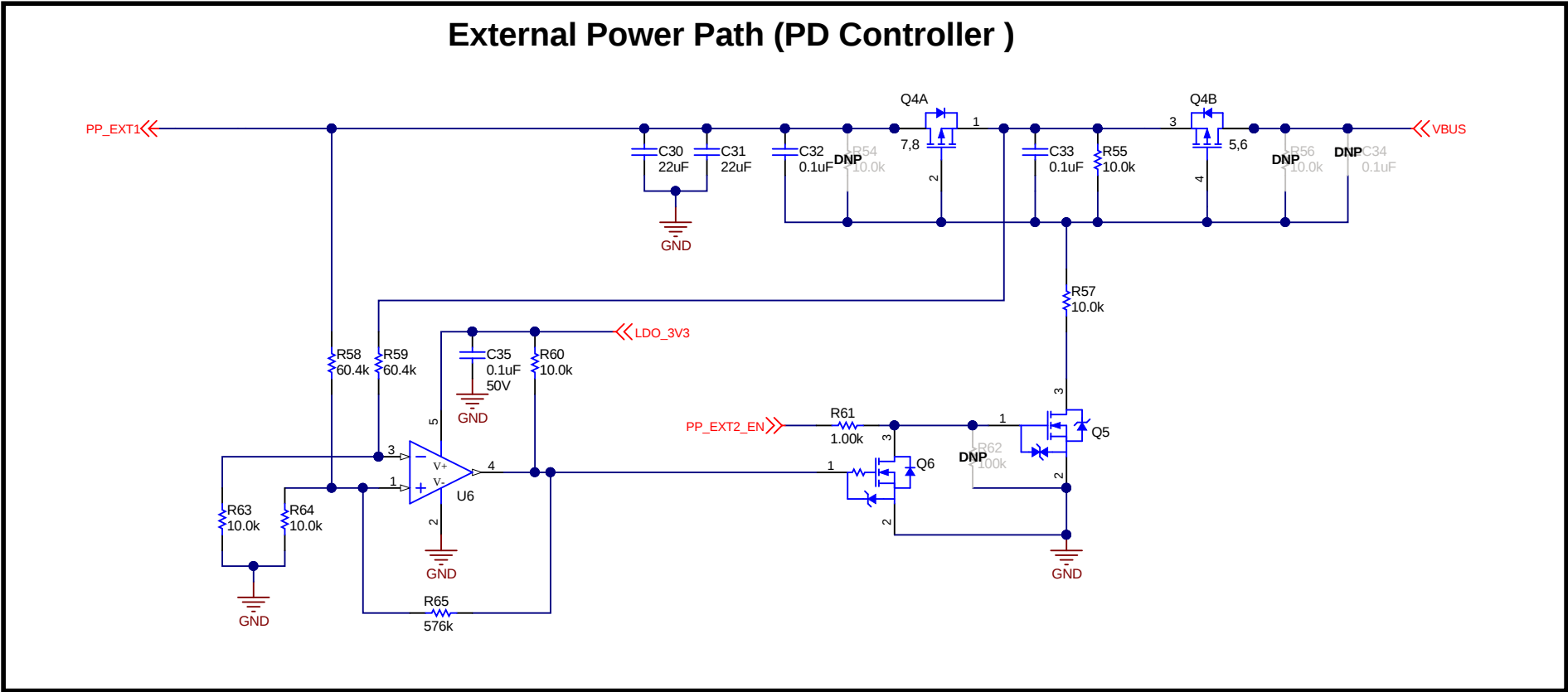


ADCIN1:2 Resistor Divider Setting



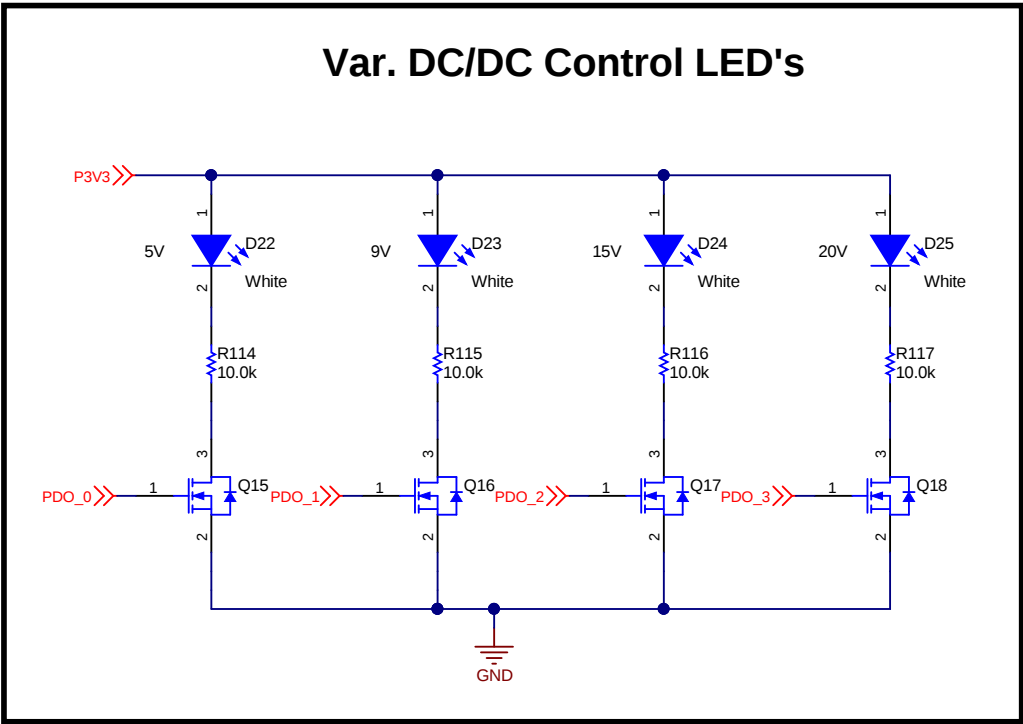
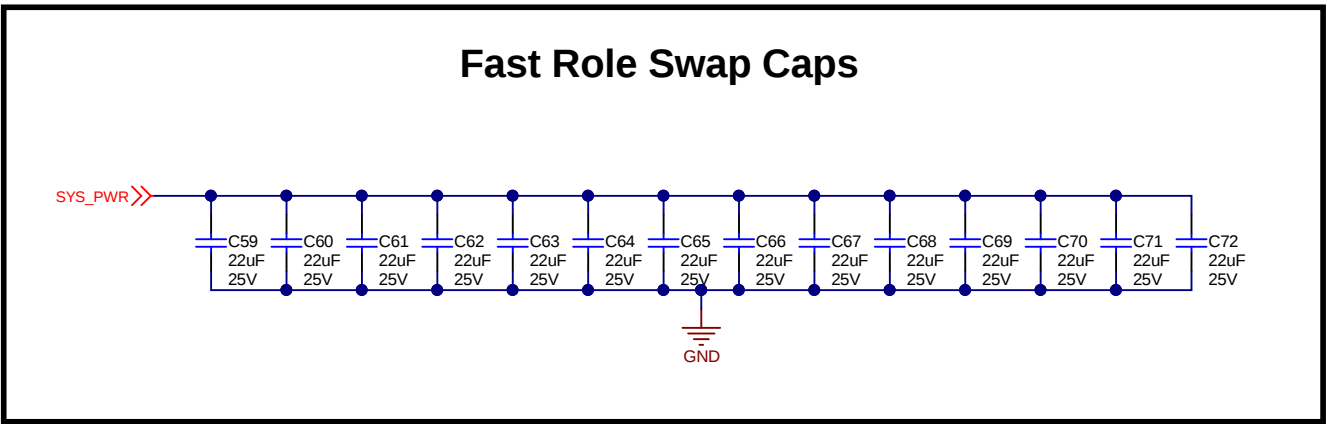
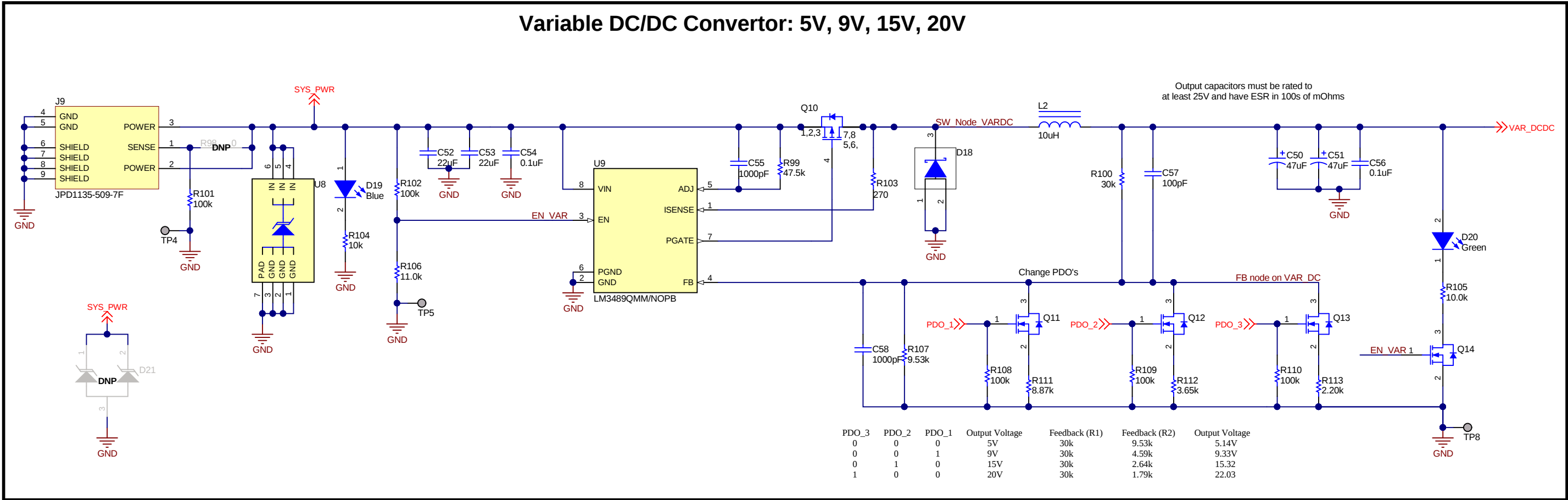
EEPROM



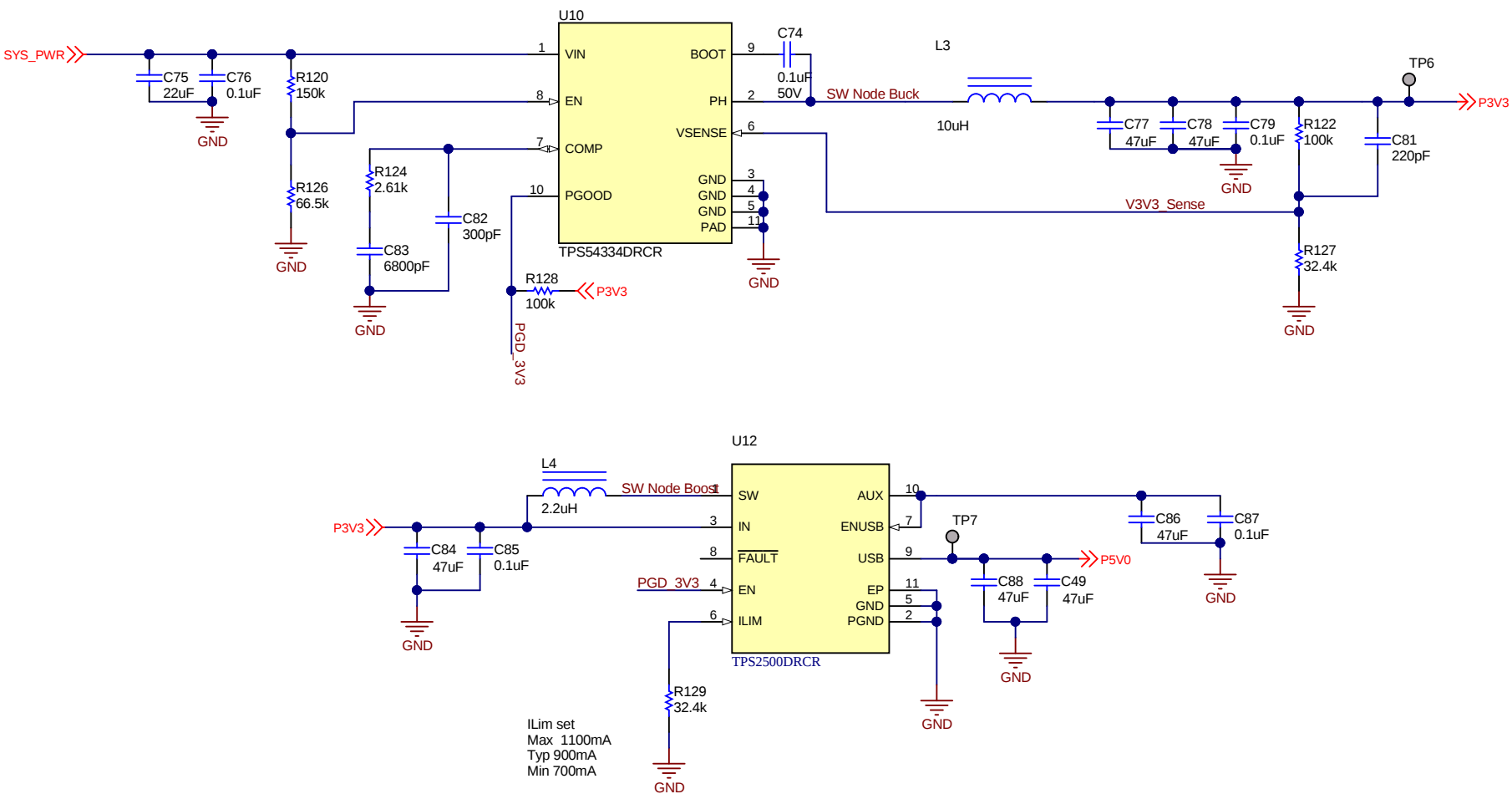


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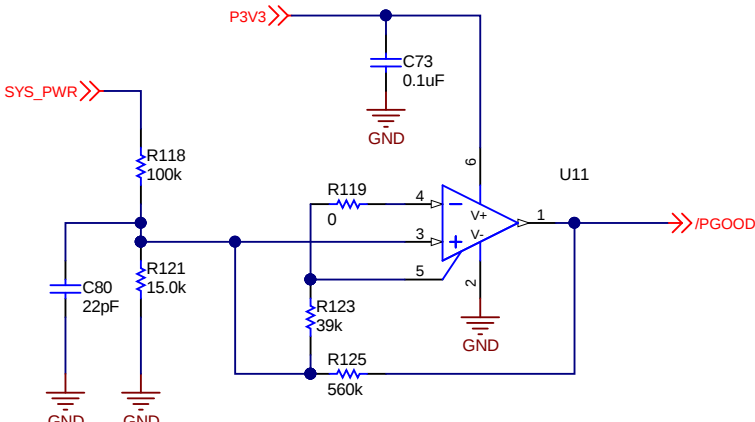
|                                                 |                                                     |                                               |                                                                                                                                                                                                        |
|-------------------------------------------------|-----------------------------------------------------|-----------------------------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
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| Number: <a href="#">PSIL060</a>                 | Rev: <a href="#">A</a>                              | Sheet Title: <a href="#">PD Power Path</a>    |                                                                                                                                                                                                        |
| SVN Rev: <a href="#">Not in version control</a> | Assembly Variant: <a href="#">001</a>               | Sheet: <a href="#">5</a> of <a href="#">9</a> |                                                                                                                                                                                                        |
| Drawn By: <a href="#">Ghouse M Mohiuddin</a>    | File: <a href="#">PSIL060A_PD Power Path.SchDoc</a> | Size: <a href="#">B</a>                       |                                                                                                                                                                                                        |
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**Buck Converter: SYS\_PWR => 3.3V  
&  
Boost Converter: 3.3V => 5V**



**Barrel Jack Detection**



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| Orderable: <a href="#">TPS65987EVM</a>          | Designed for: <a href="#">Public Release</a>        | Mod. Date: <a href="#">12/16/2018</a>         | <br>TEXAS<br>INSTRUMENTS<br><a href="http://www.ti.com">http://www.ti.com</a><br>© Texas Instruments 2018 |
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| Number: <a href="#">PSIL060</a>                 | Rev: <a href="#">A</a>                              | Sheet Title: <a href="#">Power_Supply2</a>    |                                                                                                                                                                                                |
| SVN Rev: <a href="#">Not in version control</a> | Assembly Variant: <a href="#">001</a>               | Sheet: <a href="#">8</a> of <a href="#">9</a> |                                                                                                                                                                                                |
| Drawn By: <a href="#">Ghouse M Mohiuddin</a>    | File: <a href="#">PSIL060A_Power_Supply2.SchDoc</a> | Size: <a href="#">B</a>                       |                                                                                                                                                                                                |
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