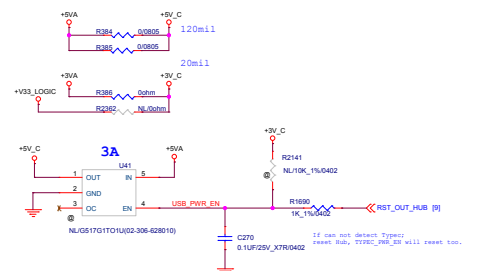
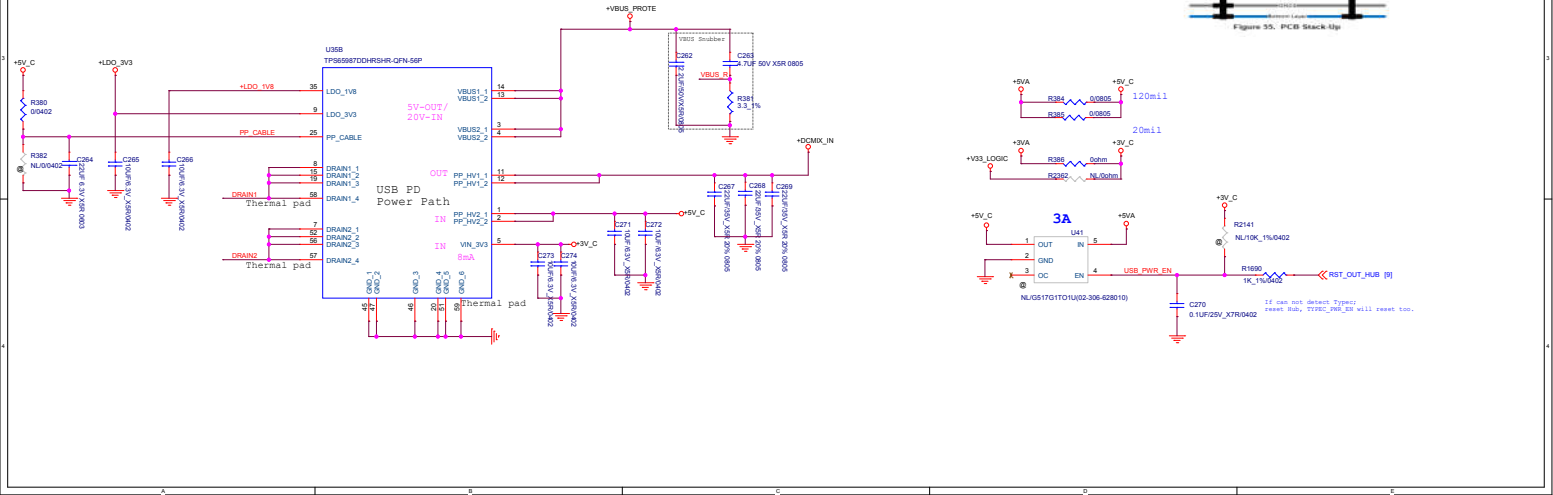
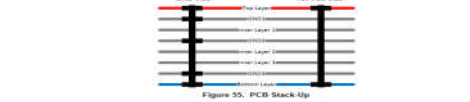


Figure 50. Top Layer FET Pads
Figure 51. Bottom Layer FET Pads



0 = Tie 1k to GND
 R = Tie 20K to GND
 F = Float (leave pin open)
 1 = Tie 1k to VCC

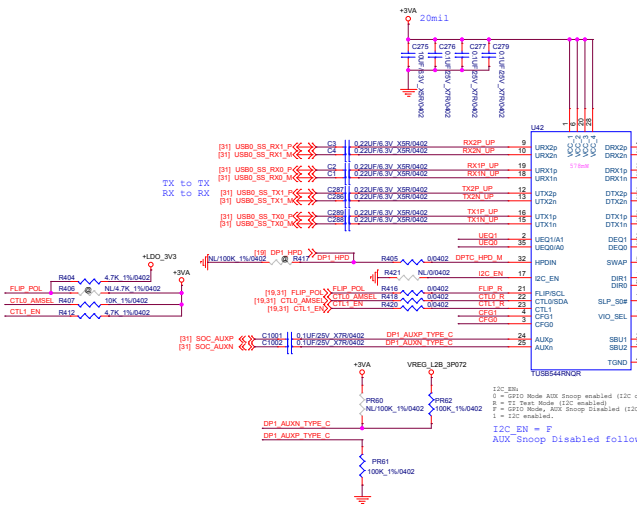


Table 3: GPIO ALIxP or ALIxN to SBU1 or SBU2 Mapping

CTL1 Pin	FLP Pin	MAPPING
H	L	ALiN - SBU1
H	H	ALiN - SBU2
L	H	ALiN - SBU1
L	L	Open

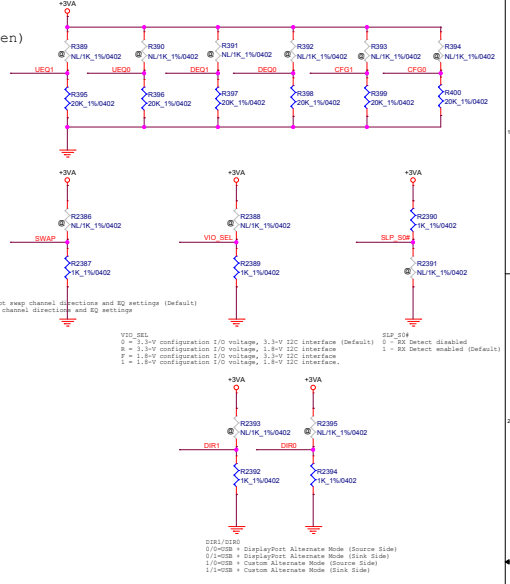


Table 7: TUSB546 Receiver Equalization GPIO Control

Receiver Equalization	GPIO	Level	IO Mode	IO Voltage	IO Current	IO Drive	IO Delay	IO Slew	IO Pull-up	IO Pull-down
0	0	0	0	0	0	0	0	0	0	0
1	1	1	1	1	1	1	1	1	1	1

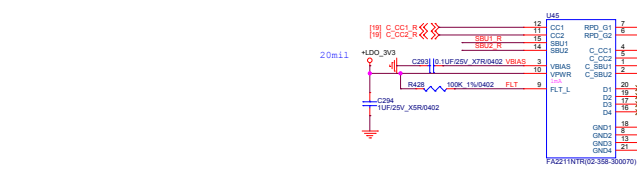


Table 8: VDD Linear Range and DC Gain

Setting #	CFD pin level	CFD pin level	Overvoltage DC Gain (dB)	Overvoltage DC Gain (dB)	Overvoltage VDD Linear Range (mV)	Overvoltage VDD Linear Range (mV)
1	0	0	0	0	0	0
2	0	0	0	0	0	0
3	0	0	0	0	0	0
4	0	0	0	0	0	0
5	0	0	0	0	0	0
6	0	0	0	0	0	0
7	0	0	0	0	0	0
8	0	0	0	0	0	0
9	0	0	0	0	0	0
10	0	0	0	0	0	0
11	0	0	0	0	0	0
12	0	0	0	0	0	0
13	0	0	0	0	0	0
14	0	0	0	0	0	0
15	0	0	0	0	0	0
16	0	0	0	0	0	0

Table 7: TUSB546 Receiver Equalization GPIO Control (continued)

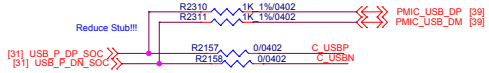
Receiver Equalization	GPIO	Level	IO Mode	IO Voltage	IO Current	IO Drive	IO Delay	IO Slew	IO Pull-up	IO Pull-down
17	0	0	0	0	0	0	0	0	0	0
18	0	0	0	0	0	0	0	0	0	0
19	0	0	0	0	0	0	0	0	0	0
20	0	0	0	0	0	0	0	0	0	0
21	0	0	0	0	0	0	0	0	0	0
22	0	0	0	0	0	0	0	0	0	0
23	0	0	0	0	0	0	0	0	0	0
24	0	0	0	0	0	0	0	0	0	0
25	0	0	0	0	0	0	0	0	0	0
26	0	0	0	0	0	0	0	0	0	0



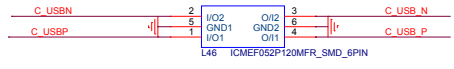
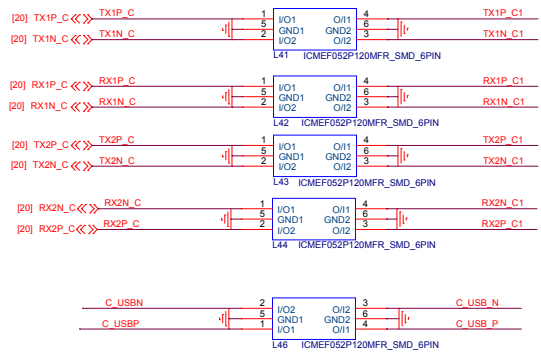
TUSB546

SLUUC047C (Rev. 0)

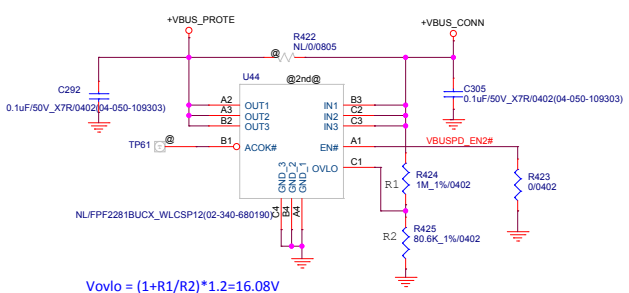
www.ti.com



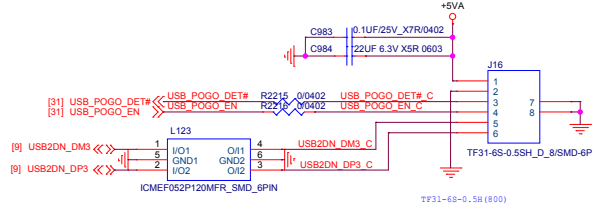
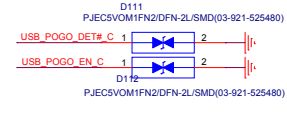
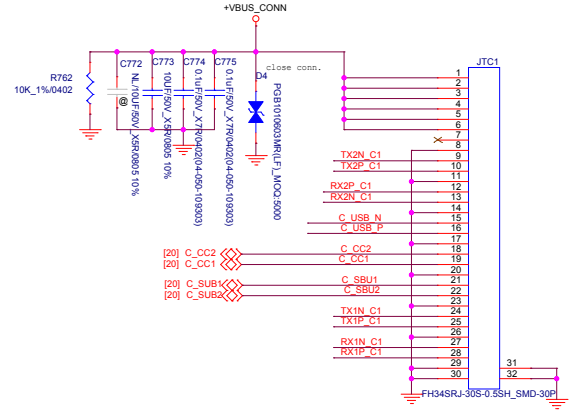
USB diff: 90 ohm +/- 10%



U44 @1ST@



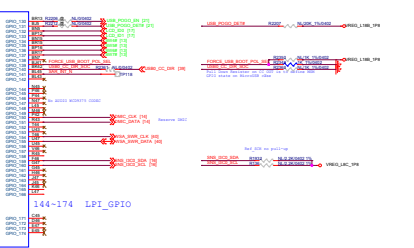
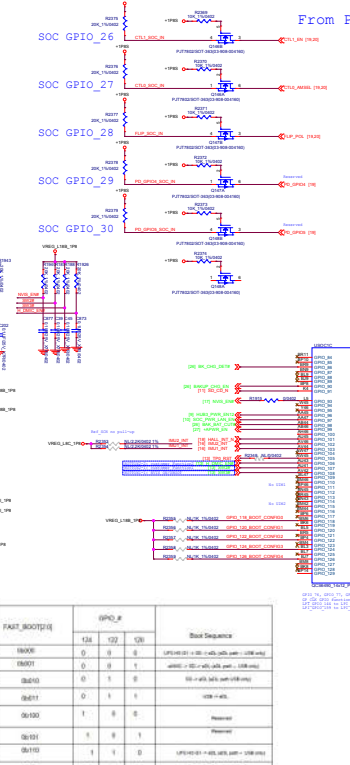
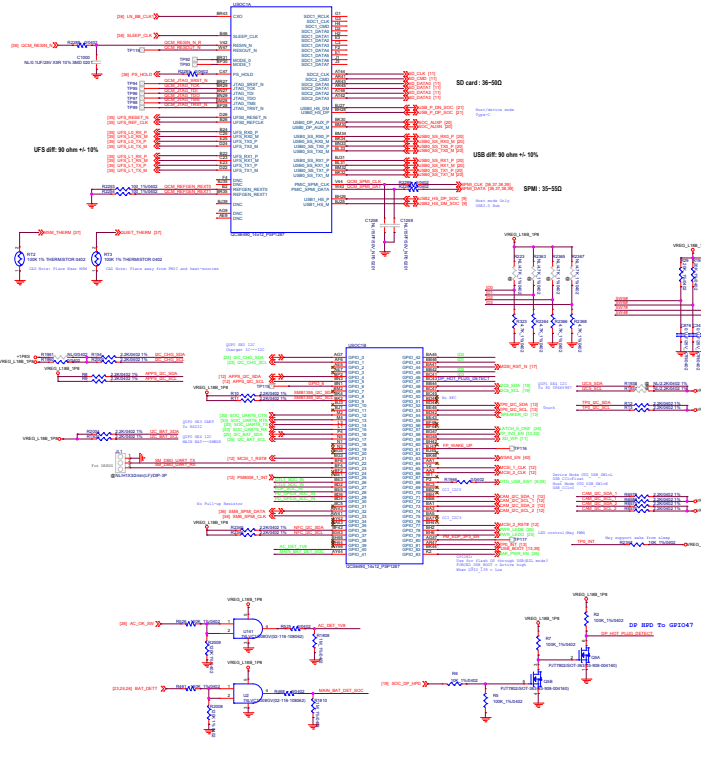
$V_{ovlo} = (1+R1/R2) * 1.2 = 16.08V$



GPIO For Type-C

Table 2. GPIO Configuration Control

CTRL PIN	CTRL PIN	PLP PIN	TUSB3000/20 CONFIGURATION	VESA PowerPort ALT MODE DFP_0 CONFIGURATION
L	L	L	Power Down	---
L	L	HS	Power Down	---
L	HS	L	One Port USB 3.1 - No Flap	---
L	HS	HS	One Port USB 3.1 - Web Flap	---
HS	L	L	4 Lane DP - No Flap	0 and 6
HS	L	HS	4 Lane DP - Web Flap	0 and 6
HS	HS	L	One Port USB 3.1 + 2 Lane DP - No Flap	0 and 7
HS	HS	HS	One Port USB 3.1 + 2 Lane DP - Web Flap	0 and 7



PART_BOOT[2]	GPIO_#			Boot Sequence
	124	122	120	
0000	0	0	0	GPIO120 = 100, GPIO122 = 100, GPIO124 = 100, GPIO126 = 100, GPIO128 = 100, GPIO130 = 100, GPIO132 = 100, GPIO134 = 100, GPIO136 = 100, GPIO138 = 100, GPIO140 = 100, GPIO142 = 100, GPIO144 = 100, GPIO146 = 100, GPIO148 = 100, GPIO150 = 100, GPIO152 = 100, GPIO154 = 100, GPIO156 = 100, GPIO158 = 100, GPIO160 = 100, GPIO162 = 100, GPIO164 = 100, GPIO166 = 100, GPIO168 = 100, GPIO170 = 100, GPIO172 = 100, GPIO174 = 100
0001	0	0	1	GPIO120 = 100, GPIO122 = 100, GPIO124 = 100, GPIO126 = 100, GPIO128 = 100, GPIO130 = 100, GPIO132 = 100, GPIO134 = 100, GPIO136 = 100, GPIO138 = 100, GPIO140 = 100, GPIO142 = 100, GPIO144 = 100, GPIO146 = 100, GPIO148 = 100, GPIO150 = 100, GPIO152 = 100, GPIO154 = 100, GPIO156 = 100, GPIO158 = 100, GPIO160 = 100, GPIO162 = 100, GPIO164 = 100, GPIO166 = 100, GPIO168 = 100, GPIO170 = 100, GPIO172 = 100, GPIO174 = 100
0010	0	1	0	GPIO120 = 100, GPIO122 = 100, GPIO124 = 100, GPIO126 = 100, GPIO128 = 100, GPIO130 = 100, GPIO132 = 100, GPIO134 = 100, GPIO136 = 100, GPIO138 = 100, GPIO140 = 100, GPIO142 = 100, GPIO144 = 100, GPIO146 = 100, GPIO148 = 100, GPIO150 = 100, GPIO152 = 100, GPIO154 = 100, GPIO156 = 100, GPIO158 = 100, GPIO160 = 100, GPIO162 = 100, GPIO164 = 100, GPIO166 = 100, GPIO168 = 100, GPIO170 = 100, GPIO172 = 100, GPIO174 = 100
0011	0	1	1	GPIO120 = 100, GPIO122 = 100, GPIO124 = 100, GPIO126 = 100, GPIO128 = 100, GPIO130 = 100, GPIO132 = 100, GPIO134 = 100, GPIO136 = 100, GPIO138 = 100, GPIO140 = 100, GPIO142 = 100, GPIO144 = 100, GPIO146 = 100, GPIO148 = 100, GPIO150 = 100, GPIO152 = 100, GPIO154 = 100, GPIO156 = 100, GPIO158 = 100, GPIO160 = 100, GPIO162 = 100, GPIO164 = 100, GPIO166 = 100, GPIO168 = 100, GPIO170 = 100, GPIO172 = 100, GPIO174 = 100
0100	1	0	0	Reserved
0101	1	0	1	Reserved
0110	1	1	0	GPIO120 = 100, GPIO122 = 100, GPIO124 = 100, GPIO126 = 100, GPIO128 = 100, GPIO130 = 100, GPIO132 = 100, GPIO134 = 100, GPIO136 = 100, GPIO138 = 100, GPIO140 = 100, GPIO142 = 100, GPIO144 = 100, GPIO146 = 100, GPIO148 = 100, GPIO150 = 100, GPIO152 = 100, GPIO154 = 100, GPIO156 = 100, GPIO158 = 100, GPIO160 = 100, GPIO162 = 100, GPIO164 = 100, GPIO166 = 100, GPIO168 = 100, GPIO170 = 100, GPIO172 = 100, GPIO174 = 100
0111	1	1	1	GPIO120 = 100, GPIO122 = 100, GPIO124 = 100, GPIO126 = 100, GPIO128 = 100, GPIO130 = 100, GPIO132 = 100, GPIO134 = 100, GPIO136 = 100, GPIO138 = 100, GPIO140 = 100, GPIO142 = 100, GPIO144 = 100, GPIO146 = 100, GPIO148 = 100, GPIO150 = 100, GPIO152 = 100, GPIO154 = 100, GPIO156 = 100, GPIO158 = 100, GPIO160 = 100, GPIO162 = 100, GPIO164 = 100, GPIO166 = 100, GPIO168 = 100, GPIO170 = 100, GPIO172 = 100, GPIO174 = 100

GPIO_116	BOOT_CONFIG0	WDOG_DISABLE
GPIO_120	BOOT_CONFIG0	FASTBOOT_SELECT
GPIO_122	BOOT_CONFIG0	FASTBOOT_SELECT
GPIO_124	BOOT_CONFIG0	FASTBOOT_SELECT
GPIO_126	SW	FORCE_USB_BOOT
GPIO_138		FORCED_USB_BOOT_POLARITY_SELECT

GPIO_# is the pin number of the GPIO pin. GPIO_# is the pin number of the GPIO pin. GPIO_# is the pin number of the GPIO pin.