The $\overline{RST}$ recovery timing is set by the capacitor on the TRST pin. A 2-µA current is enabled when the reset condition is met, charging the capacitor. The TRST voltage is monitored internally and the reset ends when the voltage reaches 0.6 V. The capacitor value to reset time can be computed with Equation 4:

$$t_{RST} = 0.6V \times \frac{C}{2 \times 10^{-6} A} \quad (4)$$

The value t_{RST} is the time from the end of condition that activated $\overline{RST}$ until $\overline{RST}$ returns to its Hi-Z state. The TRST pin would be internally discharged to ground when the reset condition is true or after t_{RST}.

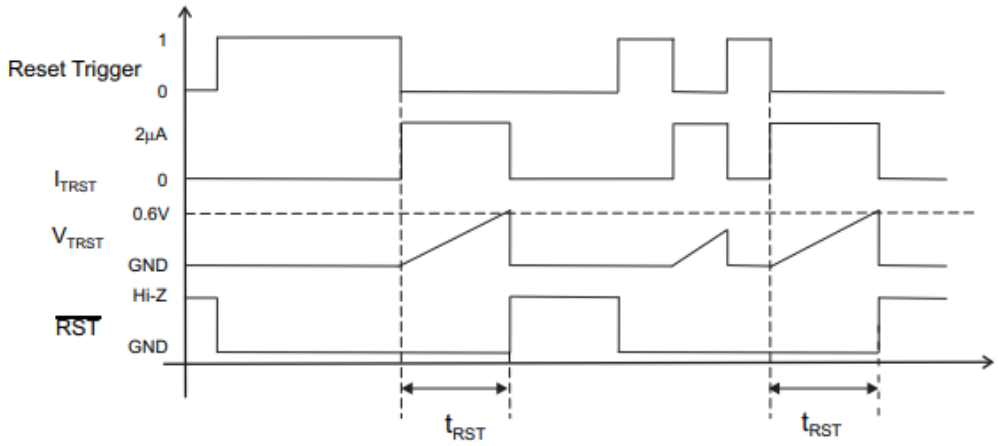

Figure 23. $\overline{RST}$ Recovery Timing