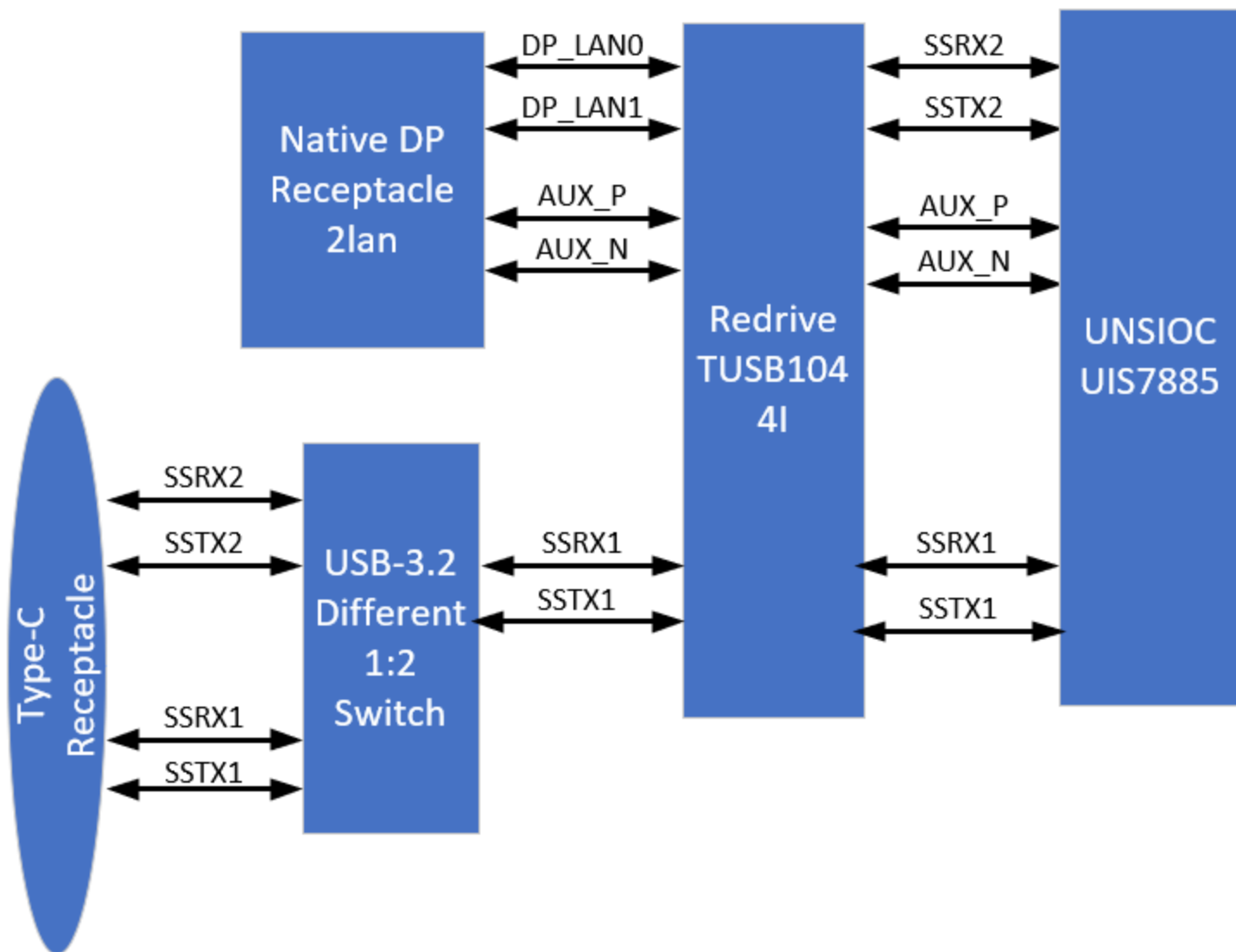
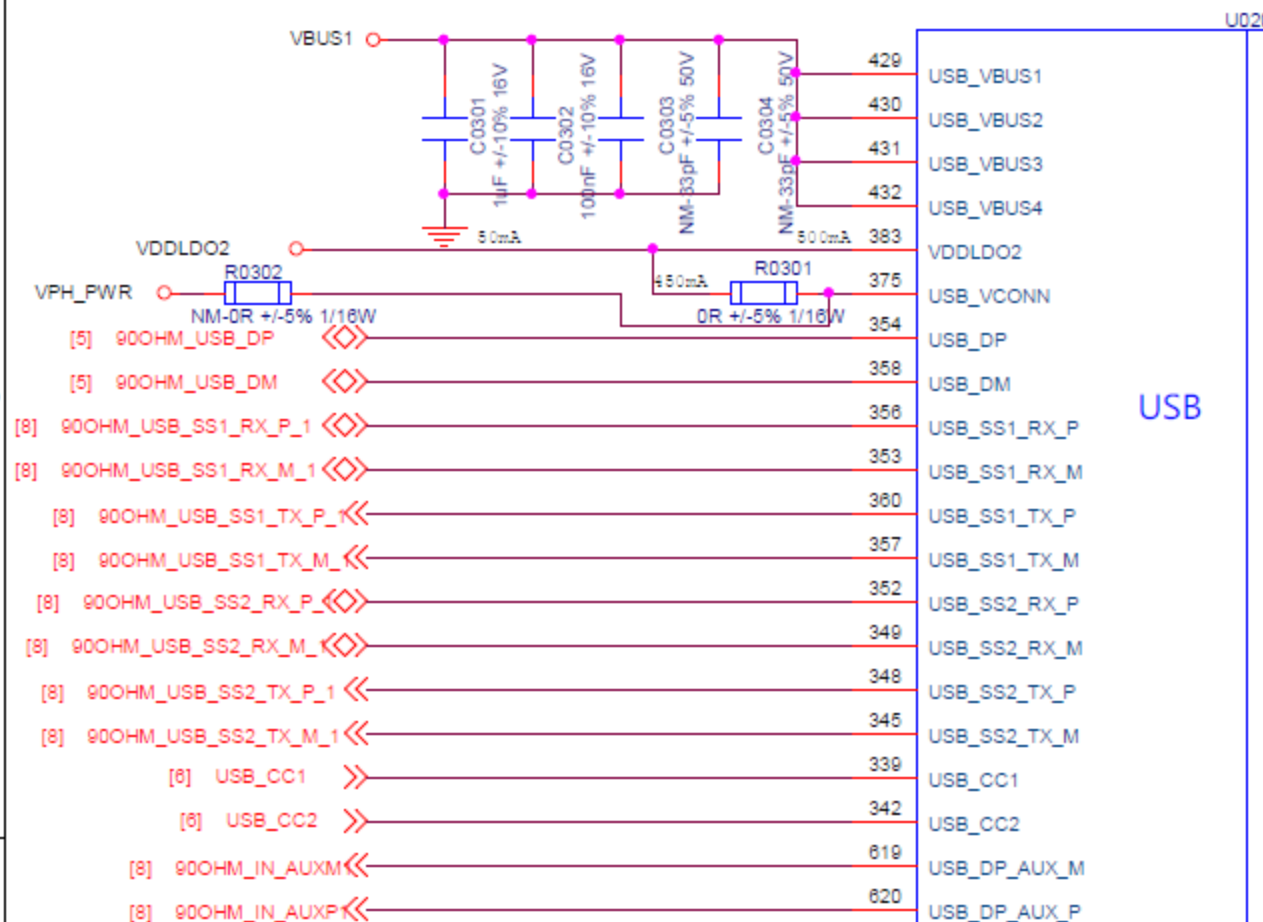






Input From UNISOC'S UIS7885

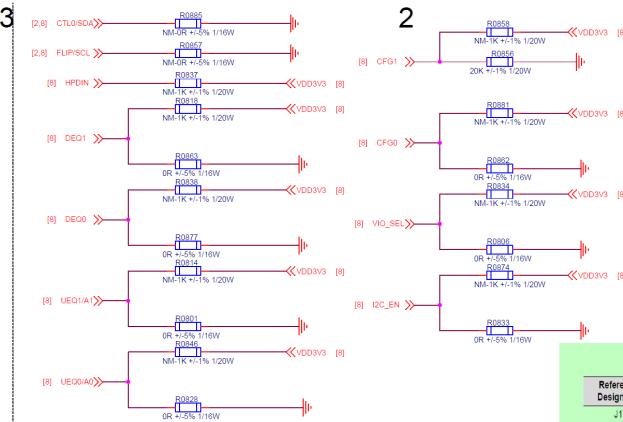
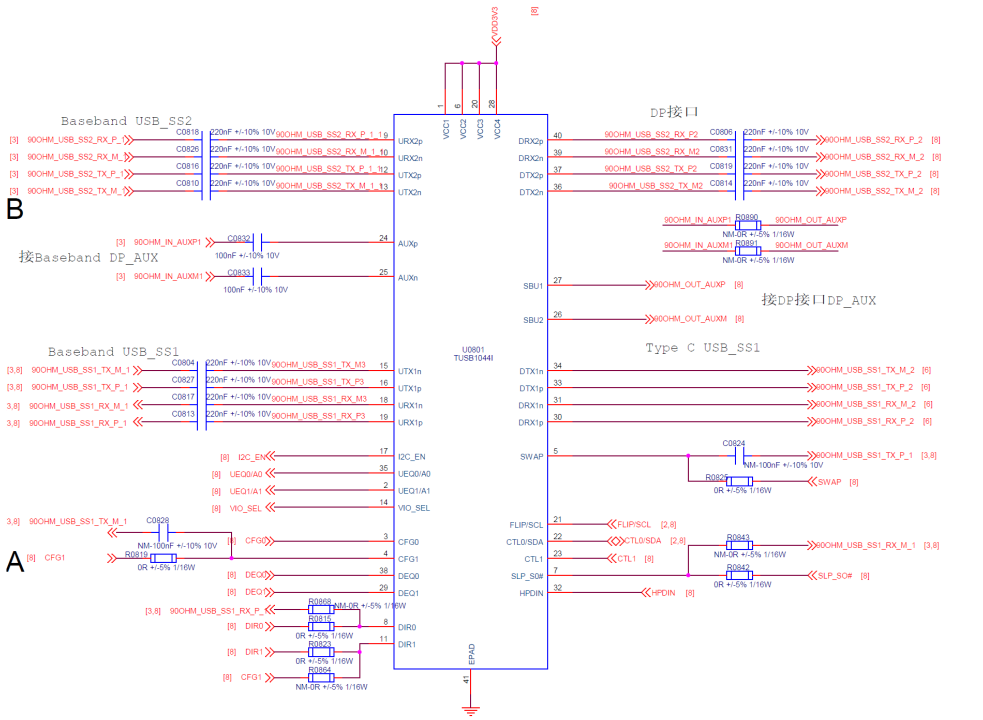
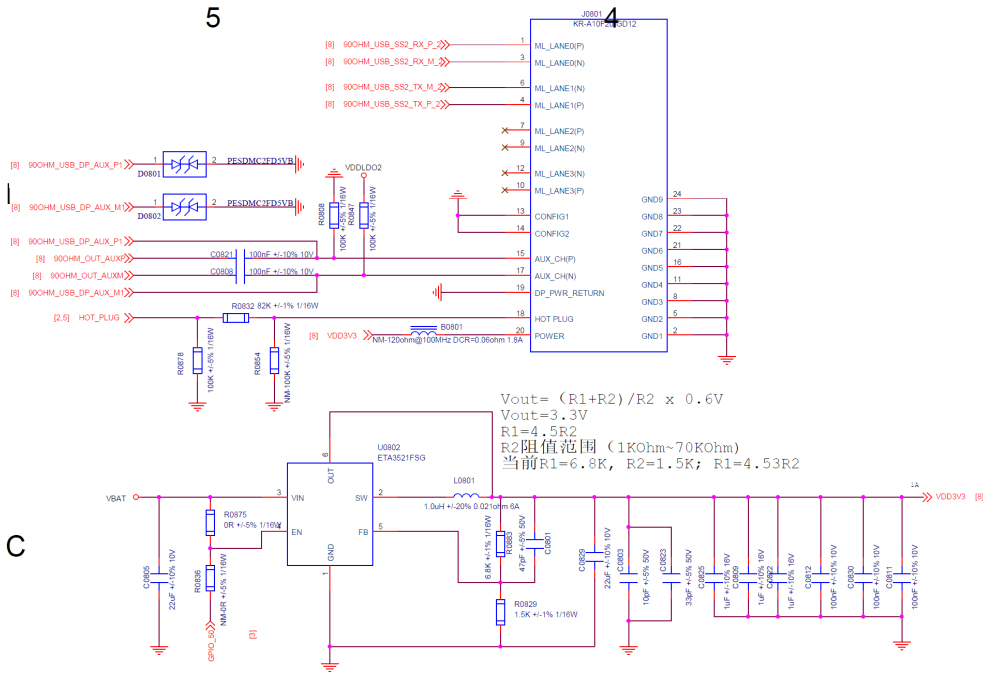


Table 1. USB1044 Configuration Pins

Reference Designator	JMP Control	Configuration
J1	CTL0SDA	No Connect
J2	FLIP/SCL	No Connect
J3	HPDIN	No Connect
J4	VCC Isolate	No Connect
JMP2	DE01	SHUNT on pin 1-2 (GND)
JMP3	DE00	SHUNT on pin 1-2 (GND)
JMP4	UEQ1/A1	SHUNT on pin 1-2 (GND)
JMP5	UEQ0/A0	SHUNT on pin 1-2 (GND)
JMP6	CF01	SHUNT on pin 2-4 (20K PD)
JMP7	CF00	SHUNT on pin 1-2 (GND)
JMP8	VIO_SEL	SHUNT on pin 1-2 (GND)
JMP9	I2C_EN	SHUNT on pin 1-2 (GND)
JMP10	E01	SHUNT on pin 1-2 (200 Ω to GND)
JMP11	DC_B0CST1	SHUNT on pin 1-2 (200 Ω to 3.3 V)
JMP12	E02	SHUNT on pin 1-2 (200 Ω to GND)
JMP13	DC_B0CST2	SHUNT on pin 1-2 (200 Ω to 3.3 V)

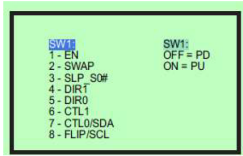
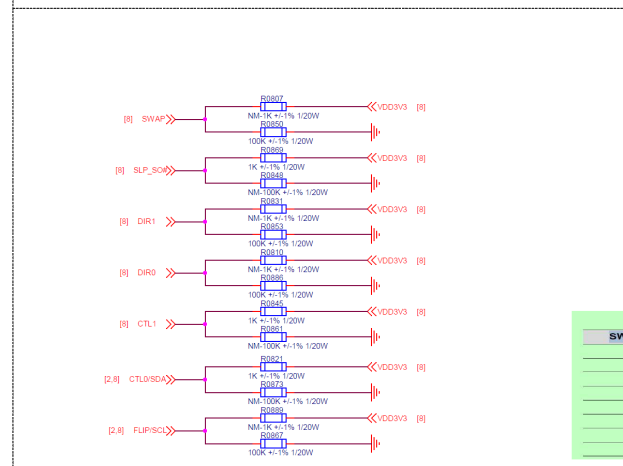


Table 2. Switch Configuration

SW1 Position	Control Signal	Configuration
1	EN	ON
2	SWAP	OFF
3	SLP_S0#	ON
4	DIR1	OFF
5	DIR0	OFF
6	CTL1	ON
7	CTL0SDA	ON
8	FLIP/SCL	OFF

