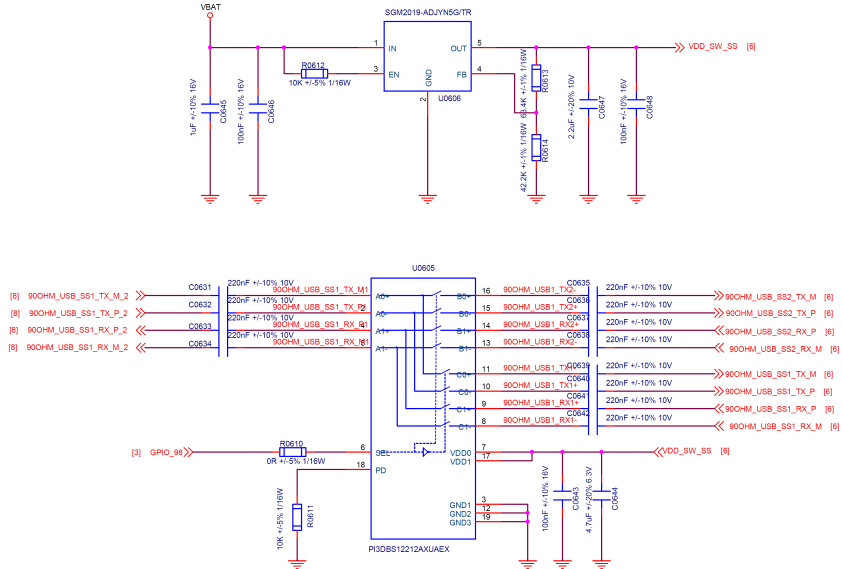
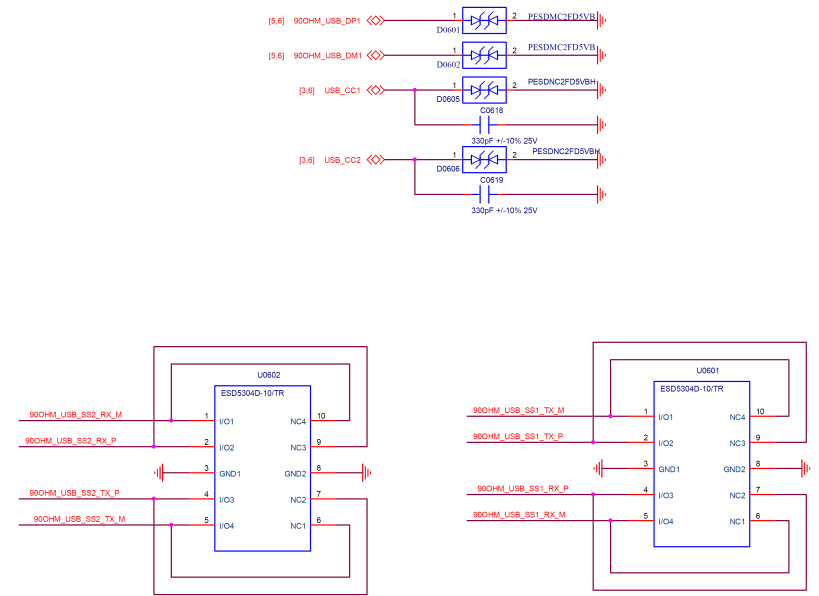


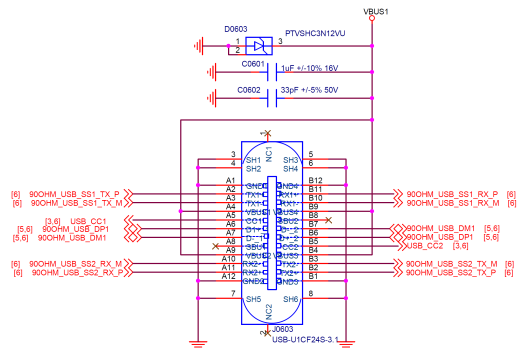
## DP

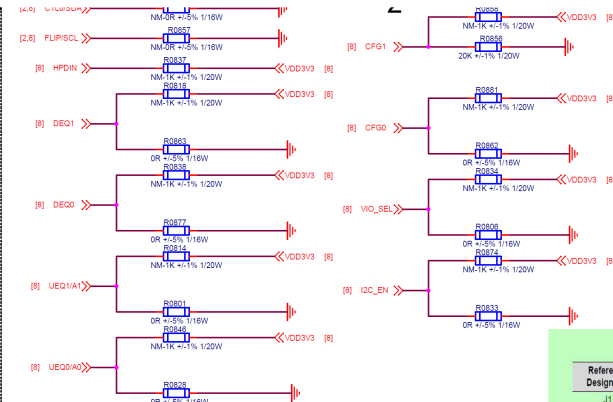
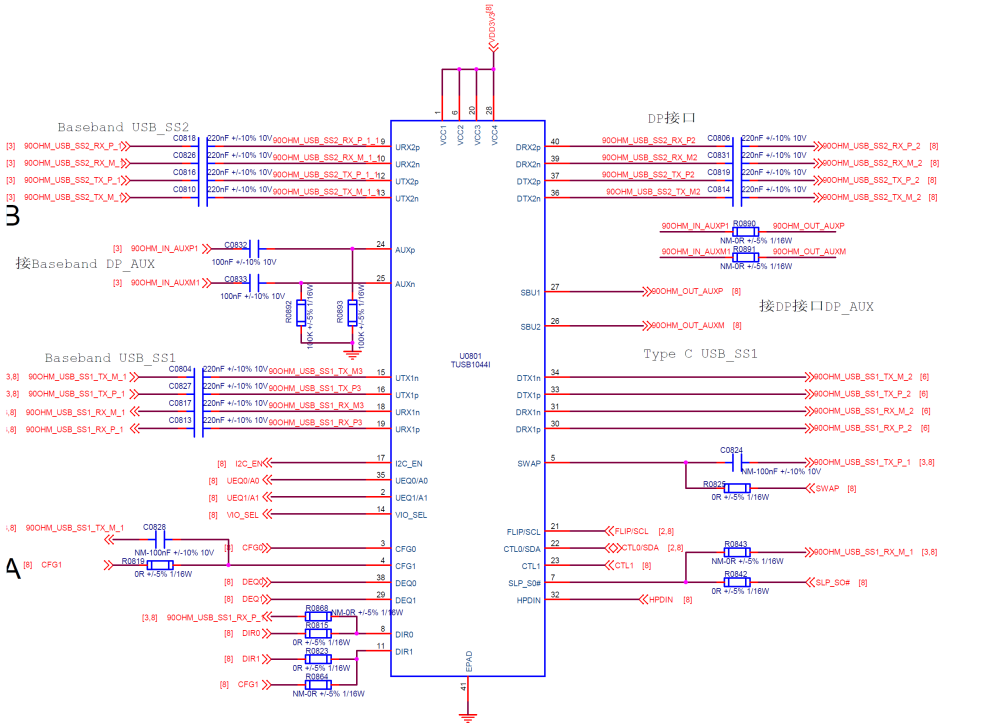
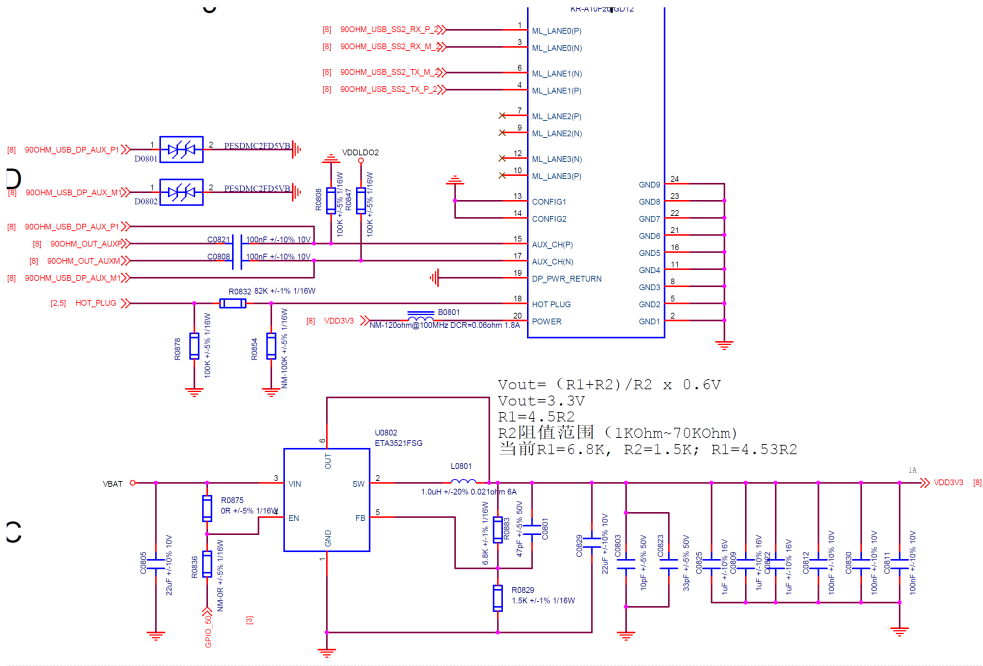


## ESD

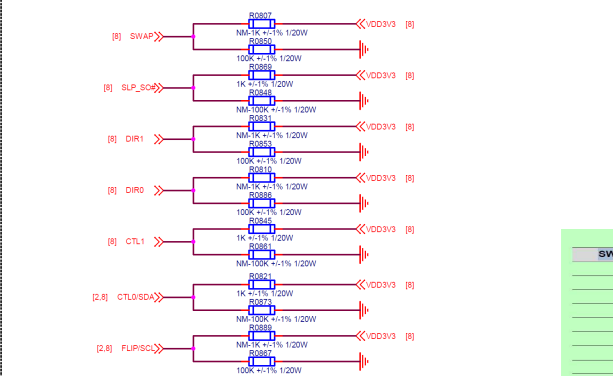


## TYPE-C CONNECTER





| Reference Designator | JMP Control | Configuration                     |
|----------------------|-------------|-----------------------------------|
| J1                   | CTL0/SDA    | No Connect                        |
| J2                   | FLIP/ISCL   | No Connect                        |
| J3                   | HPDIN       | No Connect                        |
| J4                   | VCC Isolate | No Connect                        |
| JMP2                 | DEQ1        | SHUNT on pin 1-2 (GND)            |
| JMP3                 | DEQ0        | SHUNT on pin 1-2 (GND)            |
| JMP4                 | UEQ1/A1     | SHUNT on pin 1-2 (GND)            |
| JMP5                 | UEQ0/A0     | SHUNT on pin 1-2 (GND)            |
| JMP6                 | CFG1        | SHUNT on pin 2-4 (20K PD)         |
| JMP7                 | CFG0        | SHUNT on pin 1-2 (GND)            |
| JMP8                 | VIO_SEL     | SHUNT on pin 1-2 (GND)            |
| JMP9                 | ICQ_EN      | SHUNT on pin 1-2 (GND)            |
| JMP10                | EQ2         | SHUNT on pin 1-2 (200Ω to GND)    |
| JMP11                | DC_BOOST1   | SHUNT on pin 1-2 (20 kΩ to 3.3 V) |
| JMP12                | EQ2         | SHUNT on pin 1-2 (200Ω to GND)    |
| JMP13                | DC_BOOST2   | SHUNT on pin 1-2 (20 kΩ to 3.3 V) |



| SW1 Position | Control Signal | Configuration |
|--------------|----------------|---------------|
| 1            | EN             | ON            |
| 2            | SWAP           | OFF           |
| 3            | SLP_S0#        | ON            |
| 4            | DIR1           | OFF           |
| 5            | DIR0           | OFF           |
| 6            | CTL1           | ON            |
| 7            | CTL0/SDA       | ON            |
| 8            | FLIP/ISCL      | OFF           |

