

# BQ24297's Datasheet Page.4

## Pin Functions

PIN		NUMBER	TYPE	DESCRIPTION
bq24296	bq24297			
VBUS	VBUS	1,24	P	Charger Input Voltage. The internal n-channel reverse block MOSFET (RBFET) is connected between VBUS and PMID with VBUS on source. Place a 1-μF ceramic capacitor from VBUS to PGND and place it as close as possible to IC.
PSEL	PSEL	2	I	Power source selection input. High indicates a USB host source and Low indicates an adapter source.
–	D+	2	I Analog	Positive line of the USB data line pair. D+/D– based USB host/charging port detection. The detection includes data contact detection (DCD), primary detection in bc1.2, and non-standard adapters.
PG	–	3	O	Open drain active low power good indicator. Connect to the pull up rail via 10-kΩ resistor. LOW indicates a good input source if the input voltage is between UVLO and ACOV, above SLEEP mode threshold, and current limit is above 30 mA.
–	D–	3	I Analog	Negative line of the USB data line pair. D+/D– based USB host/charging port detection. The detection includes data contact detection (DCD), primary detection in bc1.2, and non-standard adapters.
STAT	STAT	4	O	Open drain charge status output to indicate various charger operation. Connect to the pull up rail via 10-kΩ resistor. LOW indicates charge in progress. HIGH indicates charge complete or charge disabled. When any fault condition occurs, STAT pin in the charge blinks at 1 Hz.
SCL	SCL	5	I	I <sup>2</sup> C Interface clock. Connect SCL to the logic rail through a 10-kΩ resistor.
SDA	SDA	6	I/O	I <sup>2</sup> C Interface data. Connect SDA to the logic rail through a 10-kΩ resistor.
INT	INT	7	O	Open-drain Interrupt Output. Connect the INT to a logic rail via 10kΩ resistor. The INT pin sends active low, 256-μs pulse to host to report charger device status and fault.
OTG	OTG	8	I Digital	USB current limit selection pin during buck mode, and active high enable pin during boost mode. For bq24296, when in buck mode with USB host (PSEL = High), when OTG = High, IIN limit = 500 mA and when OTG = Low, IIN limit = 100 mA. For bq24297, when in buck mode with USB host, when OTG = High, IIN limit = 500 mA and when OTG = Low, IIN limit = 100 mA. The boost mode is activated when the REG01[5] = 1 and OTG pin is High.
CE	CE	9	I	Active low Charge Enable pin. Battery charging is enabled when REG01[5:4] = 01 and CE pin = Low. CE pin must be pulled high or low.

$\overline{CE}$