



LLC secondary side synchronous rectification circuit.

We have elected FETs,

Unlike flyback, the secondary side resonance current waveform has a small current immediately after ON.

When a FET with a low ON resistance (for example, 8 mΩ) is used,

When MINIMUM ON TIME ends and VTHVG OFF is less than -9 mV, the gate turns OFF.

Therefore, I think that FET with low ON resistance can not be used.

Is this thought ok?

VD-VS Voltage

VG-VS Voltage