

Q1

We are examining SR with high side drive.

The application note is a low side drive.

Is there a description on the high side drive?

Also please tell me if there are problems with high side drive.

Q2

Regarding high-side drive,

Is there a way to monitor the lamp current?

In addition, please teach the current value and the value of the capacitor.

Q3

Please see Table 1 below.

Raise the input voltage and SRMOS

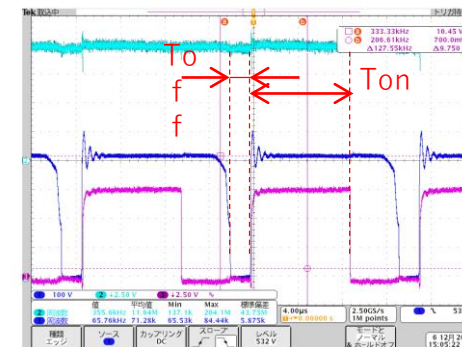
ON time has become extremely short.

Improvement was made when replacing with a new IC.

Was this IC destroyed?

What is the reason for this?

Vin	Load factor	RatioVPC_VSC
90	25	5.41
	50	5.82
	75	6.16
	100	6.16
264	25	9.86
	50	8.09
	75	7.25
	100	7.36



Light blue : Vcc voltage  
Blue : DCDC section FETVds  
purple : SR Vgs