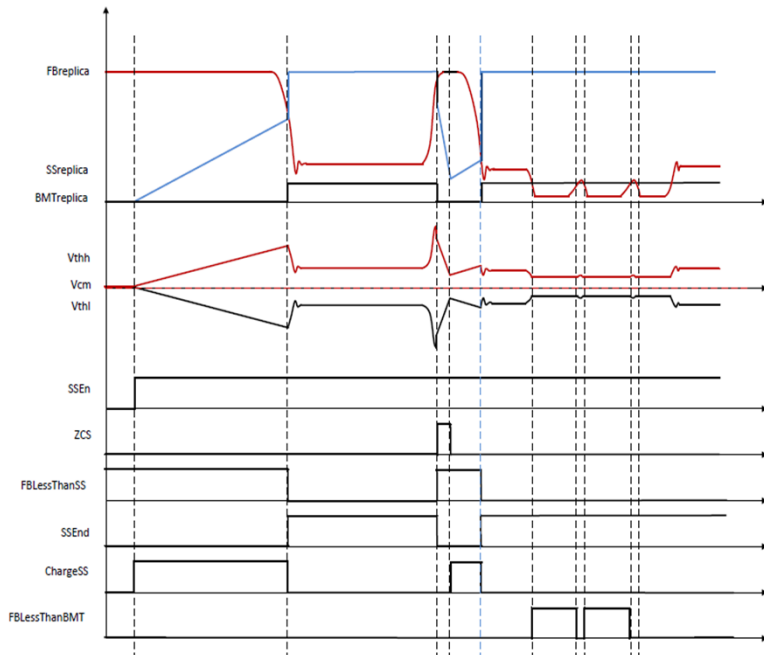# ① About soft start

Figure 29. Feedback Chain Timing Diagram

AC85V
Io＝0

2CH:LL/SS
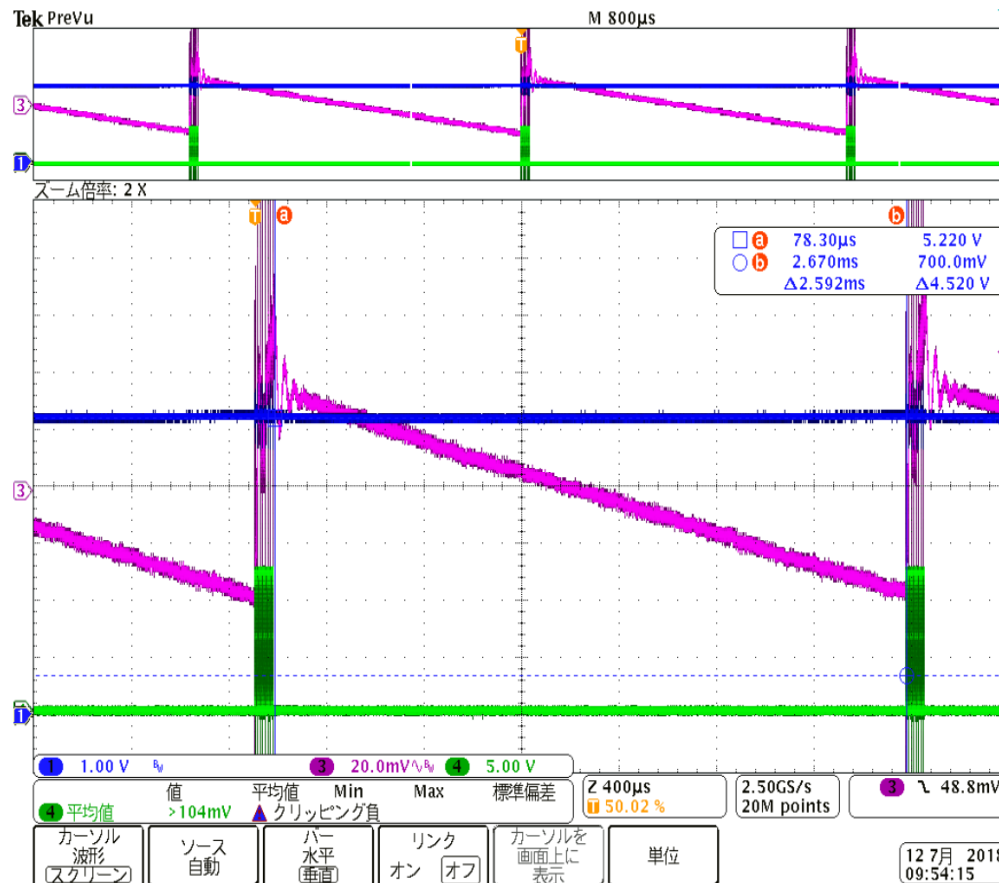3CH：FB
4CH:Vo

End soft start?

Looking at the above timing of the data sheet, SSEnd goes High when Ssreplica becomes higher than FBReplica. Soft start has ended at this time.

1 Ssreplica ＝ LL/SS Voltage
   Fbreplica ＝ FB Voltage
   Is this correct?

2 The upper waveform (under evaluation) is FB> LL / SS. It seems that soft-start has ended.
Is the behavior of this waveform correct?
(I think it is different from the operation described in the data sheet.)

3 In addition, the sending start is over in the above part.
Is this correct in your understanding?

② About burst mode threshold

1ch-Blue: V_FB
3ch-Pink: Vo
4ch-Green: L-INV_Vgs

The circuit diagram is as shown in the basic data sheet.
The changes are as follows.
Circuit constant
RVCC=6.8V
R1=1K
R2=33
C1=0.022u

The waveform is performing burst operation at no load.
RLLupper（Terminal Upper Resistance)=open.
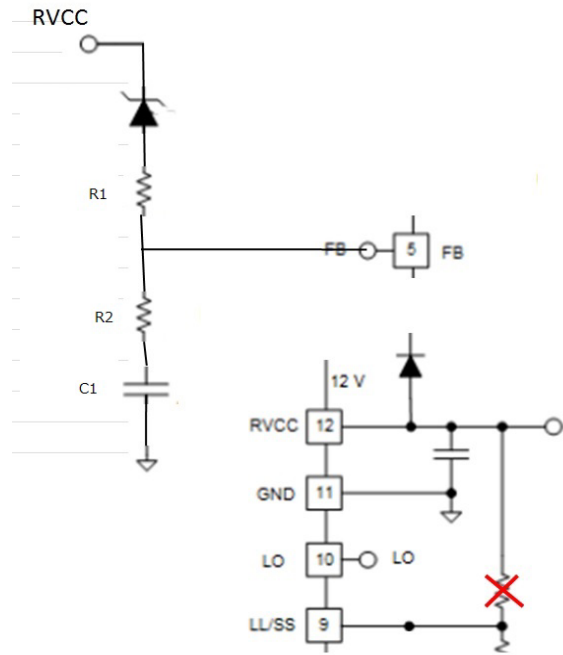VLL(Burst threshold voltage)=0.7V(min)
When the FB voltage falls below VLL, the burst operation starts.
State of FB voltage> 0.7 V. However, I think that the burst operation state.
why?
What is the reason for this?

Circuit

Circuit constant
RVCC=6.8V
R1=1K
R2=33
C1=0.022u