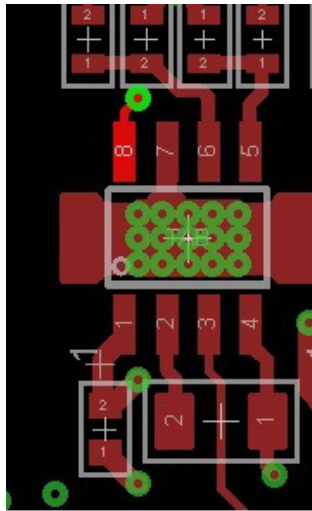
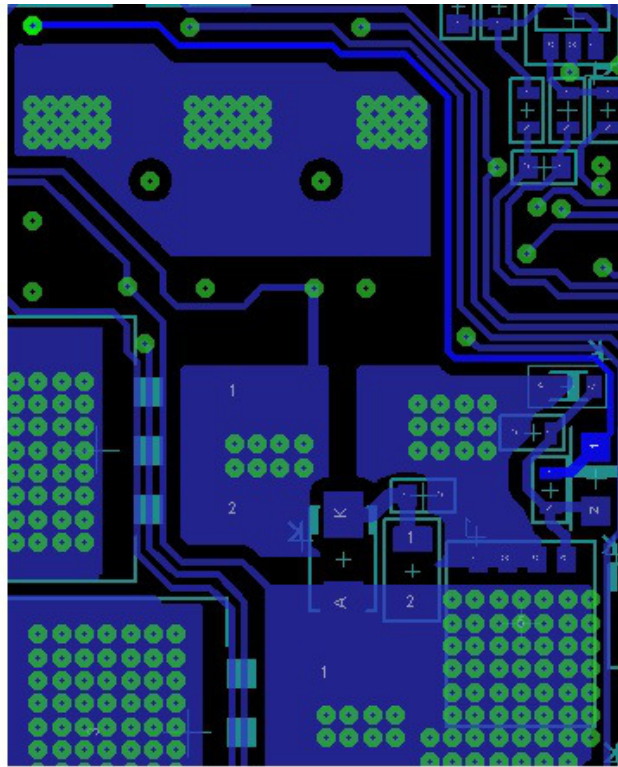


Layout of UCC27201A-Q1

1. For IC1- Routing of LO track on top and bottom layer.

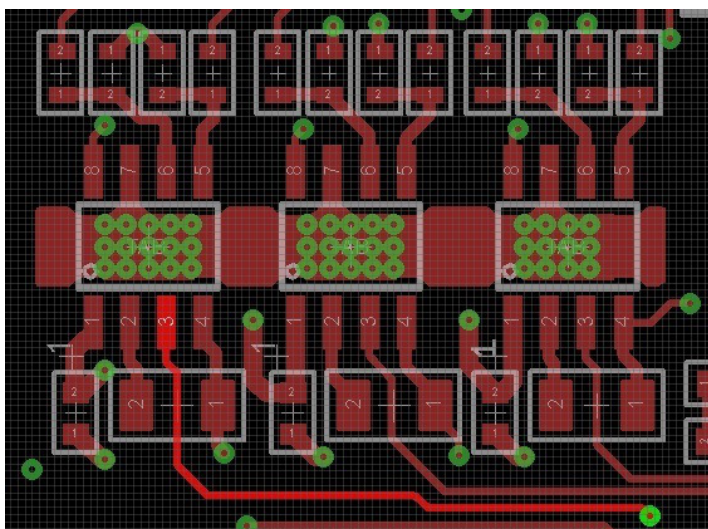


Top

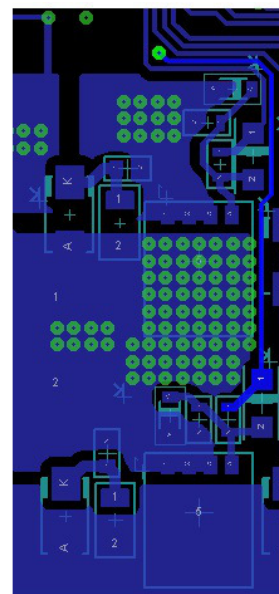


Bottom

2. For IC1- Routing of HO track on top and bottom layer.

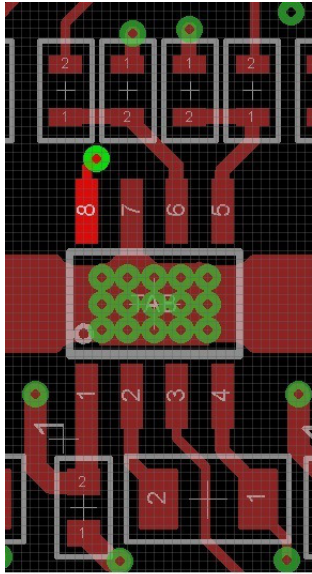


TOP

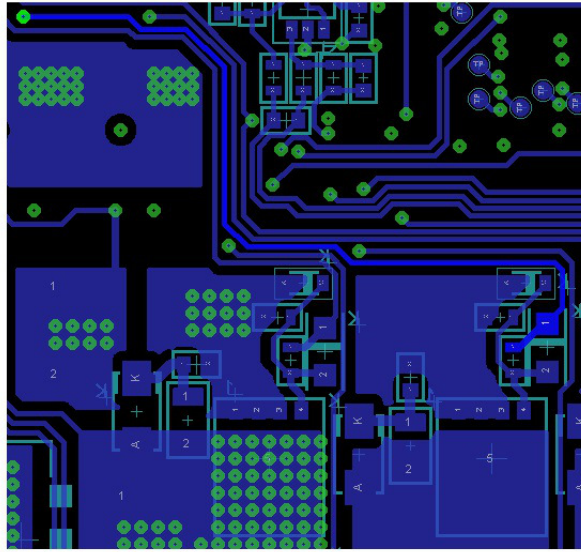


BOTTOM

3. For IC2- Routing of LO track on top and bottom layer.

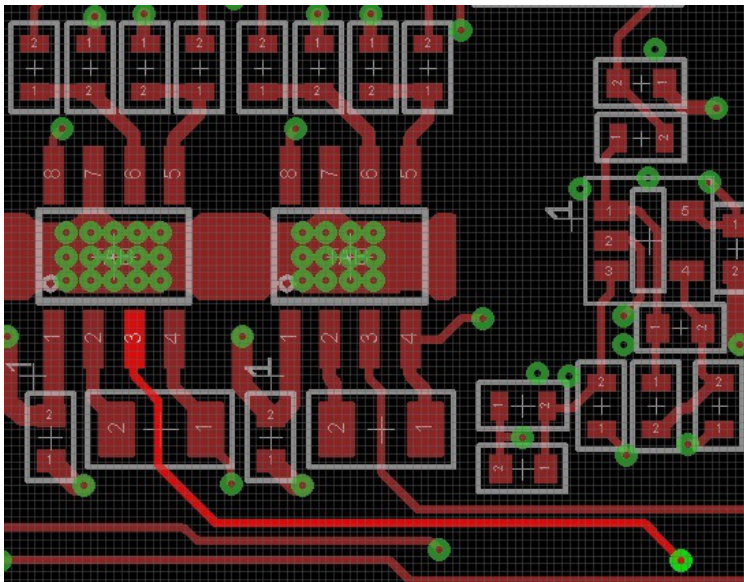


TOP

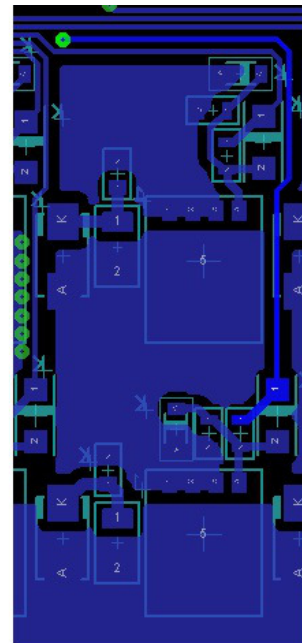


BOTTOM

4. For IC2- Routing of HO track on top and bottom layer.

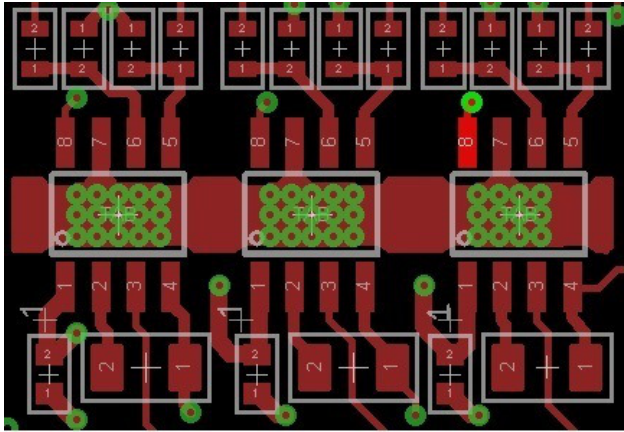


Top

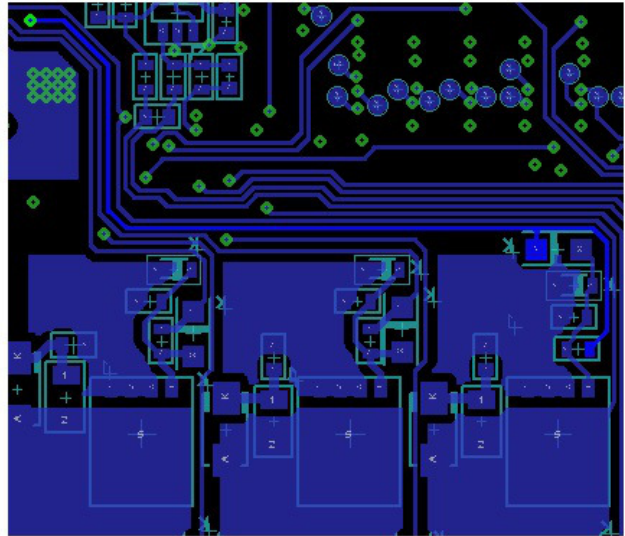


Bottom

5. For IC3- Routing of LO track on top and bottom layer.

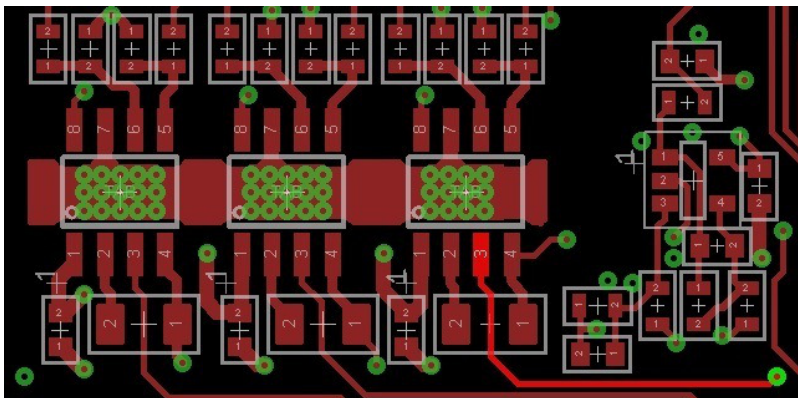


Top

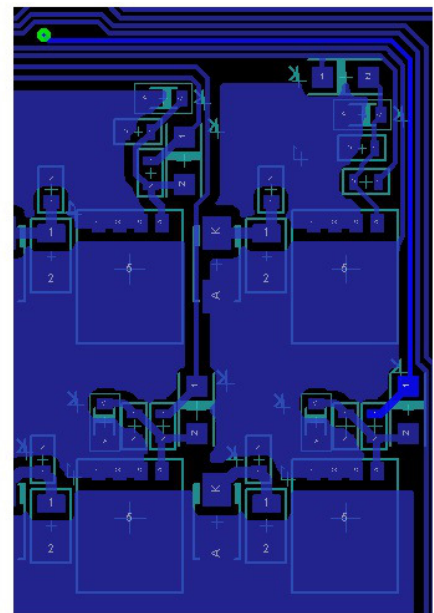


Bottom

6. For IC4- Routing of HO track on top and bottom layer.



Top



Bottom

7. Ground routing for all three IC's.



Seperate ground provided on ground layer for all three drivers.