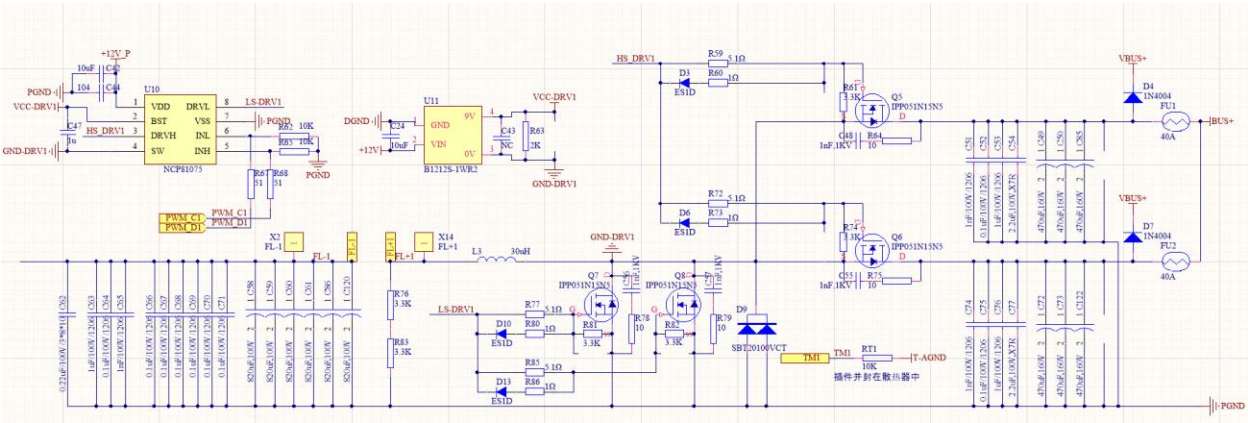
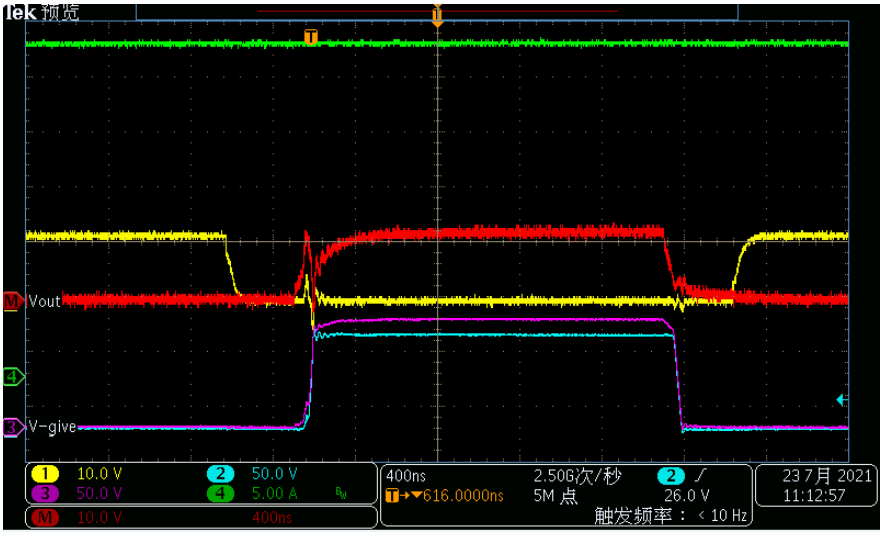
Schematic:

1. Customer use 1pcs UCC27211 to drive 2pcs parallel MOSFET.
2. Customer use isolated DC/DC to help UCC27211 start up under pre-bias, which we discussed on E2E: https://e2e.ti.com/support/power-management-group/power-management/f/power-management-forum/1013075/ucc27211-ucc27211-uvlo-under-pre-bias-start-up?tisearch=e2e-sitesearch&keymatch=UCC27211#

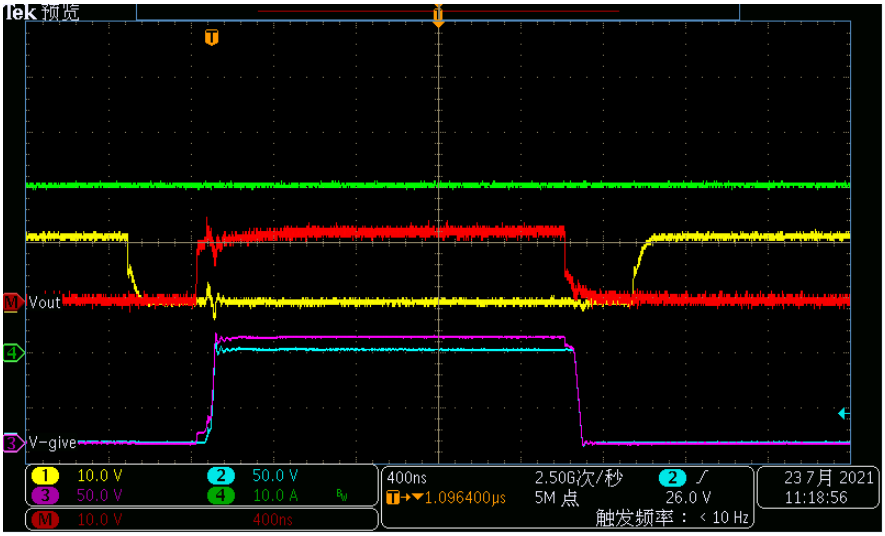


Issues:

1. **Noise on LS/HS dirve waveforms when output 30A current.**

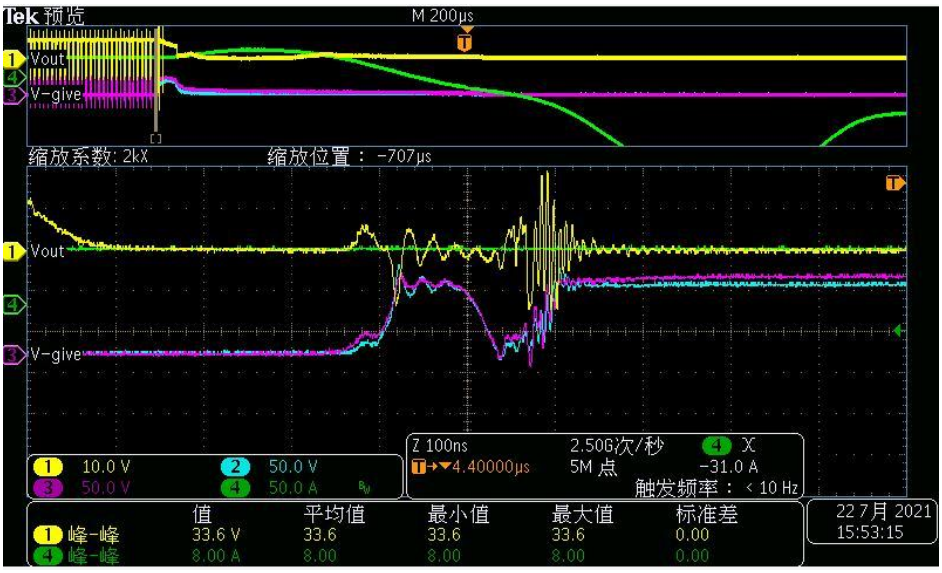


CH1：Q7(Low side MOSFET) VG_S,
CH2: Q7(Low side MOSFET) VD_S,
CH3：Q5(High side MOSFET) VG_GND,
CH4：output current,
M: CH3-CH2

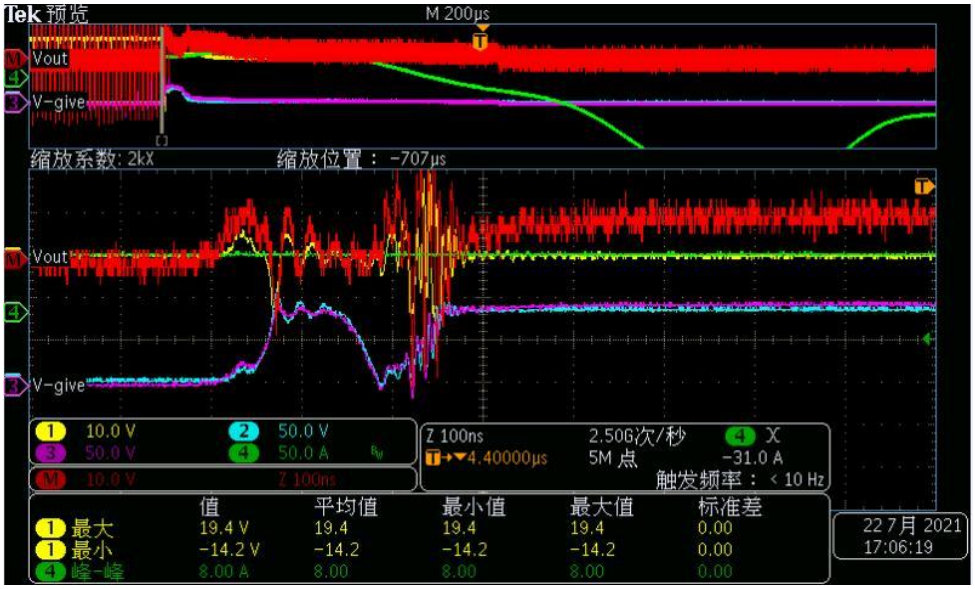


CH1：LS_DRV1 to GND,
CH2: Q7(Low side MOSFET) VD_S,
CH3：HS_DRV1 to GND,
CH4：output current,
M: CH3-CH2

2. **More serious noise on LS/HS dirve waveforms when output 60A current. MOSFETs eventually damage.**

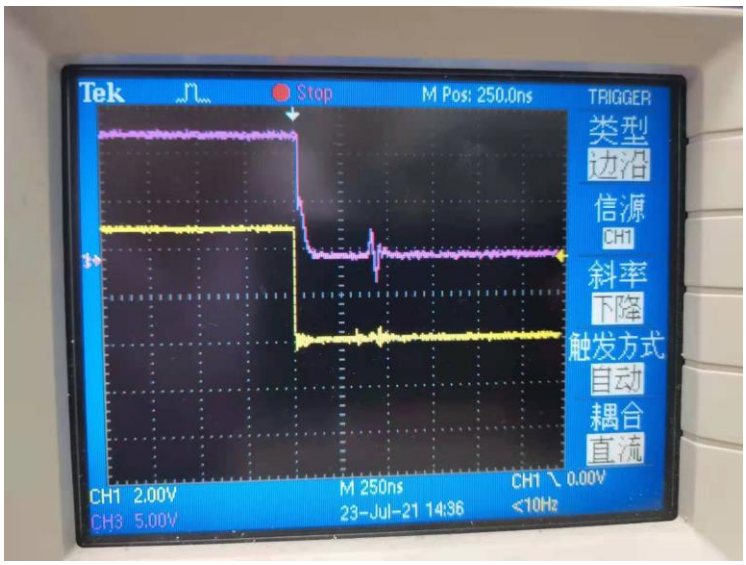


SW drop due to HS and LS turn off at the same time.



CH1： Q7(Low side MOSFET) VG_S,
CH2: Q7(Low side MOSFET) VD_S,
CH3： Q5(High side MOSFET) VG_GND,
CH4： output current,
M: CH3-CH2

3. Low side Input and output signals of UCC27211



This is no obvious noise from input signal.