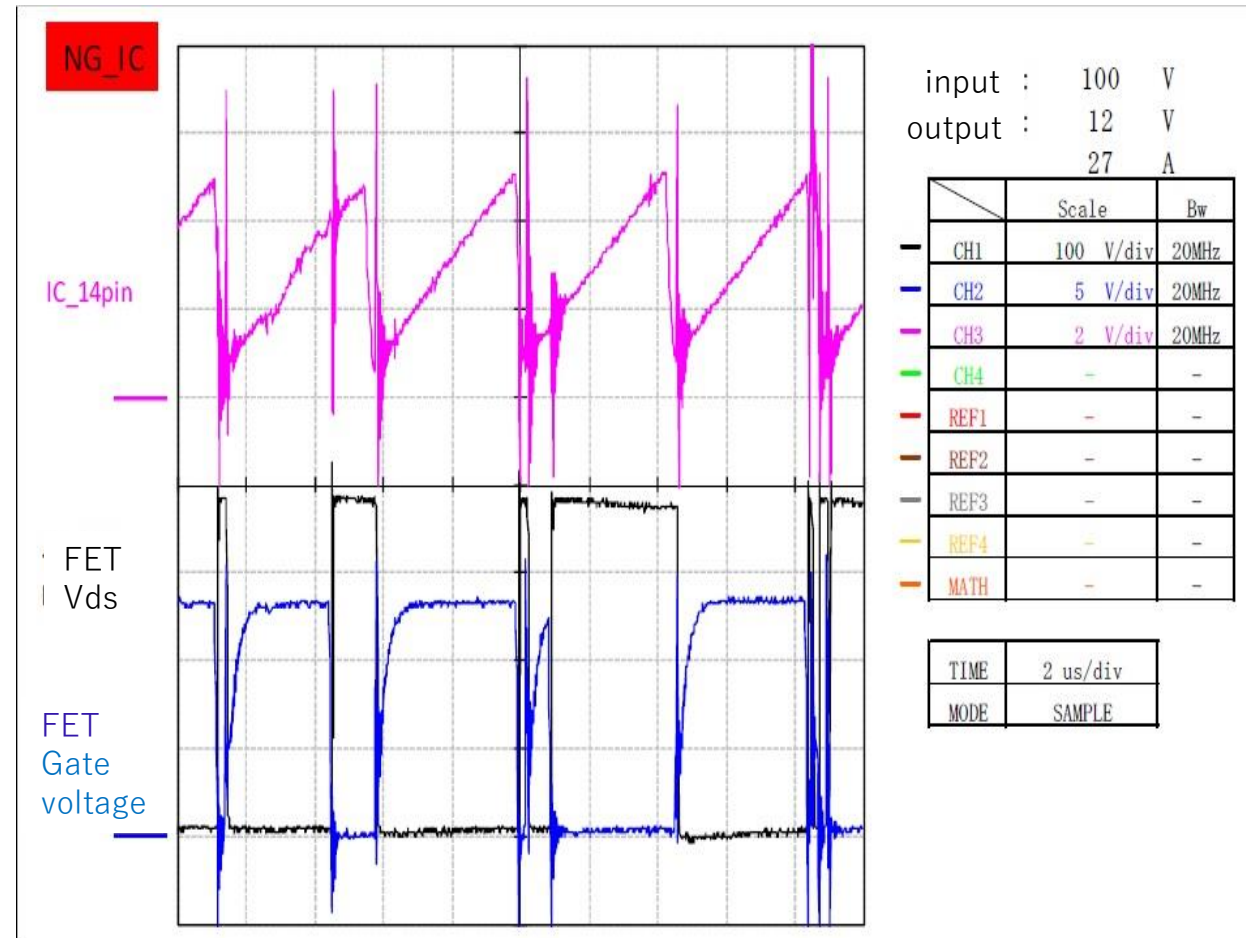


input : 100 V  
output : 12 V  
27 A

	Scale	Bw
CH1	100 V/div	20MHz
CH2	5 V/div	20MHz
CH3	2 V/div	20MHz
CH4	-	-
REF1	-	-
REF2	-	-
REF3	-	-
REF4	-	-
MATH	-	-

TIME	2 us/div
MODE	SAMPLE



In comparison with OK\_IC, ON / OFF timing of the gate is abnormal in the case of NG\_IC.  
Why is this behavior?  
Please tell me the possible causes.