SIRS:

A LITTLE ABOUT THE ATTACHED WAVEFORMS. FIGURES #1 THRU #7 ARE ALL THE SAME SIGNAL. I TRIED TO GIVE YOU THE PICS TTHAT I THOUGHT WWOULD THAT I BELIEVE WOULD DEMONSTRATE THE PROBLEM I AM FACED WITH AND WHICH I HAVE NOT BEEN ABLE TO SOLVE. (I HATE IT).

ALL WAVE FORMS FOLLOW THE SAME COLOR SCHEME:

(CH3) MAGENTA- IC PIN# 20 OUTC

(CH4) BLUE- IC PIN# 19 OUTD

(CH1) YELLOW- GDT SEC#1 (no mosfet attached)

(CH2) TEAL- GDT SEC#2 (no mosfet attached)

SO, LETS GO STRAIGHT TO THE PROBLEM, WHICH I SEE AS A PROBLEM. NOTICE THE RINGING WHICH PEEKS ITS UGLY, BUT WELL SHAPED RINGING RIGHT IN THE MIDDLE OF THE GDT SEC#1 AND GDT SEC#2 WAVEFORMS? I BELIEVE WHAT SHOULD BE THERE ARE TWO ZERO REFERENCING LINES.

(NOTE, I JUST NOTICED THAT THE TWO SIGNALS, OUTC AND OUTD WERE AC COUPLED, WHICH IS WRONG. THEY WERE SUPPOSED TO BE DC COUPLED. SORRY ABOUT THAT.)

SEE ATTACHED WAVEFORMS

Question #1: IS THAT THE WAY THE SIGNAL SHOULD BE?

Question #2: IF NOT, WILL A SNUBBER TAKE CARE OF IT?

Question #3: IS THER TOO MUCH DELAY TIME BETWEEN SIGNALS? SINCE MY LAST POSTING I HAVE BLOWN 14 MOSFETS. NOT GOOD.

Question #4: IS THE SLOPE OF THE GDT OUTPUT SIGNALS (YELLOW AND TEAL GOOD? IF NOT GIVE ME SOME SUGGESTIONS.

Question #5: WILL YOU GIVE ME A SUMMARY OF WHAT YOU NOTICED, CAN FORESEE, ANTICIPATE, IN ENERAL, ANTHING THAT YOU BELIEVE WILL BE OF HELP.

THANK YOU

EDDIE LOY

FIGURE #1 – (MAGENTA) OUTC, (BLUE) OUTD, [(YELLOW) GDT SEC OUT #1, (TEAL) GDT SEC OUT #2 -- NO MOSFET GATES ATTACHED]

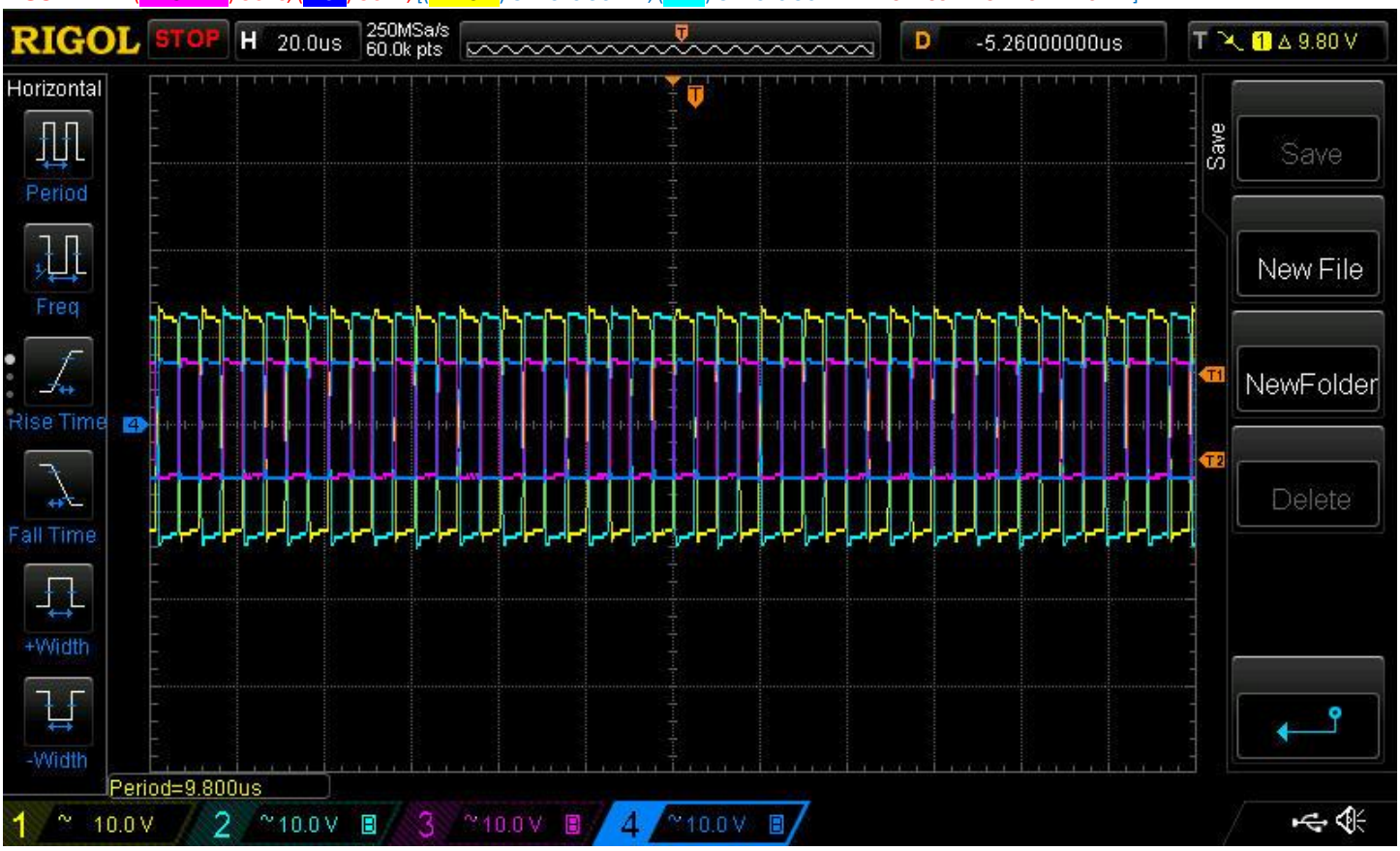

FIGURE #2 – (MAGENTA) OUTC, (BLUE) OUTD, [(YELLOW) GDT SEC OUT #1, (TEAL) GDT SEC OUT #2 -- NO MOSFET GATES ATTACHED

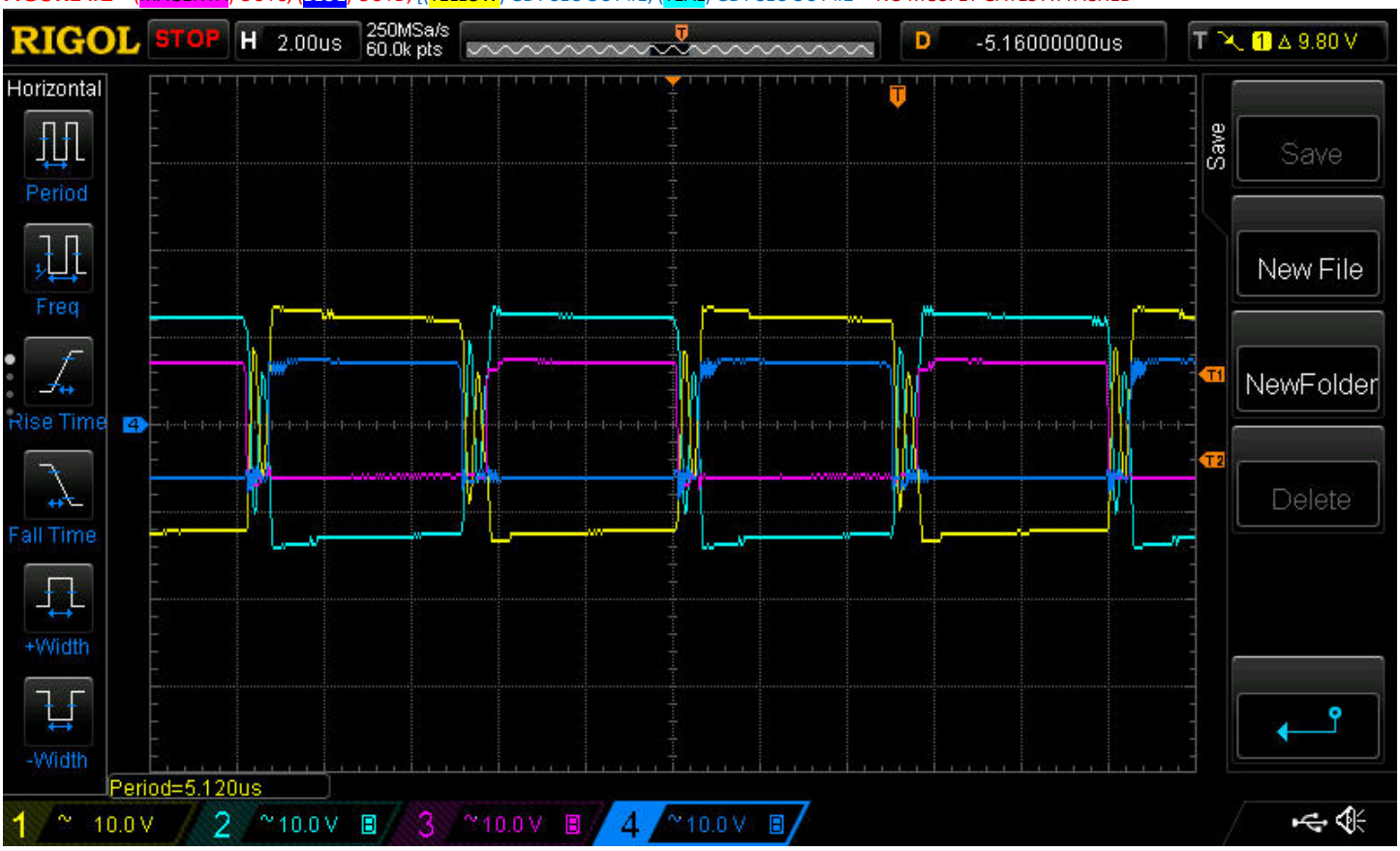

FIGURE #3 – (MAGENTA) OUTC, (BLUE) OUTD, [(YELLOW) GDT SEC OUT #1, (TEAL) GDT SEC OUT #2 -- NO MOSFET GATES ATTACHED

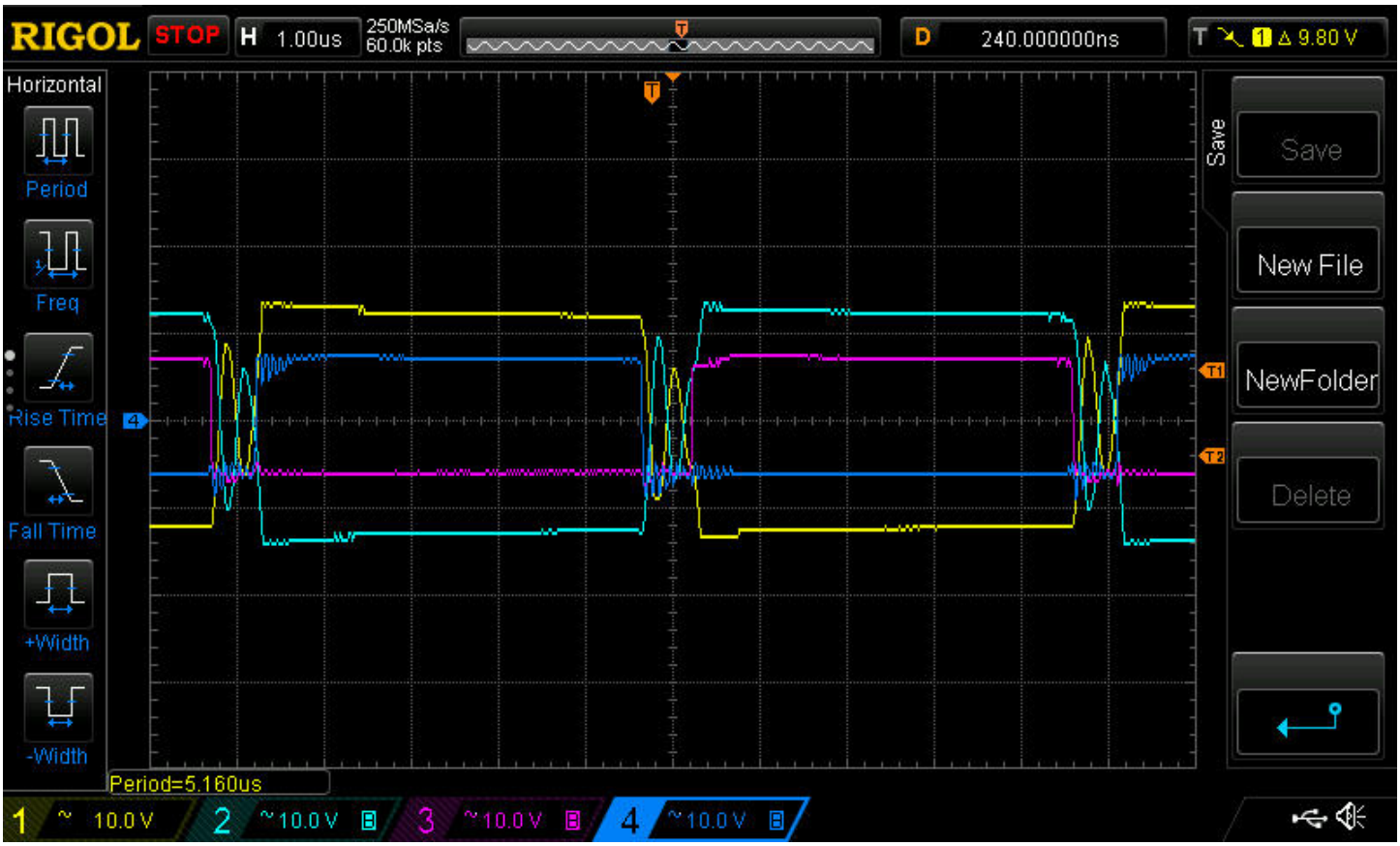

FIGURE #4 – (MAGENTA) OUTC, (BLUE) OUTD, [(YELLOW) GDT SEC OUT #1, (TEAL) GDT SEC OUT #2 -- NO MOSFET GATES ATTACHED

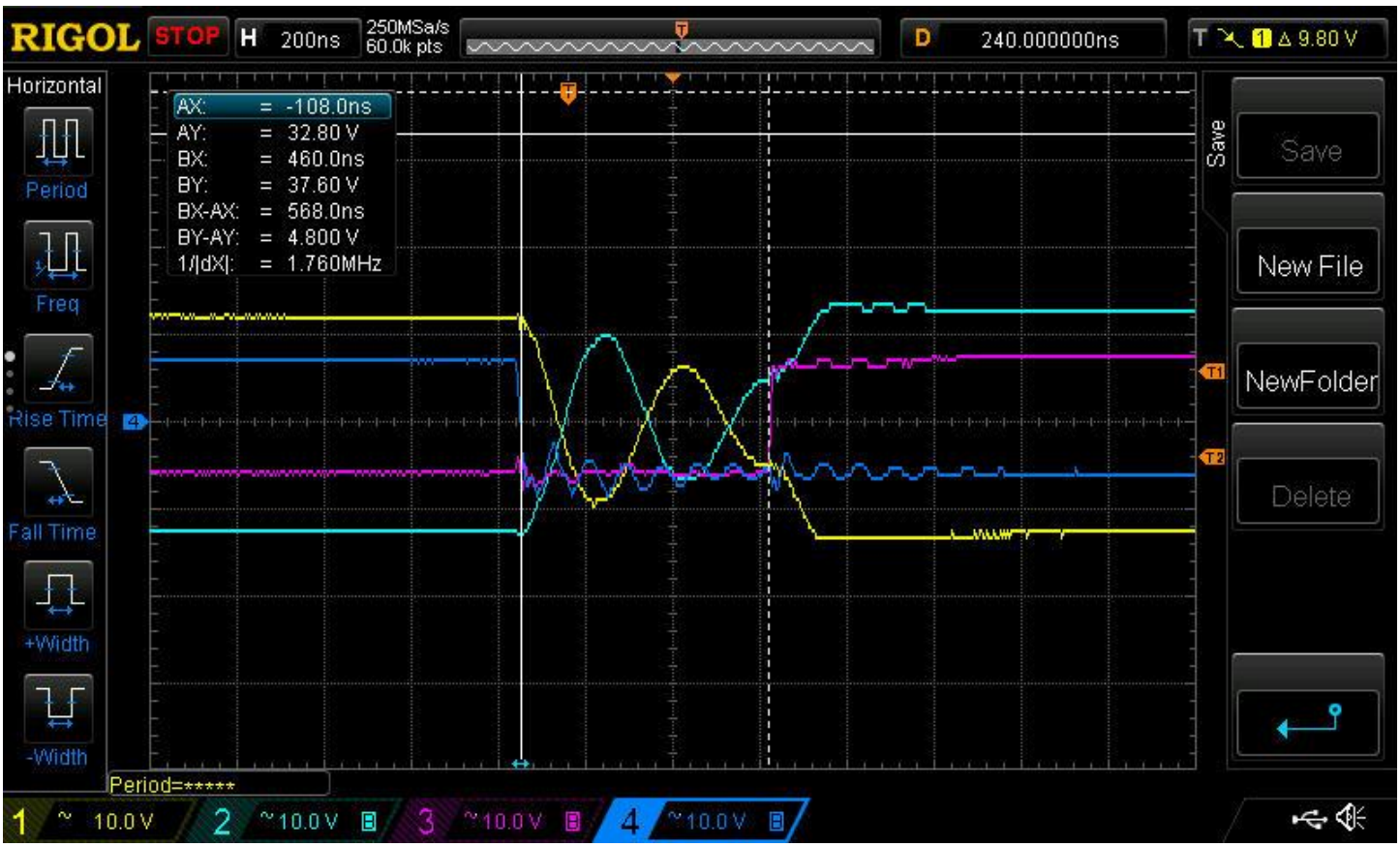

FIGURE #5 – (MAGENTA) OUTC, (BLUE) OUTD, [(YELLOW) GDT SEC OUT #1, (TEAL) GDT SEC OUT #2 -- NO MOSFET GATES ATTACHED

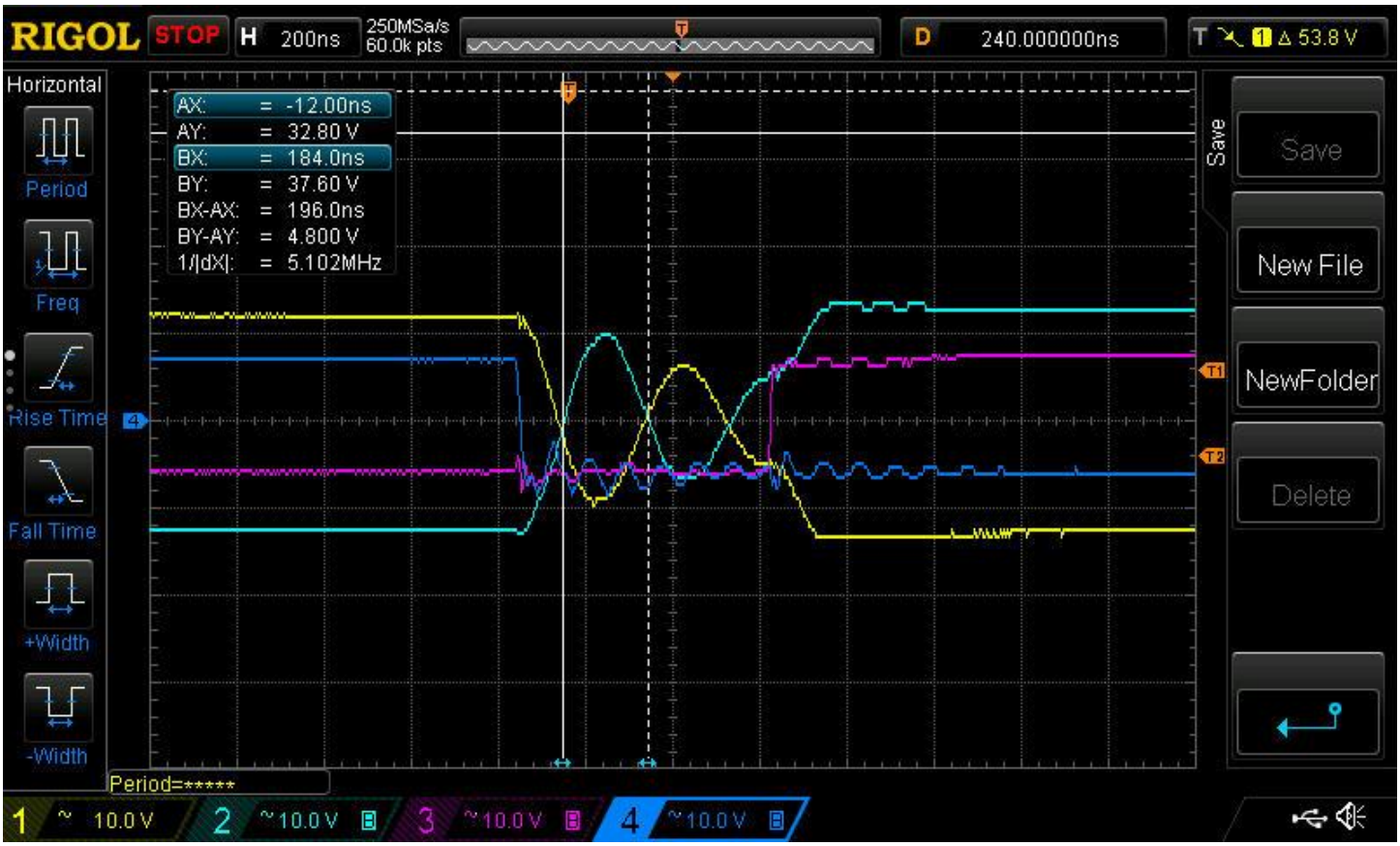

FIGURE #6 – (MAGENTA) OUTC, (BLUE) OUTD, [(YELLOW) GDT SEC OUT #1, (TEAL) GDT SEC OUT #2 -- NO MOSFET GATES ATTACHED

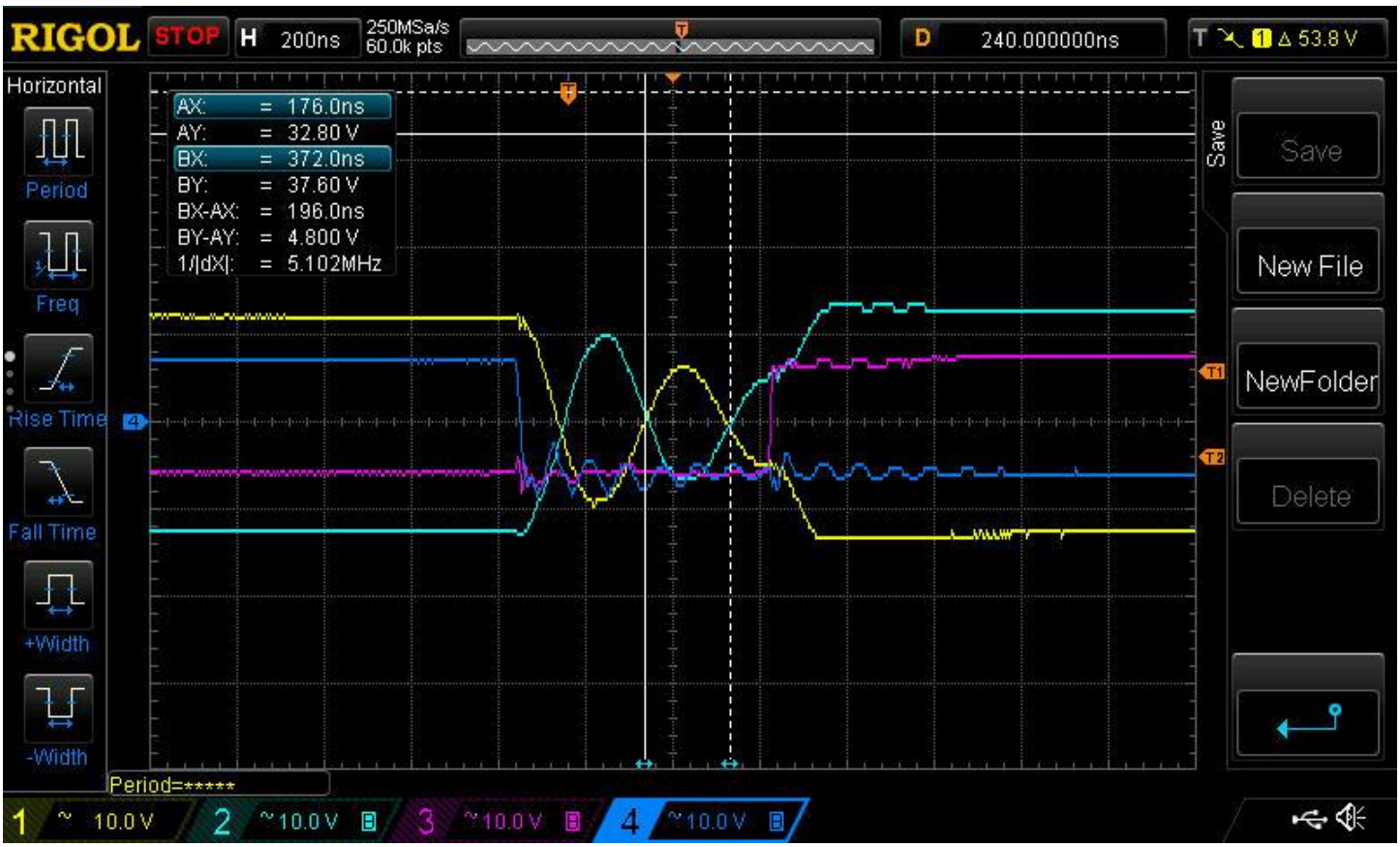

FIGURE #7 – (MAGENTA) OUTC, (BLUE) OUTD, [(YELLOW) GDT SEC OUT #1, (TEAL) GDT SEC OUT #2 -- NO MOSFET GATES ATTACHED