

UCC29002/39002 questions

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One of our customers uses UCC29002/39002 for some their products.

They would like to get the answer for the following questions about UCC29002 and UCC39002 from TI.

Could you please give me your answers for their questions?

Q1. When is disconnection switch (*1) opened? And when is it closed?

Also the switch open/close timing are different between UCC29002 and 39002?

Q2. When is the high side NMOS FET (*2) controlled by "Start Up and Adjust Logic" block turned on?

And when is the FET turned off?

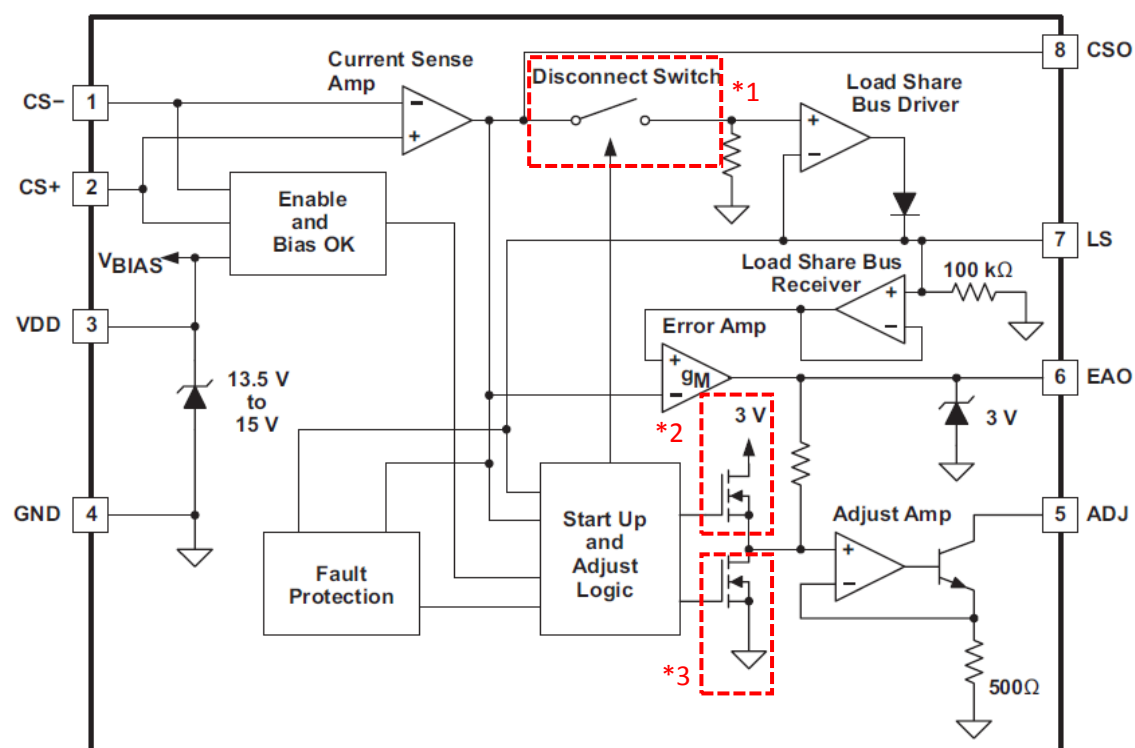
Also the FET on/off timing are different between UCC29002 and 39002?

Q3. When is the low side NMOS FET (*3) controlled by "Start Up and Adjust Logic" block turned on?

And when is the FET turned off?

Also the FET on/off timing are different between UCC29002 and 39002?

7.2 Functional Block Diagram



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