



ZHEJIANG UNIU-NE Technology CO., LTD

浙江宇力微新能源科技有限公司



U1001 Data Sheet

V 1.1

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Independent Single-Channel Gate Driver

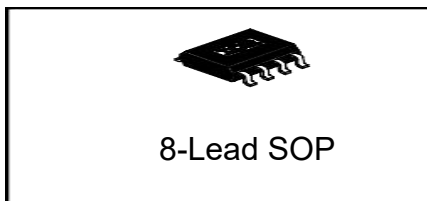
General Description

The U1001 is high voltage, high speed power MOSFET and IGBT driver with dependent high and low side referenced output channels. Proprietary HVIC and latch immune CMOS technologies enable ruggedized monolithic construction. The logic input is compatible with standard CMOS or LSTTL output, down to 3.3V logic. The output drivers feature a high pulse current buffer stage designed for minimum driver cross-conduction. The floating channel can be used to drive an N-channel power MOSFET or IGBT in the high side configuration which operates up to 30 volts.

Key Features

- Floating channel designed for bootstrap operation
- Built IN SD Function
- Tolerant to negative transient voltage dV/dt immune
- Gate drive supply range from 2.8 to 30V
- Undervoltage lockout
- 3.3V, 5V input logic compatible
- Cross-conduction prevention logic
- Internally set deadtime
- High side output in phase with input
- Shut down input turns off both channels
- Matched propagation delay for both channels

Packages



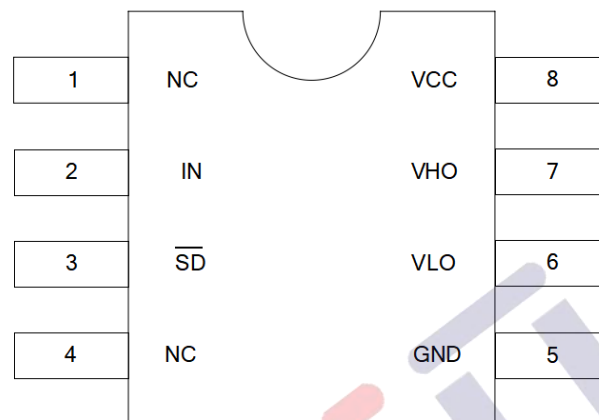
Product Summary

V _{OFFSET}	30V max
I _{O+/-}	1.2A / 1.5A
V _{CC}	8V ~ 30V
Work Tem	-40 ~150 °C

Products Information

Base Part Number	Package Type	Standard OUT		V _{OFFSET}	Logic Control
		IO+	IO-		
U1001	SOP8	1.2A	1.5A	30V	IN & $\overline{SD}$

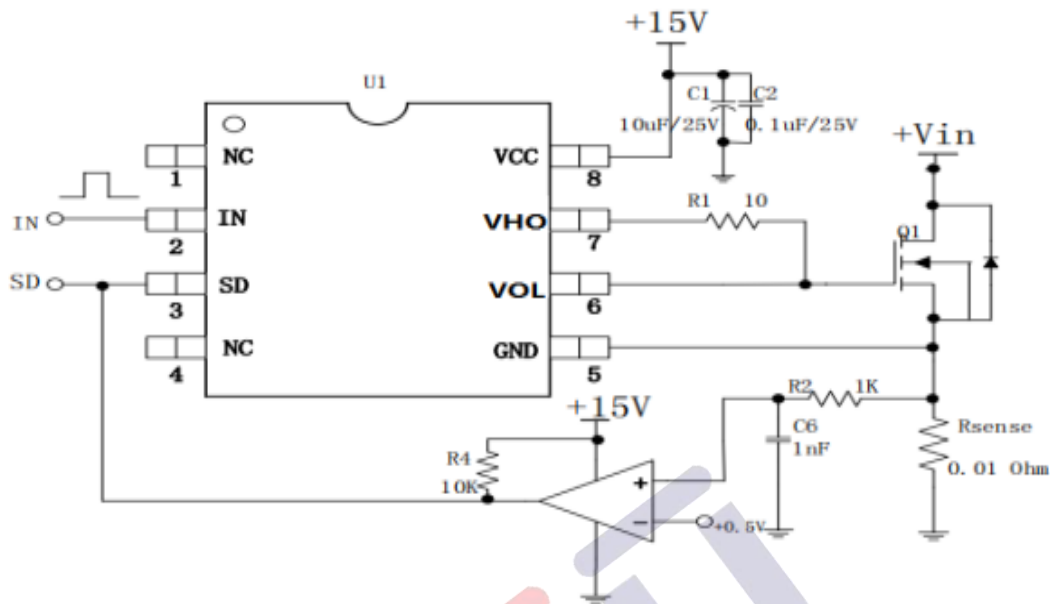
Pin Assignments



Pin Function

Number	Symbol	Description
8	VCC	Low side and logic fixed supply
2	IN	Logic input for high and low side gate driver outputs
3	$\overline{SD}$	Logic input for shutdown
5	COM	GND
6	VLO	Low side gate drive output
4	NC	
7	VHO	High side gate drive output
1	NC	

Typical Application



Absolute Maximum Ratings

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. All voltage parameters are absolute voltages referenced to COM. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions.

Symbol	Definition	Min.	Max.	Units
VHO	High side floating output voltage	-0.3	35	V
VCC	Low side and logic fixed supply voltage	-0.3	35	
VLO	Low side output voltage	-0.3	35	
VIN	Logic input voltage (IN)	-0.3	35	
SD	Logic input voltage (SD)	-0.3	7	
dVS/dt	Allowable offset supply voltage transient	—	50	V/ns
P _D	Package power dissipation @ TA ≤+25°C (8 lead SOIC)	—	0.625	W
RthJA	Thermal resistance, junction to ambient (8 lead SOIC)	—	200	°C/W
TJ	Junction temperature	—	150	°C
TS	Storage temperature	-55	150	
TL	Lead temperature (soldering, 10 seconds)	—	300	

1.版本记录

DATE	REV.	DESCRIPTION
2018/04/19	1.0	First Release
2020/05/21	1.1	Change the package

2.免责声明

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3.联系我们

浙江宇力微新能源科技有限公司

总部地址：绍兴市越城区斗门街道袍渎路25号中节能科创园45幢4/5楼

电话：0575-85087896（研发部）

传真：0575-88125157

E-mail: htw@uni-semic.com

无锡地址：无锡市新吴区纺城大道299号深港都会广场9-1401

电 话：0510-85297939

E-mail: zh@uni-semic.com

深圳地址：深圳市宝安区航城街道三围社区南昌路上合工业园B2栋501

电 话：0755-84510976

E-mail: htw@uni-semic.com