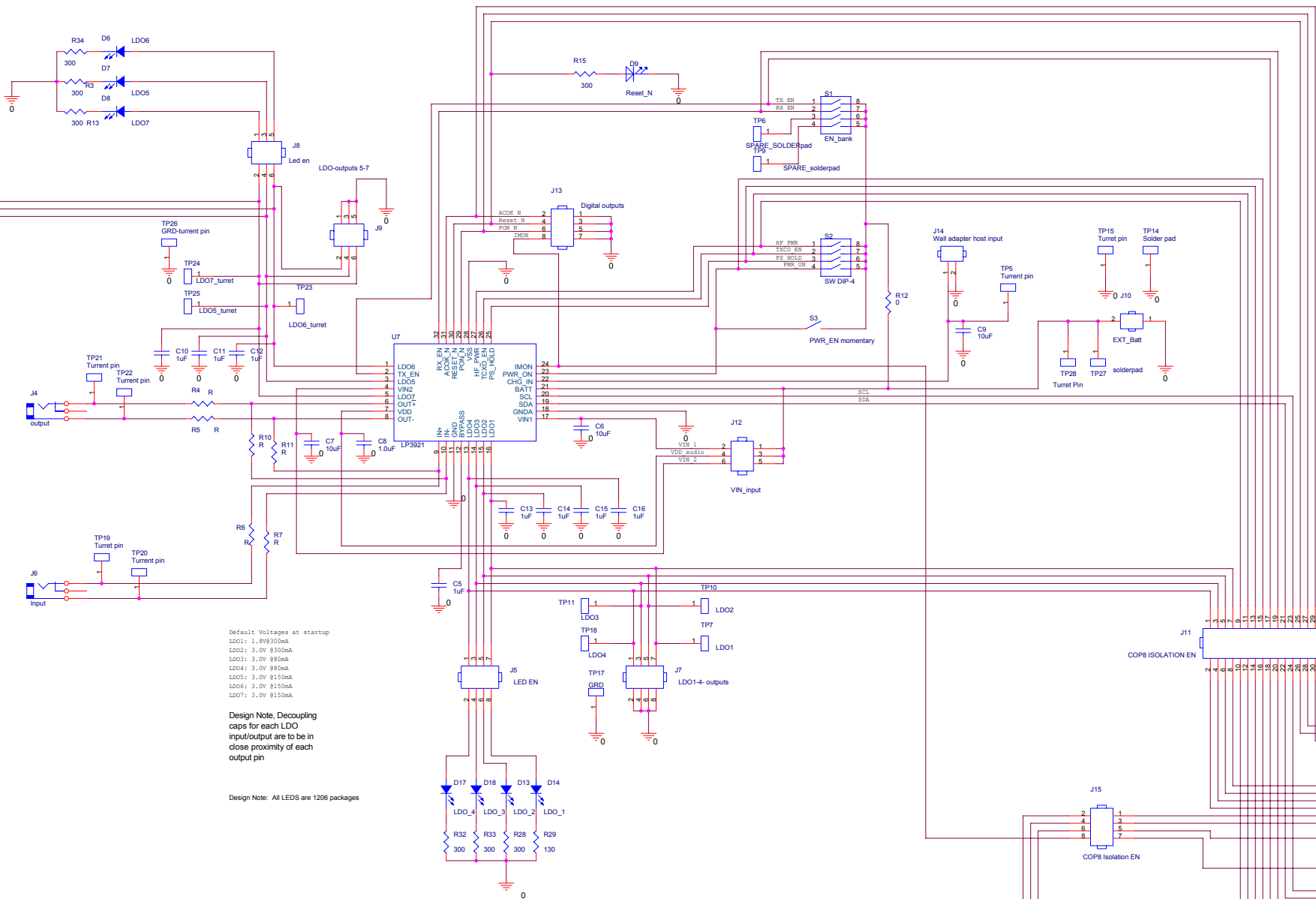
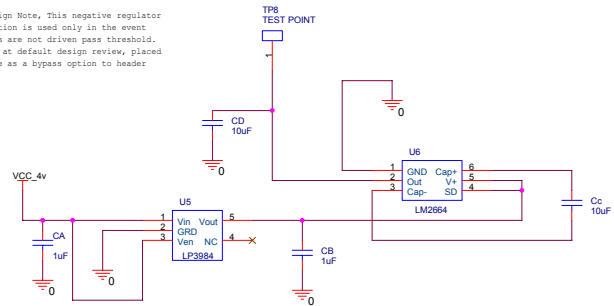


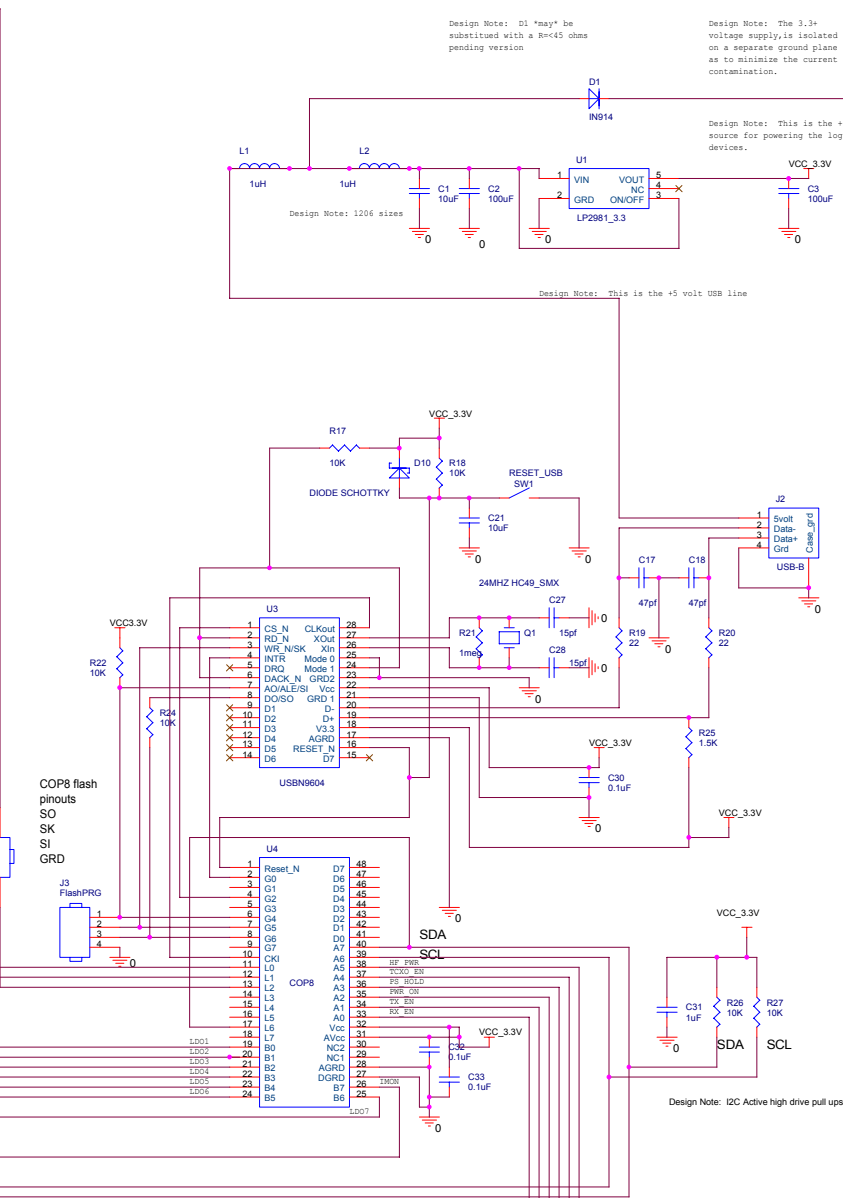
Design Note: This negative regulator section is used only in the event LEDs are not driven pass threshold. TBD at default design review, placed here as a bypass option to header



Default Voltages at startup
LDO1: 1.8V@300mA
LDO2: 3.0V @850mA
LDO3: 3.0V @880mA
LDO4: 3.0V @880mA
LDO5: 3.0V @850mA
LDO6: 3.0V @850mA
LDO7: 3.0V @850mA

Design Note: Decoupling caps for each LDO input/output are to be in close proximity of each output pin

Design Note: All LEDs are 1206 packages



Design Note: D1 *may* be substituted with a 8<45 ohms pending version

Design Note: The 3.3v voltage supply is isolated on a separate ground plane as to minimize the current contamination.

Design Note: This is the +3.3v source for powering the logic devices.

Design Note: This voltage selection jumper is to be used for selection between powering device directly from the USB port, or external PS/Battery

Design Note: For noise isolation from the digital IO and ESD latchup issues, this point is the only input entry point onto the power plane

Design Note: R13 is removed if U2 LM3588 is not stuffed

Design Note: U2 LM3588 is optional and is not stuffed if evaluation board is run either from an external PS or in demo mode via the USB port

Design Note: Pin assignments for COP8 interface
LDO1 pin16 = B0 pin 19
LDO2 pin15= B1pin 20
LDO3 pin14 = B2 pin 21
LDO4 pin13= B3 pin 22
LDO5 pin3= B4 pin 23
LDO6 pin1 = B5 pin 24
LDO7 pin5 =B6 pin 25

SDA pin 19 = A7 pin 40
SCL pin 20= A6 pin 39
RX_EN pin32 =A0 pin33
TX_EN pin23 =A1 pin 34
PWR_ON pin23 =A2 pin 35
PS_HOLD pin 25=A3 pin 36
TCXO_EN pin 26=A4 pin 37
HF_PWR pin27= A5 pin 38

Design Note: All headers and LEDs must be labeled clearly

Design Note: All LEDs are 1206 packages

Design Note: Grd Plane will consist of guarded plane to direct electron flow for most quiet operation. Digital logic interface will have separate grd shield

Design Note: Switcher components **MUST** be located as close to pins as possible. SEE LAYOUT DETAILS

File	LP3921 PMU-audio USB Design by Sheldon Mah 408 721 3526		
Size	Document Number	REV A5X5	Rev
D	RX5 package with datasheet 0.9 or greater pinout configuration		
Date:	Tuesday, December 14, 2007	Sheet	1 of 1