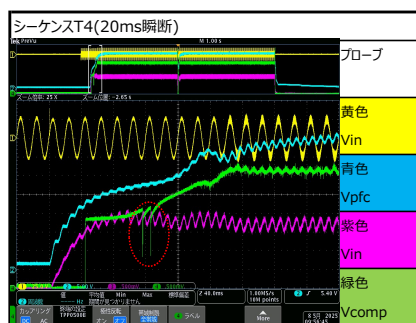


Thanks for the answer. I found the simulation to be roughly linear, although the period between the 1.9V~2.8V rise seems to be slightly different. Why is that?



Also, in the waveform I sent before, the VCOMP waveform was wobbly, but in principle, why is that? In principle, I don't think it is possible for this circuit.

Our PFC set voltage is 36V and 85% of that is 30.6V so where it is rattling around, it has not yet reached 85%. It simulates to be rattling.

Thus, I believe it is a period of 30 μ A constant current.