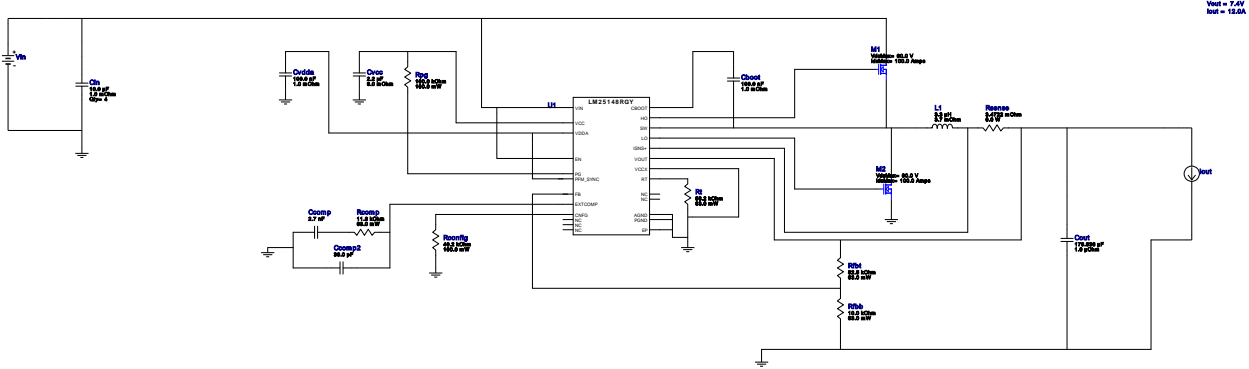


VinMin = 9.0V  
 VinMax = 36.0V  
 Vout = 7.4V  
 Iout = 12.0A

Device = LM25148RGYR  
 Topology = Buck  
 Created = 2023-12-26 14:18:31.104  
 BOM Cost = NA  
 BOM Count = 21  
 Total Pd = 4.52W

# WEBENCH<sup>®</sup> Design Report

Design : 103 LM25148RGYR  
 LM25148RGYR 9V-36V to 7.40V @ 12A

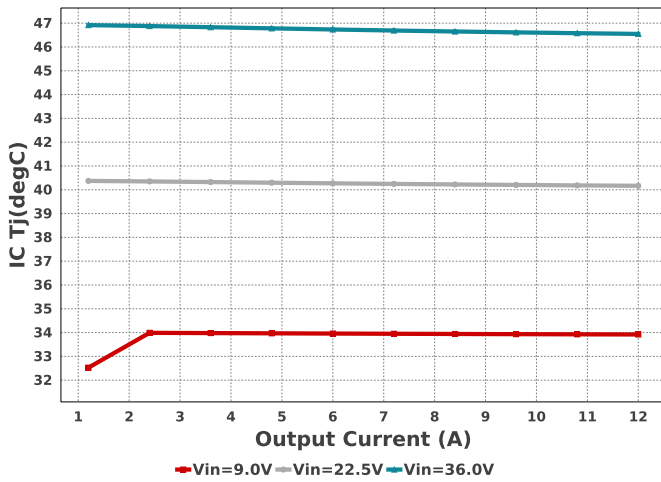


## Electrical BOM

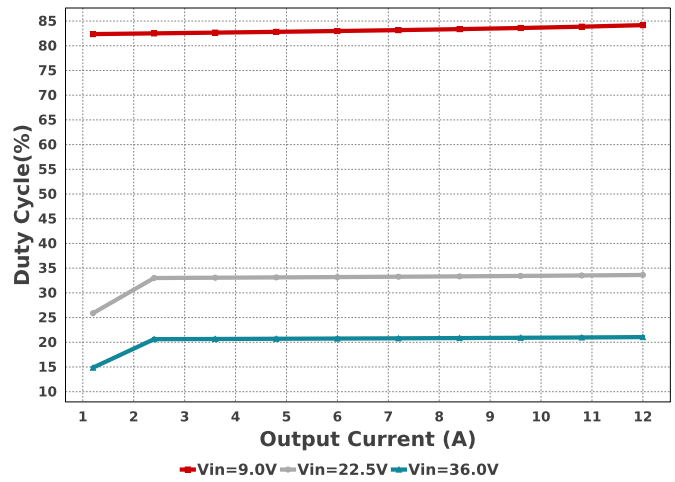
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Cboot	MuRata	GRM155R71A104KA01D Series= X7R	Cap= 100.0 nF ESR= 1.0 mOhm VDC= 10.0 V IRMS= 0.0 A	1	\$0.01	0402 3 mm <sup>2</sup>
Ccomp	MuRata	GRM1885C1H272JA01J Series= C0G/NP0	Cap= 2.7 nF VDC= 50.0 V IRMS= 0.0 A	1	\$0.02	0603 5 mm <sup>2</sup>
Ccomp2	Samsung Electro-Mechanics	CL21C390JBANNNC Series= C0G/NP0	Cap= 39.0 pF VDC= 50.0 V IRMS= 0.0 A	1	\$0.01	0805 7 mm <sup>2</sup>
Cin	MuRata	GRM32ER71J106KA12L Series= X7R	Cap= 10.0 uF ESR= 1.0 mOhm VDC= 63.0 V IRMS= 6.0 A	4	\$0.30	1210_280 15 mm <sup>2</sup>
Cout	CUSTOM	CUSTOM Series= ?	Cap= 179.636 uF ESR= 1.0 uOhm VDC= 11.1 V IRMS= 1.48578 A	1	NA	CUSTOM 0 mm <sup>2</sup>
Cvcc	MuRata	GRM188R71A225KE15D Series= X7R	Cap= 2.2 uF ESR= 9.0 mOhm VDC= 10.0 V IRMS= 3.3 A	1	\$0.02	0603 5 mm <sup>2</sup>
Cvdda	MuRata	GRM155R71A104KA01D Series= X7R	Cap= 100.0 nF ESR= 1.0 mOhm VDC= 10.0 V IRMS= 0.0 A	1	\$0.01	0402 3 mm <sup>2</sup>
L1	Coilcraft	XAL1010-332MEB	L= 3.3 µH 3.7 mOhm	1	\$1.71	XAL1010 160 mm <sup>2</sup>
M1	Texas Instruments	CSD18531Q5A	VdsMax= 60.0 V IdsMax= 100.0 Amps	1	\$0.45	TRANS_NexFET_Q5A 55 mm <sup>2</sup>

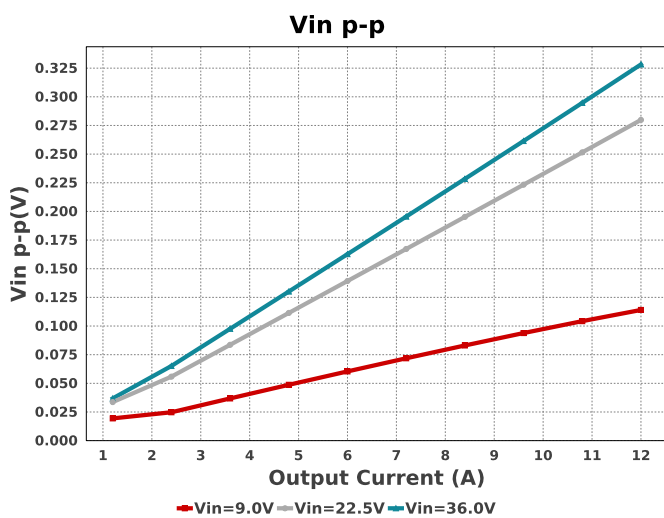
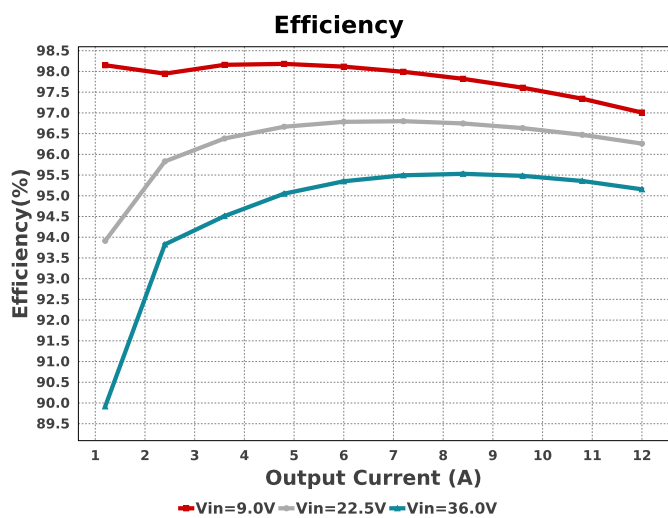
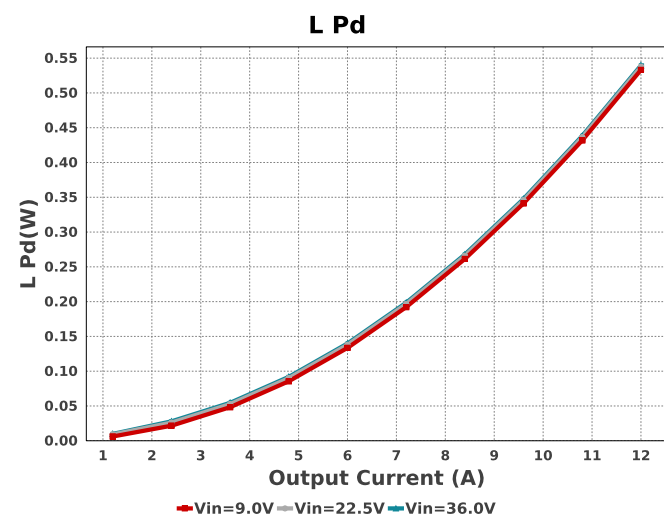
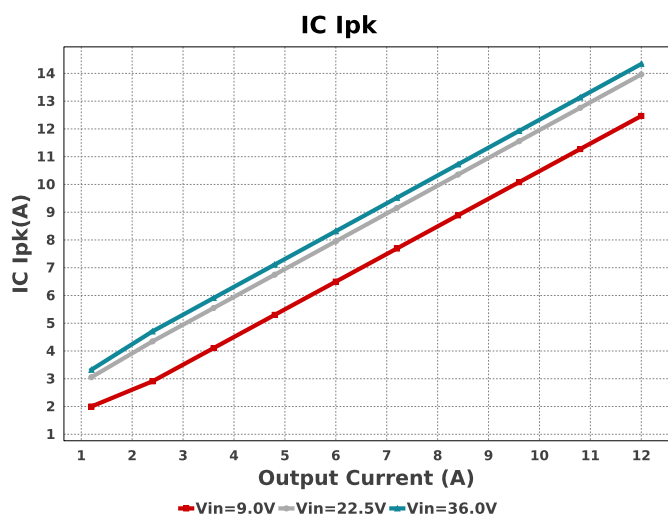
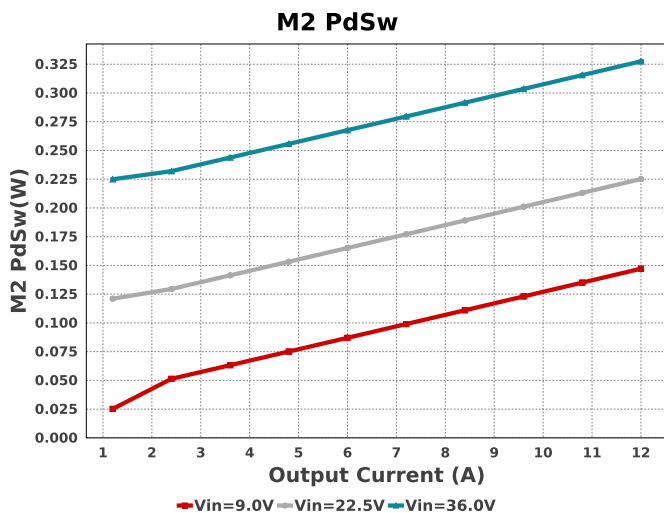
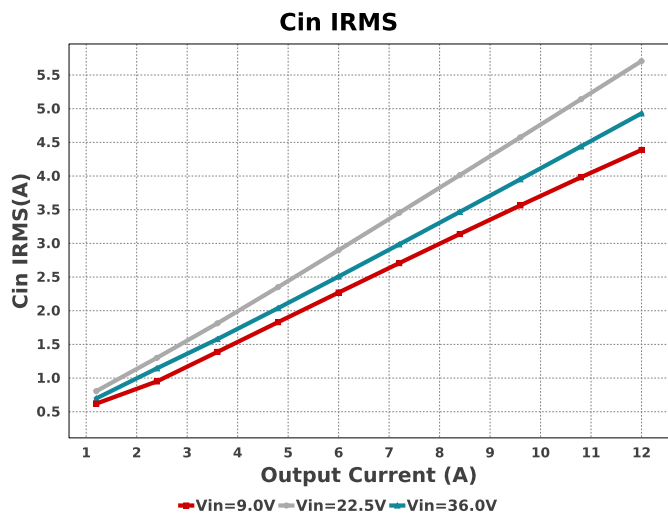
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
M2	Texas Instruments	CSD18531Q5A	VdsMax= 60.0 V IdsMax= 100.0 Amps	1	\$0.45	 TRANS_NexFET_Q5A 55 mm <sup>2</sup>
Rcomp	Vishay-Dale	CRCW040211K8FKED Series= CRCW..e3	Res= 11.8 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm <sup>2</sup>
Rconfig	Vishay-Dale	CRCW060340K2FKEA Series= CRCW..e3	Res= 40.2 kOhm Power= 100.0 mW Tolerance= 1.0%	1	\$0.01	 0603 5 mm <sup>2</sup>
Rfbb	Vishay-Dale	CRCW040210K0FKED Series= CRCW..e3	Res= 10.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm <sup>2</sup>
Rfbt	Vishay-Dale	CRCW040282K5FKED Series= CRCW..e3	Res= 82.5 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm <sup>2</sup>
Rpg	Vishay-Dale	CRCW0603100KFKEA Series= CRCW..e3	Res= 100.0 kOhm Power= 100.0 mW Tolerance= 1.0%	1	\$0.01	 0603 5 mm <sup>2</sup>
Rsense	CUSTOM	CUSTOM Series= ?	Res= 3.4722 mOhm Power= 0.0 W Tolerance= 0.0%	1	NA	CUSTOM 0 mm <sup>2</sup>
Rt	Yageo	AC0402FR-0756K2L Series= ?	Res= 56.2 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm <sup>2</sup>
U1	Texas Instruments	LM25148RGYR	Switcher	1	\$0.53	RGY0024E-MFG 48 mm <sup>2</sup>

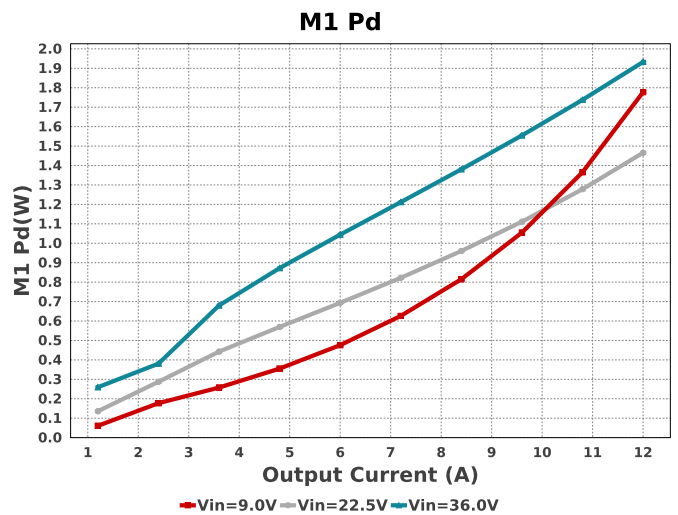
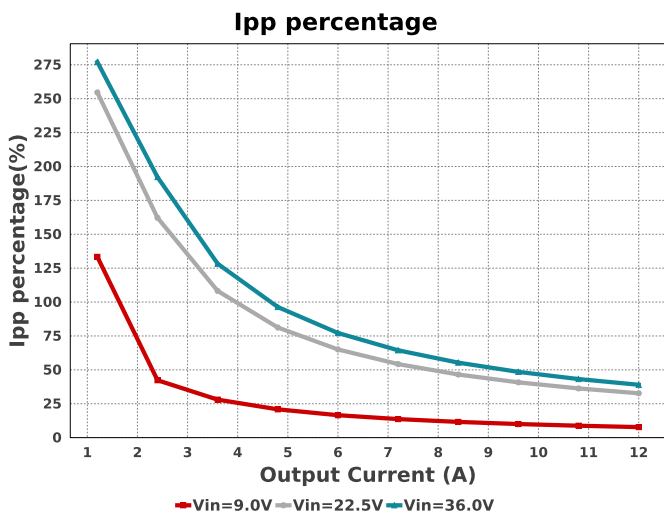
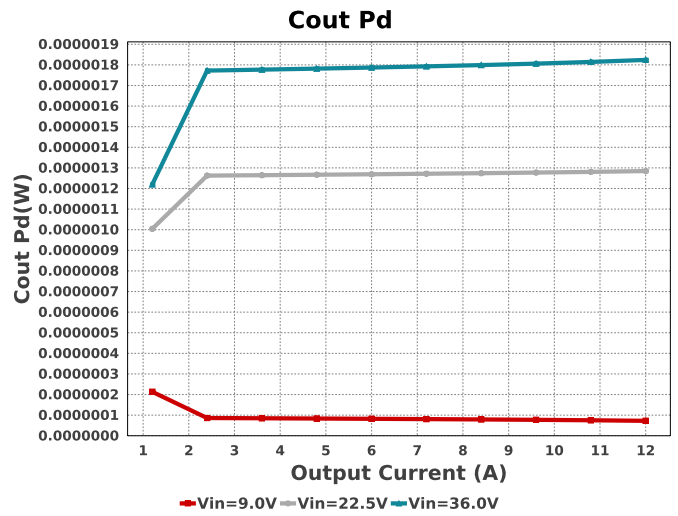
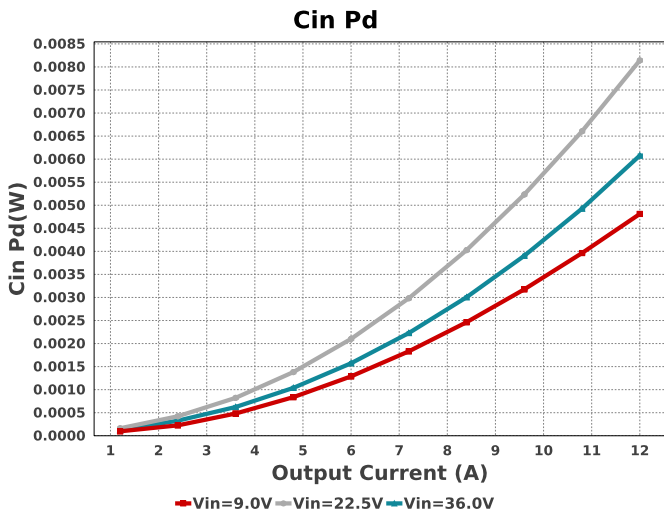
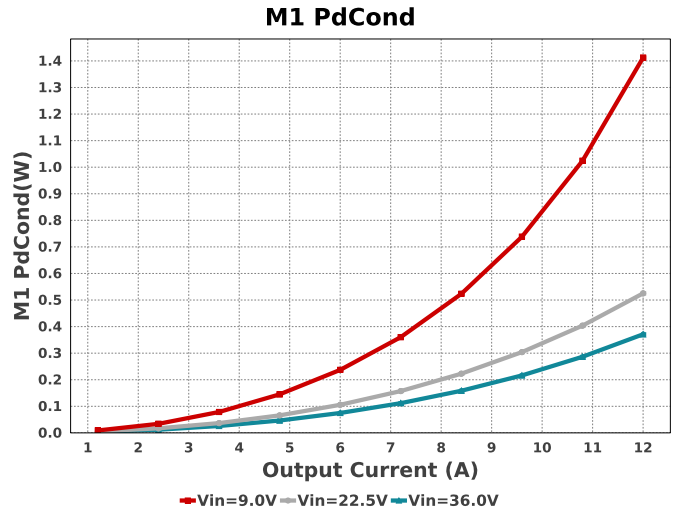
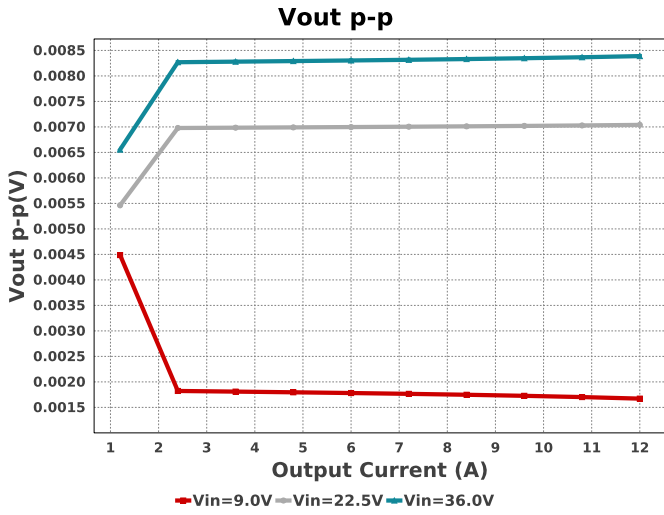
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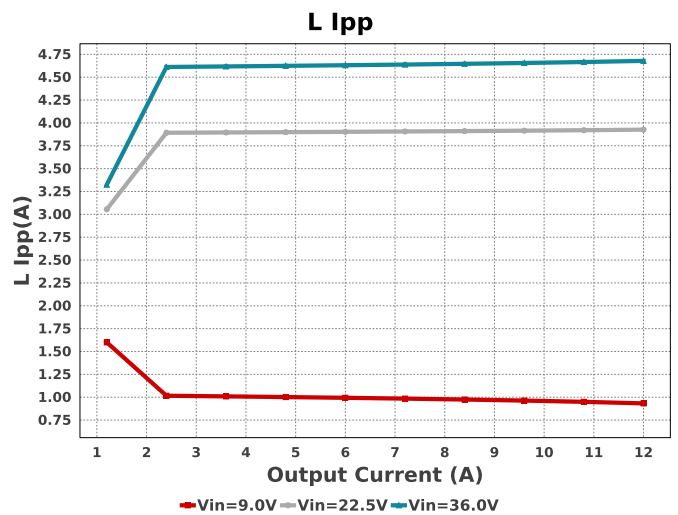
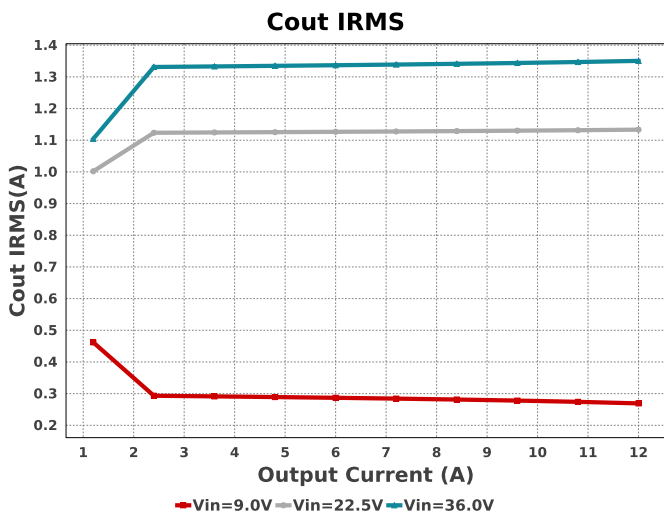
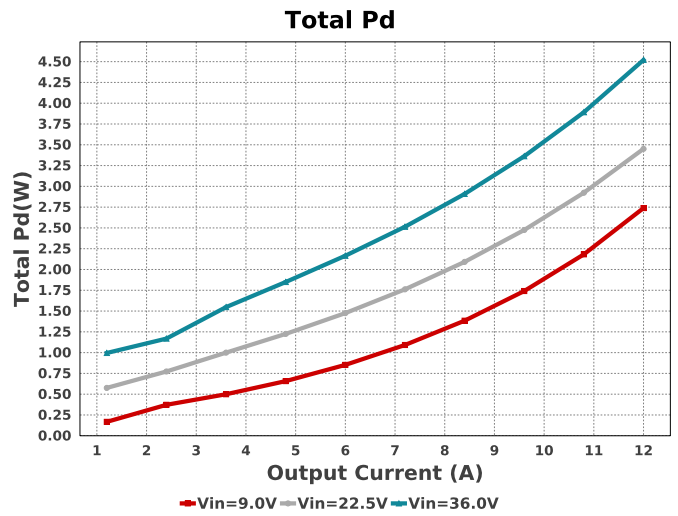
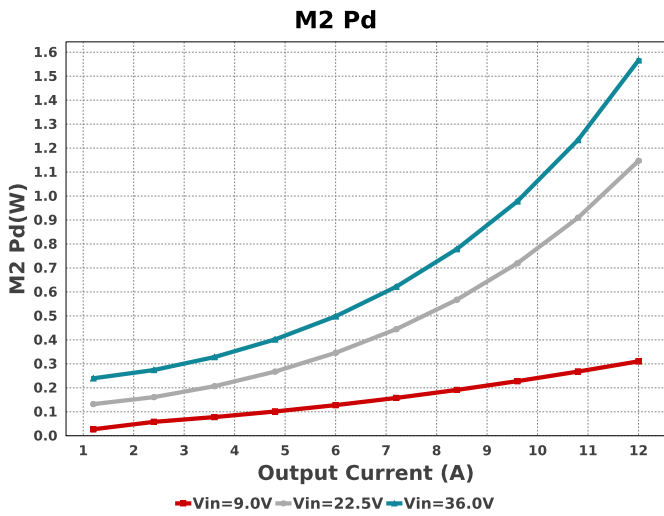
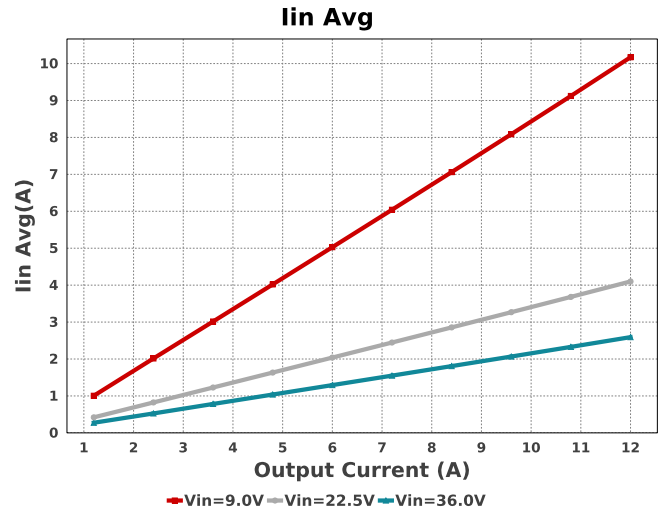
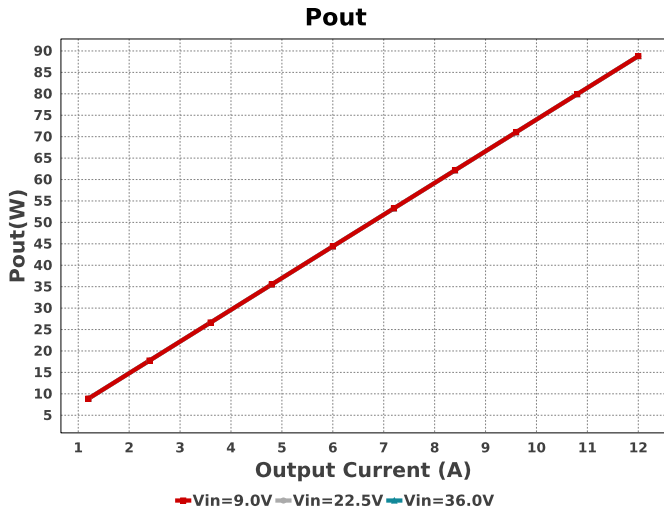


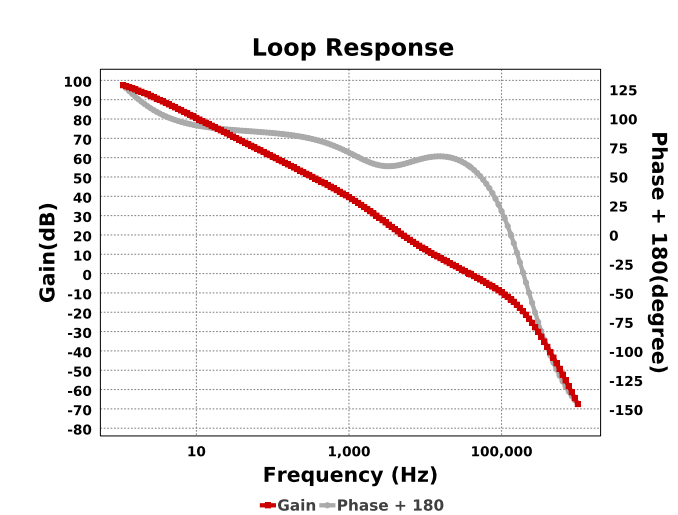
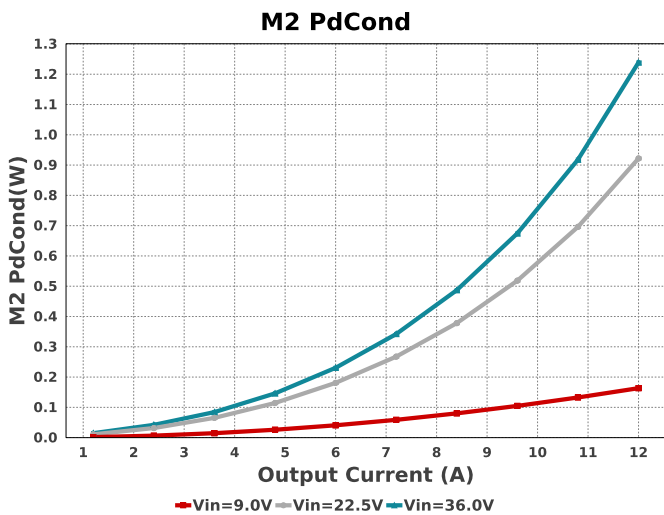
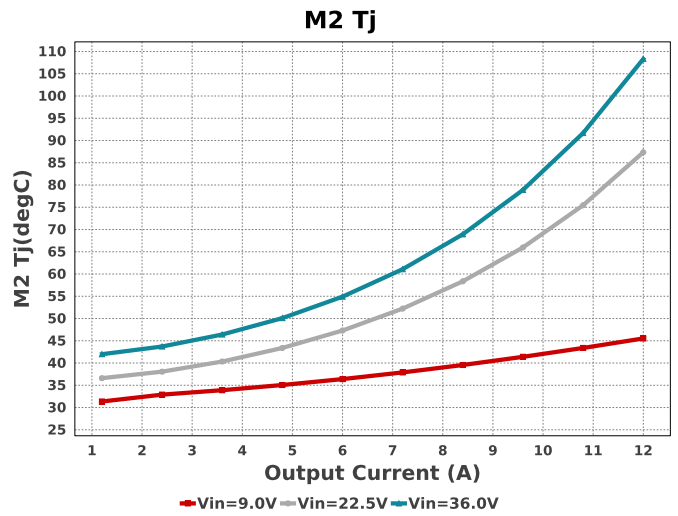
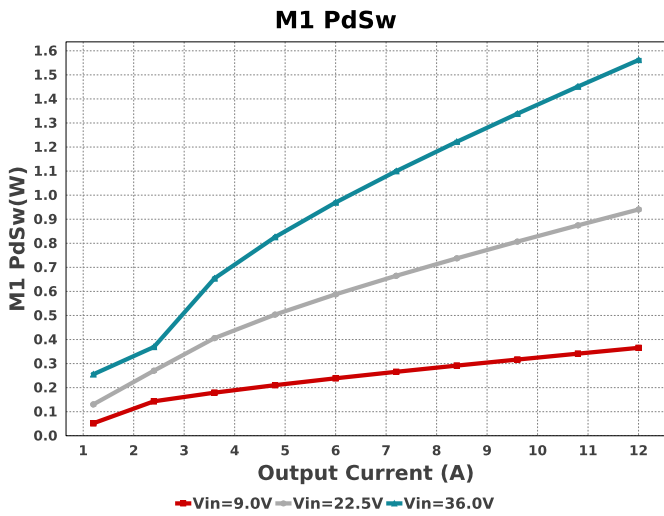
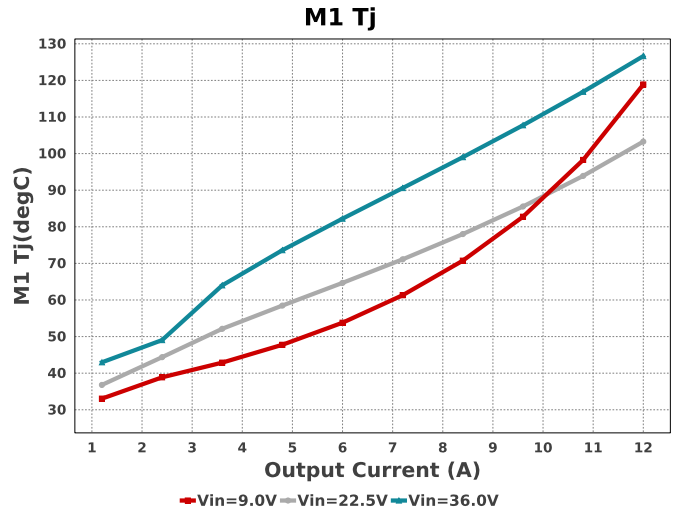
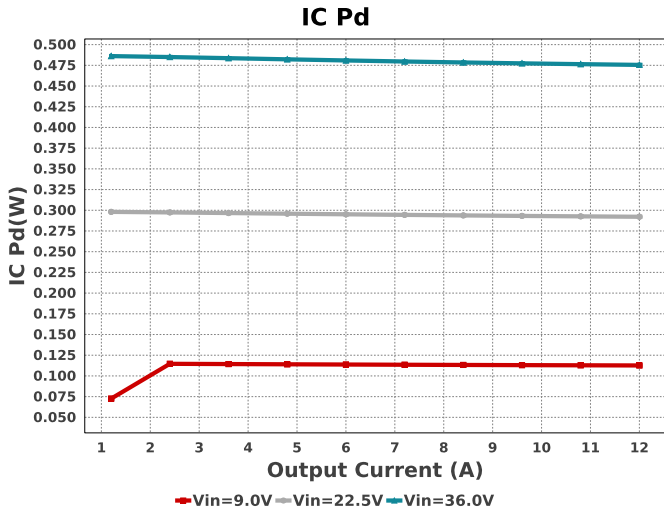
Duty Cycle

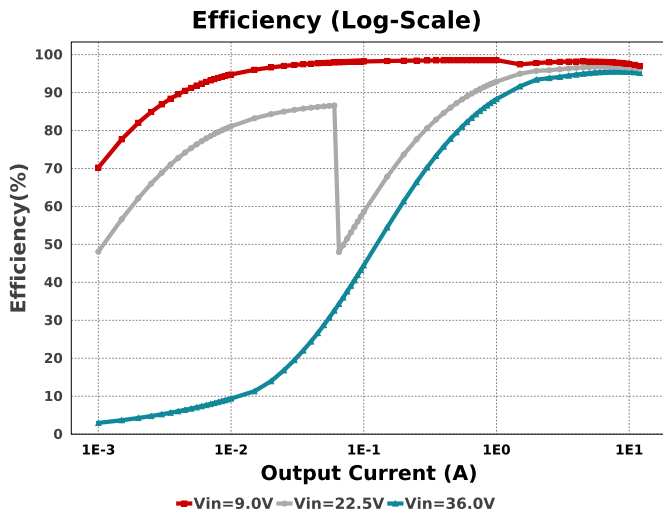












## Operating Values

#	Name	Value	Category	Description
1.	Cin IRMS	4.931 A	Capacitor	Input capacitor RMS ripple current
2.	Cin Pd	6.078 mW	Capacitor	Input capacitor power dissipation
3.	Cout IRMS	1.351 A	Capacitor	Output capacitor RMS ripple current
4.	Cout Pd	1.824 $\mu$ W	Capacitor	Output capacitor power dissipation
5.	IC Ipk	14.339 A	IC	Peak switch current in IC
6.	IC Pd	475.52 mW	IC	IC power dissipation
7.	IC Tj	46.548 degC	IC	IC junction temperature
8.	IC Tolerance	10.0 mV	IC	IC Feedback Tolerance
9.	ICThetaJA Effective	34.8 degC/W	IC	Effective IC Junction-to-Ambient Thermal Resistance
10.	Iin Avg	2.592 A	IC	Average input current
11.	Ipp percentage	38.988 %	Inductor	Inductor ripple current percentage (with respect to average inductor current)
12.	L Ipp	4.678 A	Inductor	Peak-to-peak inductor ripple current
13.	L Pd	539.55 mW	Inductor	Inductor power dissipation
14.	M1 Pd	1.933 W	Mosfet	M1 MOSFET total power dissipation
15.	M1 PdCond	371.05 mW	Mosfet	M1 MOSFET conduction losses
16.	M1 PdSw	1.562 W	Mosfet	M1 MOSFET switching losses
17.	M1 Tj	126.64 degC	Mosfet	M1 MOSFET junction temperature
18.	M2 Pd	1.566 W	Mosfet	M2 MOSFET total power dissipation
19.	M2 PdCond	1.239 W	Mosfet	M2 MOSFET conduction losses
20.	M2 PdSw	327.5 mW	Mosfet	M2 MOSFET switching losses
21.	M2 Tj	108.32 degC	Mosfet	M2 MOSFET junction temperature
22.	Cin Pd	6.078 mW	Power	Input capacitor power dissipation
23.	Cout Pd	1.824 $\mu$ W	Power	Output capacitor power dissipation
24.	IC Pd	475.52 mW	Power	IC power dissipation
25.	L Pd	539.55 mW	Power	Inductor power dissipation
26.	M1 Pd	1.933 W	Power	M1 MOSFET total power dissipation
27.	M1 PdCond	371.05 mW	Power	M1 MOSFET conduction losses
28.	M1 PdSw	1.562 W	Power	M1 MOSFET switching losses
29.	M2 Pd	1.566 W	Power	M2 MOSFET total power dissipation
30.	M2 PdCond	1.239 W	Power	M2 MOSFET conduction losses
31.	M2 PdSw	327.5 mW	Power	M2 MOSFET switching losses
32.	Total Pd	4.52 W	Power	Total Power Dissipation
33.	BOM Count	21	System	Total Design BOM count
34.	Cross Freq	38.004 kHz	System	Bode plot crossover frequency
35.	Duty Cycle	21.047 %	System	Duty cycle
36.	Efficiency	95.156 %	System	Steady state efficiency
37.	FootPrint	465.0 mm <sup>2</sup>	System	Total Foot Print Area of BOM components
38.	Frequency	387.297 kHz	System	Switching frequency
39.	Gain Marg	-13.492 dB	System	Bode Plot Gain Margin
40.	Iout	12.0 A	System	Iout operating point
41.	Iout transient step used 6.0 A for Cout calculations		System	Custom Transient current step requirement that was used for Cout selection (A).



#	Name	Value	Category	Description
42.	Low Freq Gain	97.49 dB	System Information	Gain at 1Hz
43.	Mode	CCM	System Information	Conduction Mode
44.	Overshoot Value	44.685 mV	System Information	Theoretical Vout Overshoot Value
45.	Phase Marg	59.774 deg	System Information	Bode Plot Phase Margin
46.	Pout	88.8 W	System Information	Total output power
47.	Total BOM	NA	System Information	Total BOM Cost
48.	Undershoot Value	80.076 mV	System Information	Theoretical Vout Undershoot Value
49.	Vin	36.0 V	System Information	Vin operating point
50.	Vin p-p	328.242 mV	System Information	Peak-to-peak input voltage
51.	Vout	7.4 V	System Information	Operational Output Voltage
52.	Vout Actual	7.4 V	System Information	Vout Actual calculated based on selected voltage divider resistors
53.	Vout Ripple requirement used for Cout calculations	1.0 %	System Information	Custom maximum output ripple requirement that was used for Cout selection(% of Vout).
54.	Vout Tolerance	3.074 %	System Information	Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable
55.	Vout p-p	8.406 mV	System Information	Peak-to-peak output ripple voltage
56.	Vout transient requirement used for Cout calculations	3.0 %	System Information	Custom Transient voltage change requirement that was used for Cout selection (% of Vout).

## Design Inputs

Name	Value	Description
Iout	12.0	Maximum Output Current
VinMax	36.0	Maximum input voltage
VinMin	9.0	Minimum input voltage
Vout	7.4	Output Voltage
base_pn	LM25148	Base Product Number
source	DC	Input Source Type
Ta	30.0	Ambient temperature
UserFsw	102.796 k	Customer Selected Frequency



## WEBENCH® Assembly

### Component Testing

Some published data on components in datasheets such as Capacitor ESR and Inductor DC resistance is based on conservative values that will guarantee that the components always exceed the specification. For design purposes it is usually better to work with typical values. Since this data is not always available it is a good practice to measure the Capacitance and ESR values of  $C_{in}$  and  $C_{out}$ , and the inductance and DC resistance of L1 before assembly of the board. Any large discrepancies in values should be electrically simulated in WEBENCH to check for instabilities and thermally simulated in WebTHERM to make sure critical temperatures are not exceeded.

### Soldering Component to Board

If board assembly is done in house it is best to tack down one terminal of a component on the board then solder the other terminal. For surface mount parts with large tabs, such as the DPAK, the tab on the back of the package should be pre-tinned with solder, then tacked into place by one of the pins. To solder the tab down to the board place the iron down on the board while resting against the tab, heating both surfaces simultaneously. Apply light pressure to the top of the plastic case until the solder flows around the part and the part is flush with the PCB. If the solder is not flowing around the board you may need a higher wattage iron (generally 25W to 30W is enough).

### Initial Startup of Circuit

It is best to initially power up the board by setting the input supply voltage to the lowest operating input voltage 9.0V and set the input supply's current limit to zero. With the input supply off connect up the input supply to  $V_{in}$  and GND. Connect a digital volt meter and a load if needed to set the minimum load of the design from  $V_{out}$  and GND. Turn on the input supply and slowly turn up the current limit on the input supply. If the voltage starts to rise on the input supply continue increasing the input supply current limit while watching the output voltage. If the current increases on the input supply, but the voltage remains near zero, then there may be a short or a component misplaced on the board. Power down the board and visually inspect for solder bridges and recheck the diode and capacitor polarities. Once the power supply circuit is operational then more extensive testing may include full load testing, transient load and line tests to compare with simulation results.

### Load Testing

The setup is the same as the initial startup, except that an additional digital voltmeter is connected between  $V_{in}$  and GND, a load is connected between  $V_{out}$  and GND and a current meter is connected in series between  $V_{out}$  and the load. The load must be able to handle at least rated output power + 50% ( 7.5 watts for this design). Ideally the load is supplied in the form of a variable load test unit. It can also be done in the form of suitably large power resistors. When using an oscilloscope to measure waveforms on the prototype board, the ground leads of the oscilloscope probes should be as short as possible and the area of the loop formed by the ground lead should be kept to a minimum. This will help reduce ground lead inductance and eliminate EMI noise that is not actually present in the circuit.



### Design Assistance

1. Master key : E917F4D818BA479D[v1]
2. **LM25148** Product Folder : <http://www.ti.com/product/LM25148> : contains the data sheet and other resources.

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