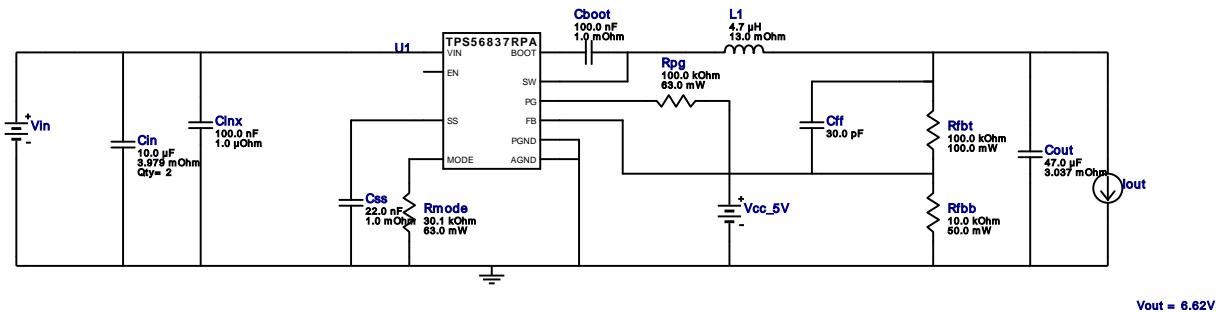


**WEBENCH® Design Report**

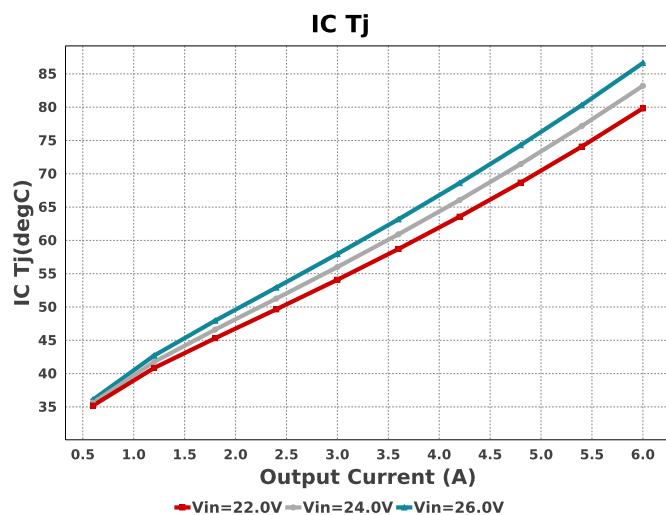
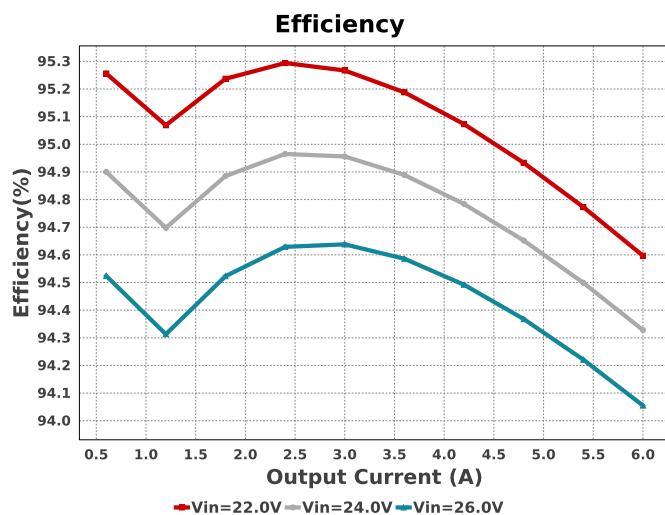
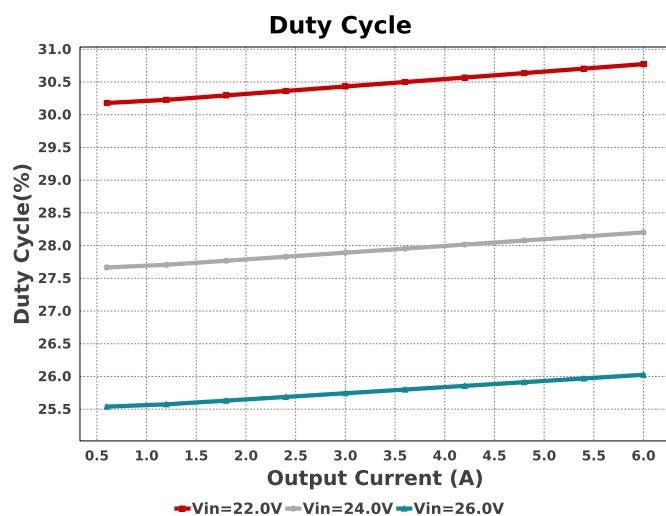
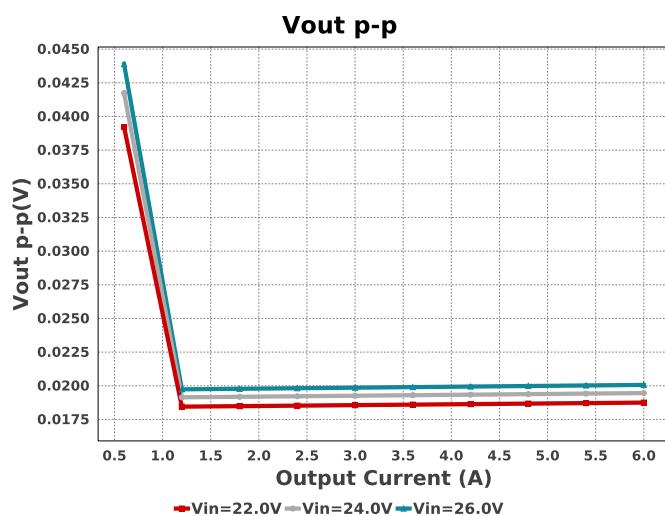
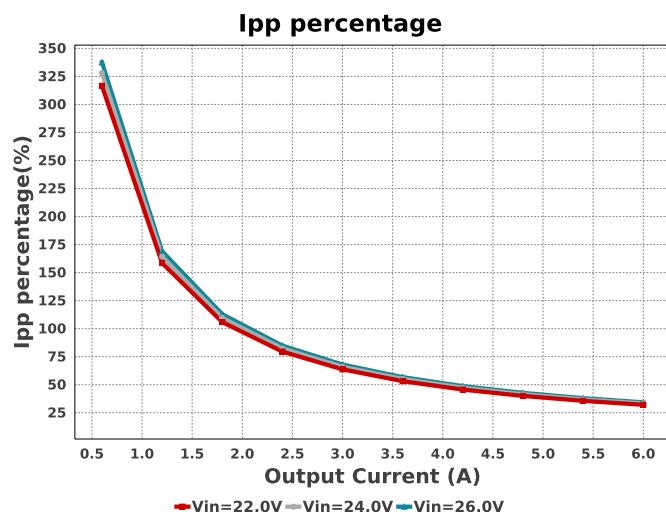
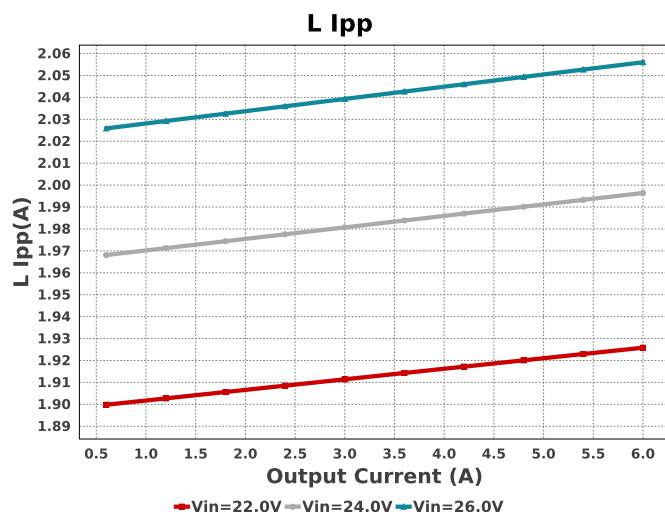
Design : 1 TPS56837RPAR  
TPS56837RPAR 5.5V-28V to 5.00V @ 8A

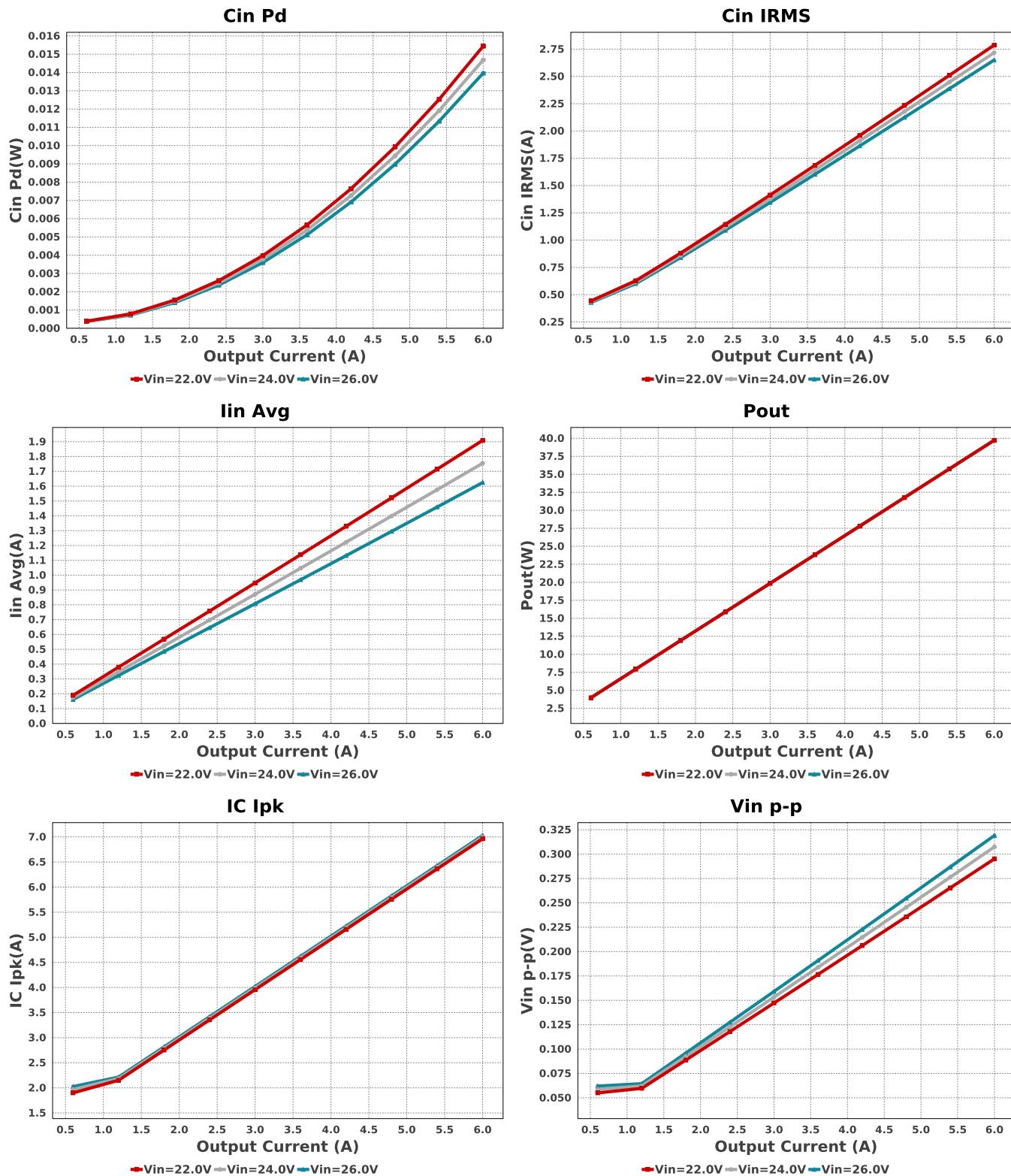
VinMin = 22.0V  
VinMax = 26.0V  
Vout = 6.62V  
Iout = 6.0A  
Device = TPS56837RPAR  
Topology = Buck  
Created = 2025-10-28 03:26:40.782  
BOM Cost = NA  
BOM Count = 13  
Total Pd = 2.51W

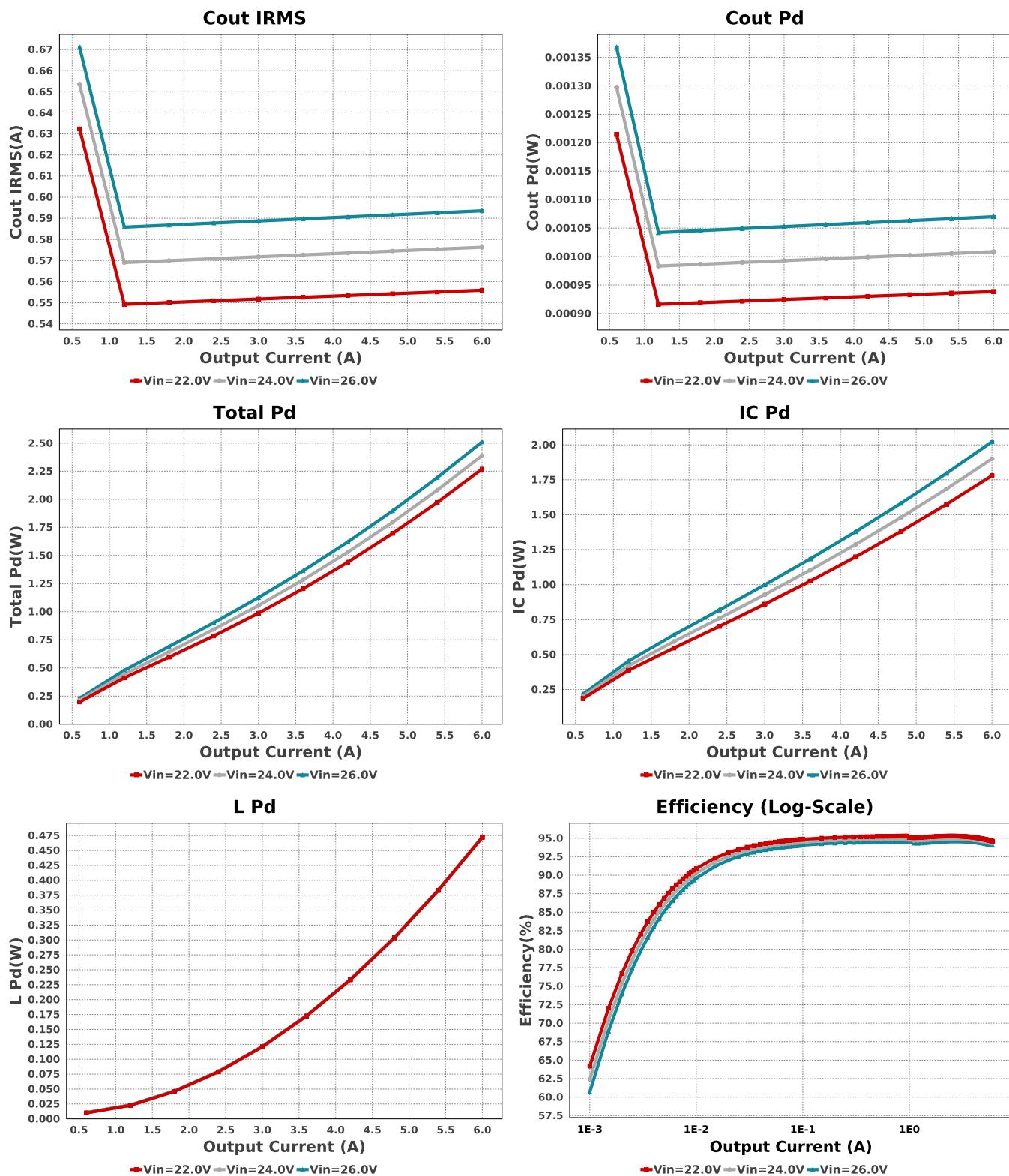

**Electrical BOM**

Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Cboot	MuRata	GRM155R71C104KA88D Series= X7R	Cap= 100.0 nF ESR= 1.0 mΩ VDC= 16.0 V IRMS= 0.0 A	1	\$0.01	■ 0402 3 mm <sup>2</sup>
Cff	AVX	06033A300FAT2A Series= C0G/NP0	Cap= 30.0 pF VDC= 25.0 V IRMS= 0.0 A	1	\$0.10	■ 0603 5 mm <sup>2</sup>
Cin	Taiyo Yuden	MSASU32MSB5106KPNA01 Series= X5R	Cap= 10.0 uF ESR= 3.979 mΩ VDC= 50.0 V IRMS= 3.4821 A	2	\$0.16	■ 1210 15 mm <sup>2</sup>
Cinx	CUSTOM	CUSTOM Series= ?	Cap= 100.0 nF ESR= 1.0 uΩ VDC= 39.0 V IRMS= 36.615 mA	1	NA	CUSTOM 0 mm <sup>2</sup>
Cout	MuRata	GRM32ER61C476KE15L Series= X5R	Cap= 47.0 uF ESR= 3.037 mΩ VDC= 16.0 V IRMS= 4.59346 A	1	\$0.17	■ 1210_280 15 mm <sup>2</sup>
Css	MuRata	GRM155R71C223KA01D Series= X7R	Cap= 22.0 nF ESR= 1.0 mΩ VDC= 16.0 V IRMS= 0.0 A	1	\$0.01	■ 0402 3 mm <sup>2</sup>
L1	Coilcraft	XAL7070-472MEB	L= 4.7 μH 13.0 mΩ	1	\$1.19	■ XAL7070 87 mm <sup>2</sup>
Rfbb	Yageo	RC0201FR-0710KL Series= ?	Res= 10.0 kΩ Power= 50.0 mW Tolerance= 1.0%	1	\$0.01	■ 0201 2 mm <sup>2</sup>
Rfbt	Yageo	RC0603FR-07100KL Series= ?	Res= 100.0 kΩ Power= 100.0 mW Tolerance= 1.0%	1	\$0.01	■ 0603 5 mm <sup>2</sup>
Rmode	Vishay-Dale	CRCW040230K1FKED Series= CRCW..e3	Res= 30.1 kΩ Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	■ 0402 3 mm <sup>2</sup>

Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Rpg	Vishay-Dale	CRCW0402100KFKED Series= CRCW..e3	Res= 100.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm <sup>2</sup>
U1	Texas Instruments	TPS56837RPAR	Switcher	1	\$1.15	RPA0010A 16 mm <sup>2</sup>







## Operating Values

#	Name	Value	Category	Description
1.	Cin IRMS	2.65 A	Capacitor	Input capacitor RMS ripple current
2.	Cin Pd	13.971 mW	Capacitor	Input capacitor power dissipation
3.	Cout IRMS	593.527 mA	Capacitor	Output capacitor RMS ripple current
4.	Cout Pd	1.07 mW	Capacitor	Output capacitor power dissipation
5.	IC Ipk	7.028 A	IC	Peak switch current in IC
6.	IC Pd	2.023 W	IC	IC power dissipation
7.	IC T <sub>j</sub>	86.633 degC	IC	IC junction temperature
8.	IC Tolerance	6.0 mV	IC	IC Feedback Tolerance
9.	IC ThetaJA	28.0 degC/W	IC	IC junction-to-ambient thermal resistance
10.	Iin Avg	1.624 A	IC	Average input current

#	Name	Value	Category	Description
11.	Ipp percentage	34.267 %	Inductor	Inductor ripple current percentage (with respect to average inductor current)
12.	L Ipp	2.056 A	Inductor	Peak-to-peak inductor ripple current
13.	L Pd	472.58 mW	Inductor	Inductor power dissipation
14.	Cin Pd	13.971 mW	Power	Input capacitor power dissipation
15.	Cout Pd	1.07 mW	Power	Output capacitor power dissipation
16.	IC Pd	2.023 W	Power	IC power dissipation
17.	L Pd	472.58 mW	Power	Inductor power dissipation
18.	Total Pd	2.511 W	Power	Total Power Dissipation
19.	BOM Count	13	System	Total Design BOM count
20.	Duty Cycle	26.026 %	System	Duty cycle
21.	Efficiency	94.055 %	System	Steady state efficiency
22.	FootPrint	205.0 mm <sup>2</sup>	System	Total Foot Print Area of BOM components
23.	Frequency	516.546 kHz	System	Switching frequency
24.	Iout	6.0 A	System	Iout operating point
25.	Mode	CCM	System	Conduction Mode
26.	Pout	39.72 W	System	Total output power
27.	Total BOM	NA	System	Total BOM Cost
28.	Vin	26.0 V	System	Vin operating point
29.	Vin p-p	319.106 mV	System	Peak-to-peak input voltage
30.	Vout	6.62 V	System	Operational Output Voltage
31.	Vout Actual	6.6 V	System	Vout Actual calculated based on selected voltage divider resistors
32.	Vout Tolerance	2.855 %	System	Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable
33.	Vout p-p	20.064 mV	System	Peak-to-peak output ripple voltage
			Information	

## Design Inputs

Name	Value	Description
Iout	6.0	Maximum Output Current
VinMax	26.0	Maximum input voltage
VinMin	22.0	Minimum input voltage
Vout	6.62	Output Voltage
base_pn	TPS56837	Base Product Number
source	DC	Input Source Type
Ta	30.0	Ambient temperature

## WEBENCH® Assembly

## Component Testing

Some published data on components in datasheets such as Capacitor ESR and Inductor DC resistance is based on conservative values that will guarantee that the components always exceed the specification. For design purposes it is usually better to work with typical values. Since this data is not always available it is a good practice to measure the Capacitance and ESR values of  $C_{in}$  and  $C_{out}$ , and the inductance and DC resistance of  $L_1$  before assembly of the board. Any large discrepancies in values should be electrically simulated in WEBENCH to check for instabilities and thermally simulated in WebTHERM to make sure critical temperatures are not exceeded.

## Soldering Component to Board

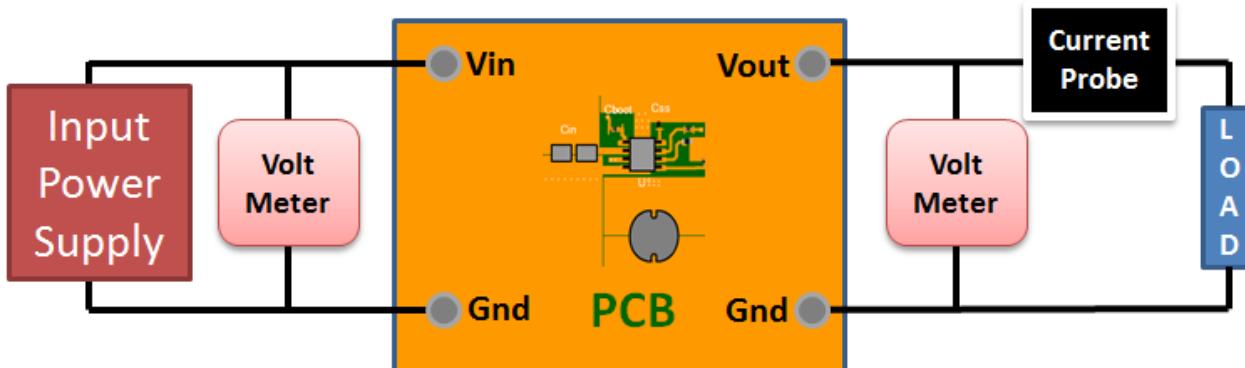
If board assembly is done in house it is best to tack down one terminal of a component on the board then solder the other terminal. For surface mount parts with large tabs, such as the DPAK, the tab on the back of the package should be pre-tinned with solder, then tacked into place by one of the pins. To solder the tab down to the board place the iron down on the board while resting against the tab, heating both surfaces simultaneously. Apply light pressure to the top of the plastic case until the solder flows around the part and the part is flush with the PCB. If the solder is not flowing around the board you may need a higher wattage iron (generally 25W to 30W is enough).

## Initial Startup of Circuit

It is best to initially power up the board by setting the input supply voltage to the lowest operating input voltage 22.0V and set the input supply's current limit to zero. With the input supply off connect up the input supply to Vin and GND. Connect a digital volt meter and a load if needed to set the minimum Iout of the design from Vout and GND. Turn on the input supply and slowly turn up the current limit on the input supply. If the voltage starts to rise on the input supply continue increasing the input supply current limit while watching the output voltage. If the current increases on the input supply, but the voltage remains near zero, then there may be a short or a component misplaced on the board. Power down the board and visually inspect for solder bridges and recheck the diode and capacitor polarities. Once the power supply circuit is operational then more extensive testing may include full load testing, transient load and line tests to compare with simulation results.

## Load Testing

The setup is the same as the initial startup, except that an additional digital voltmeter is connected between Vin and GND, a load is connected between Vout and GND and a current meter is connected in series between Vout and the load. The load must be able to handle at least rated output power + 50% ( 7.5 watts for this design). Ideally the load is supplied in the form of a variable load test unit. It can also be done in the form of suitably large power resistors. When using an oscilloscope to measure waveforms on the prototype board, the ground leads of the oscilloscope probes should be as short as possible and the area of the loop formed by the ground lead should be kept to a minimum. This will help reduce ground lead inductance and eliminate EMI noise that is not actually present in the circuit.

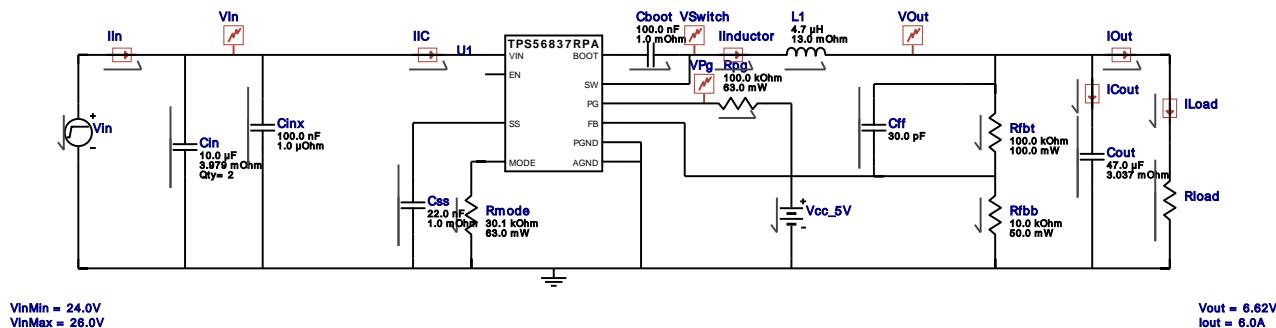


## WEBENCH® Electrical Simulation Report

Design Id = 1

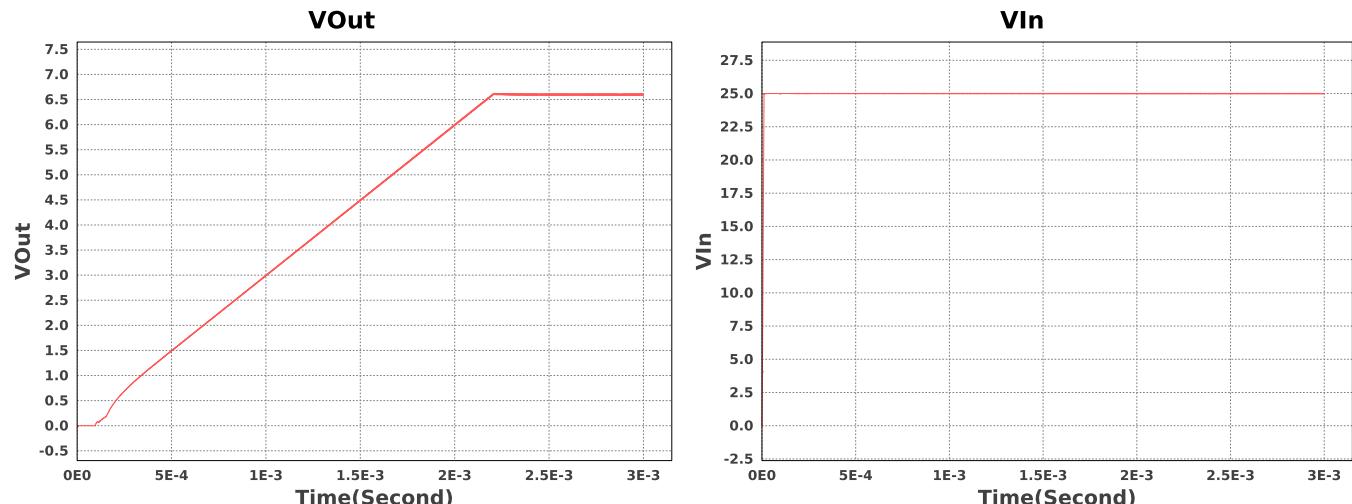
sim\_id = 1

Simulation Type = Startup



## Simulation Parameters

#	Name	Parameter Name	Description	Values
1.	Rload	R	Load Resistance	1.1033333333333333



## Design Assistance

1. Master key : 3090A28827CB0CC389CEC5F671D36AF3[v1]
2. **TPS56837 Product Folder** : <http://www.ti.com/product/TPS56837> : contains the data sheet and other resources.

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