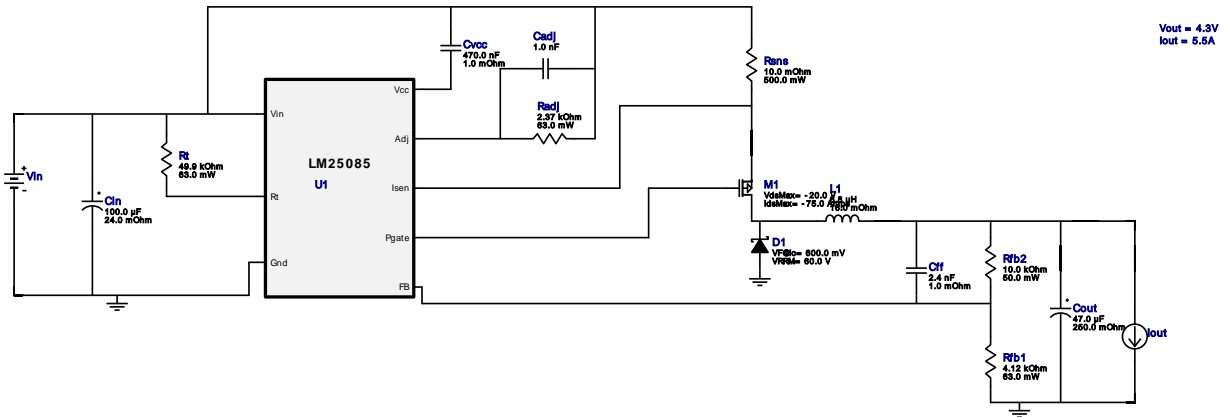






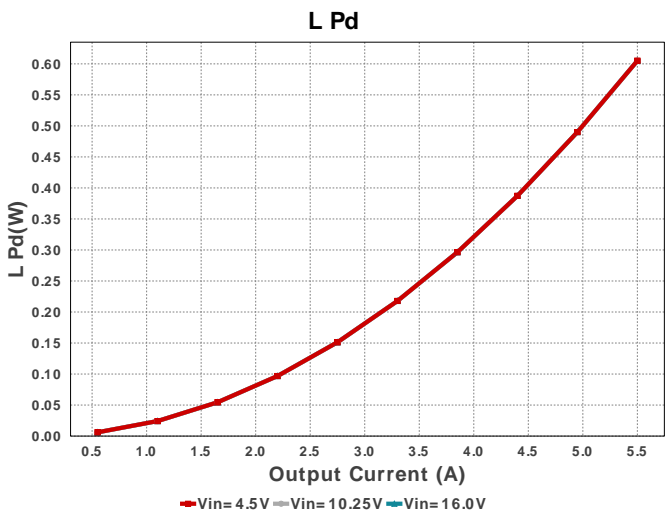
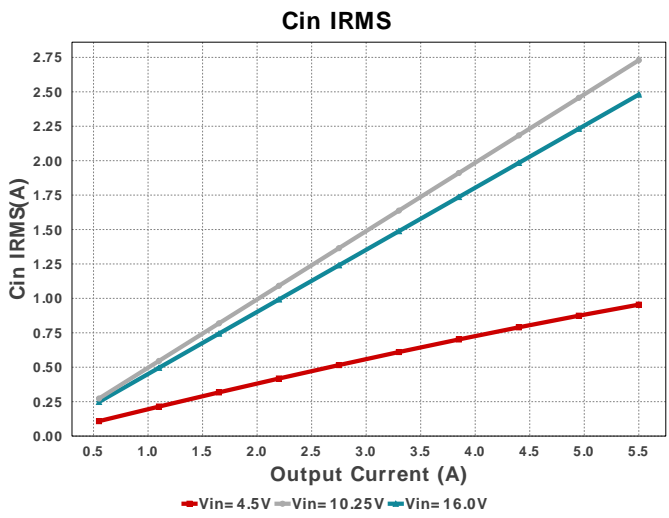
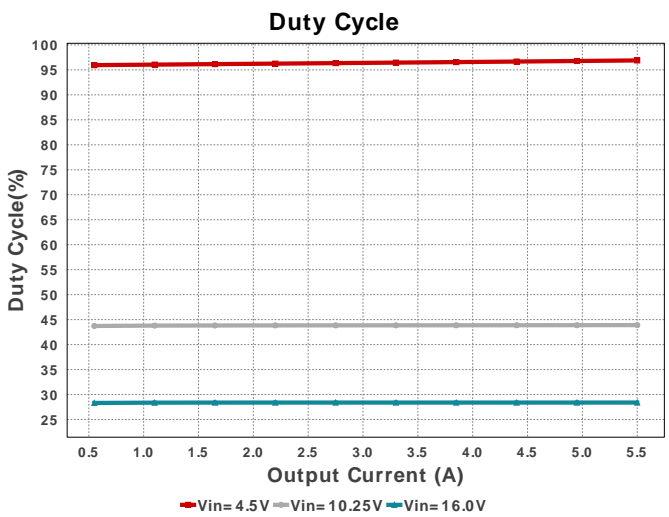
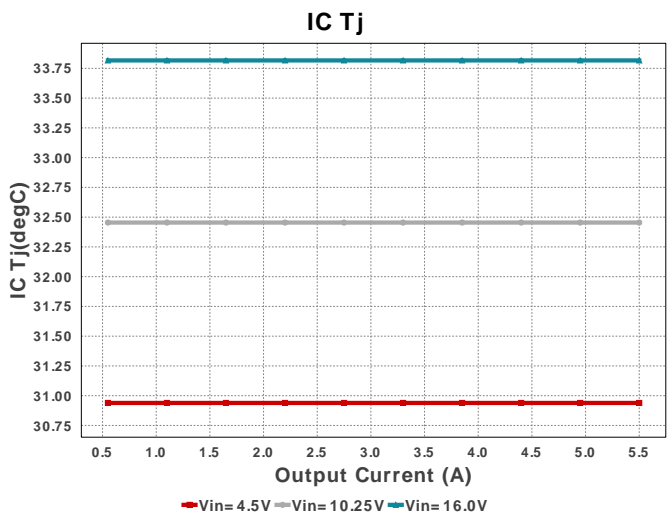


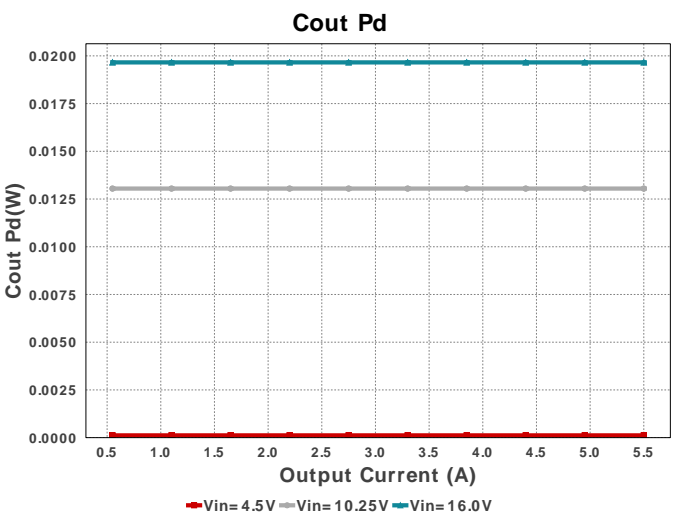
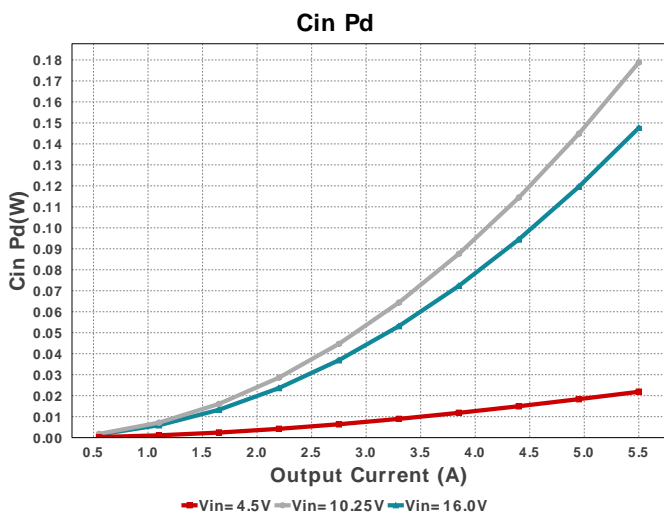
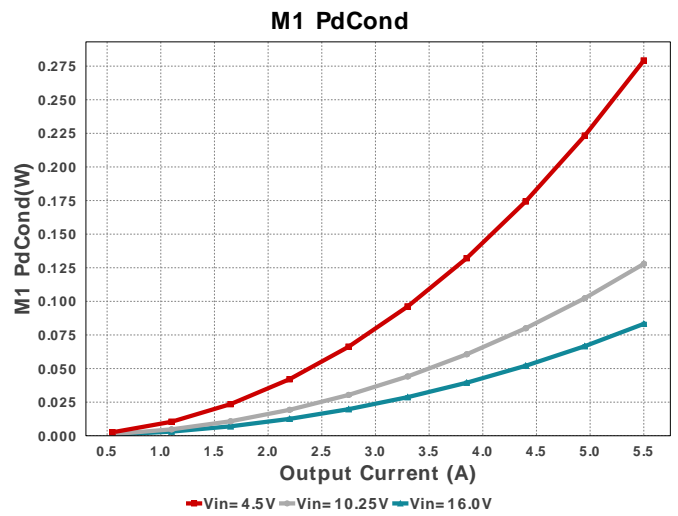
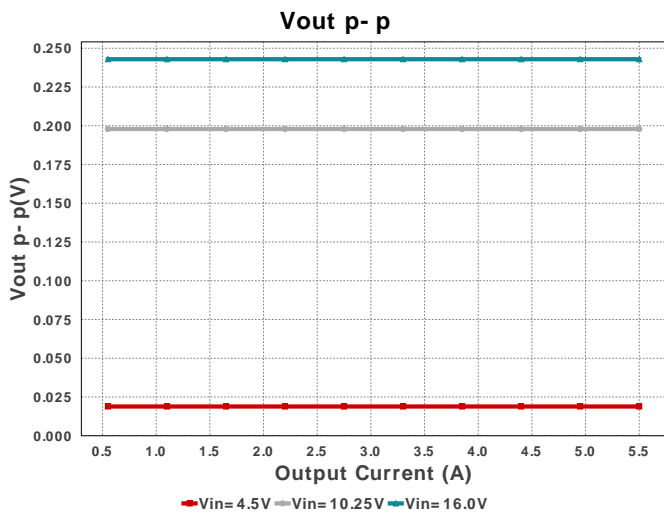
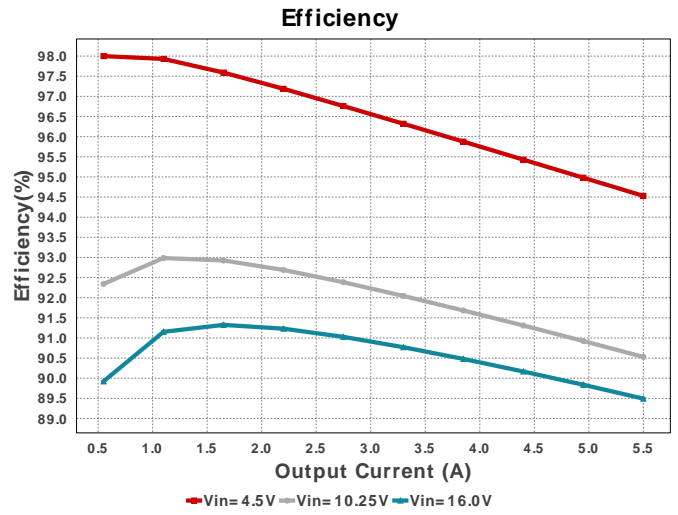
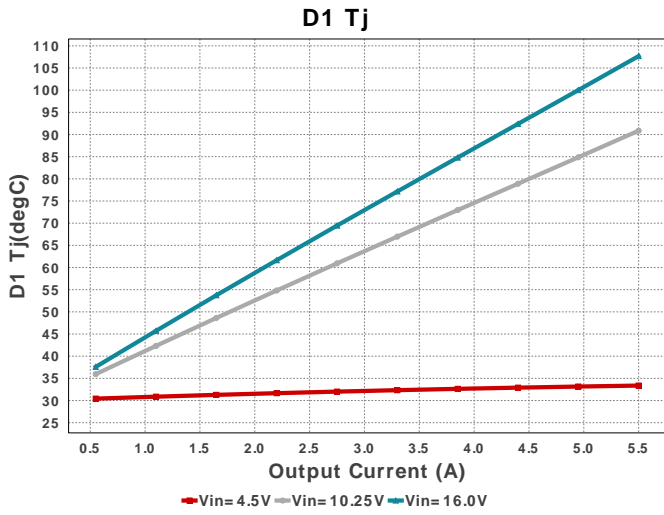
WEBENCH® Design Report

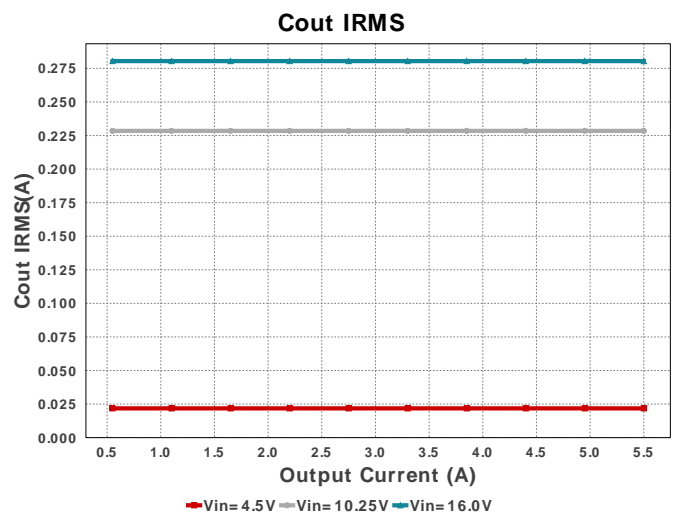
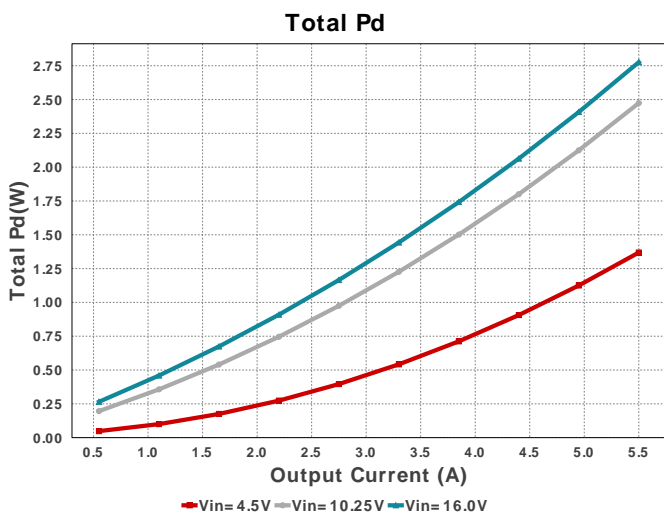
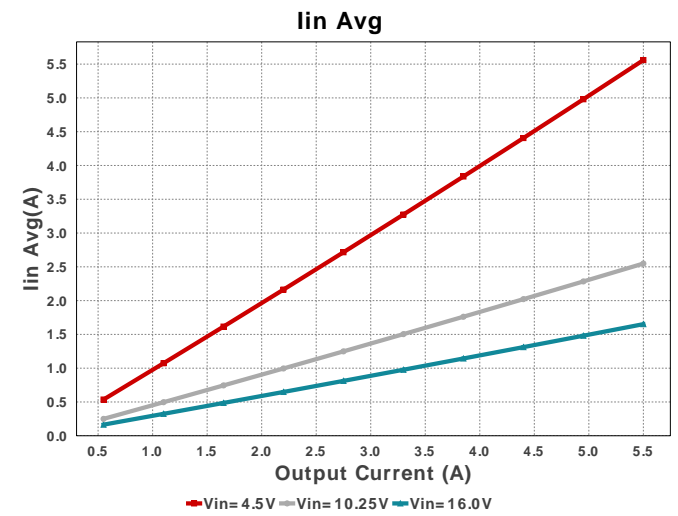
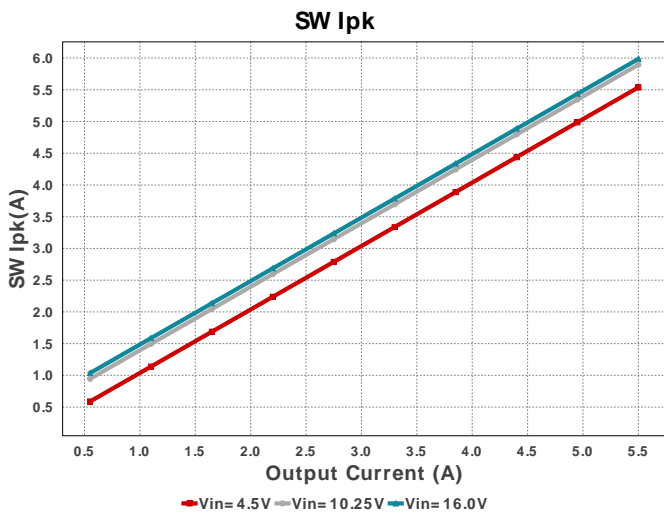
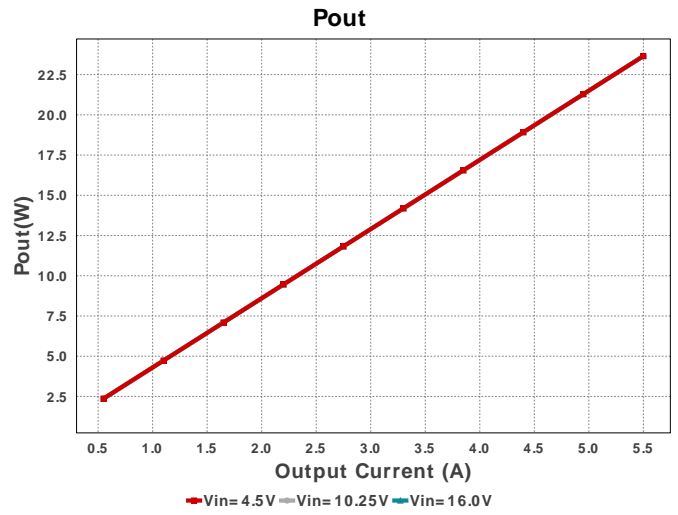
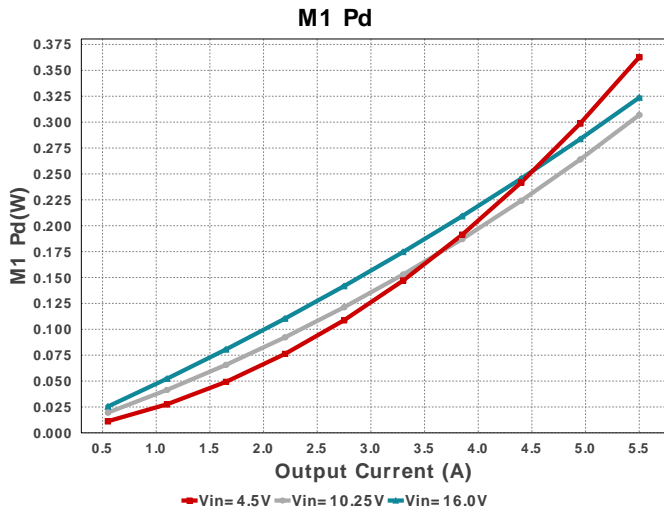
 Design : 22 LM25085MY/NOPB
 LM25085MY/NOPB Regulator 5.5A

Electrical BOM

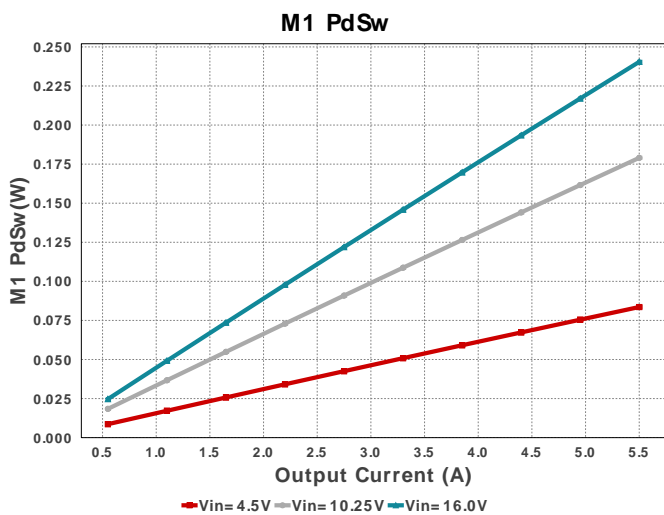
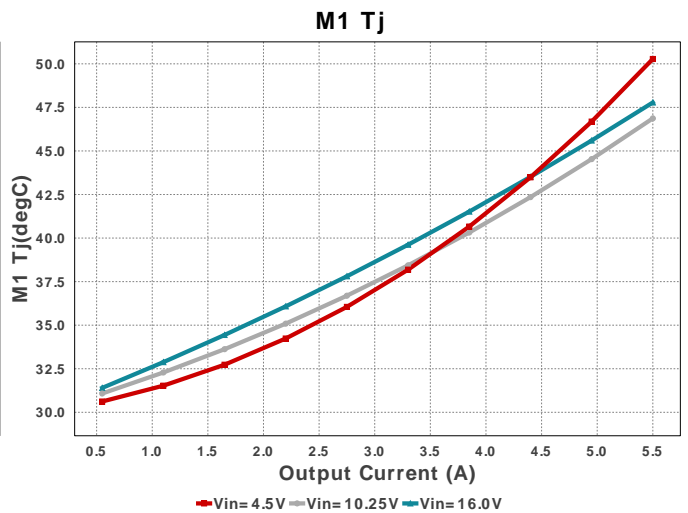
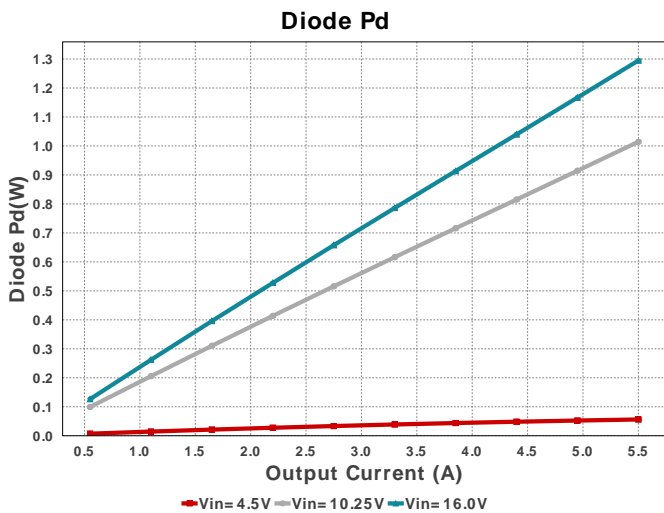
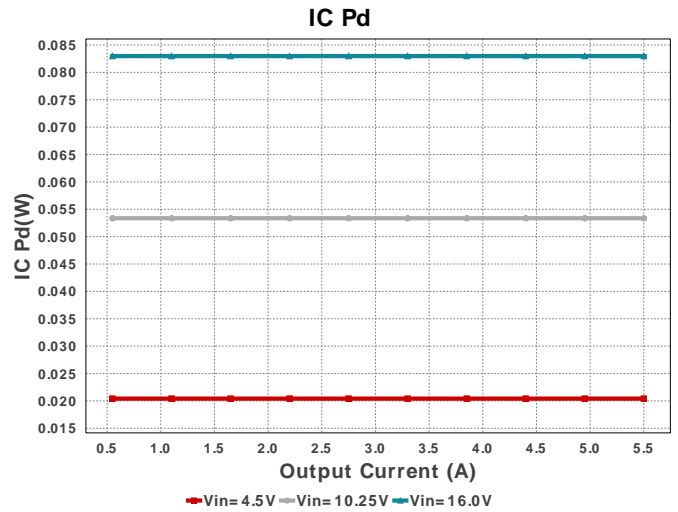
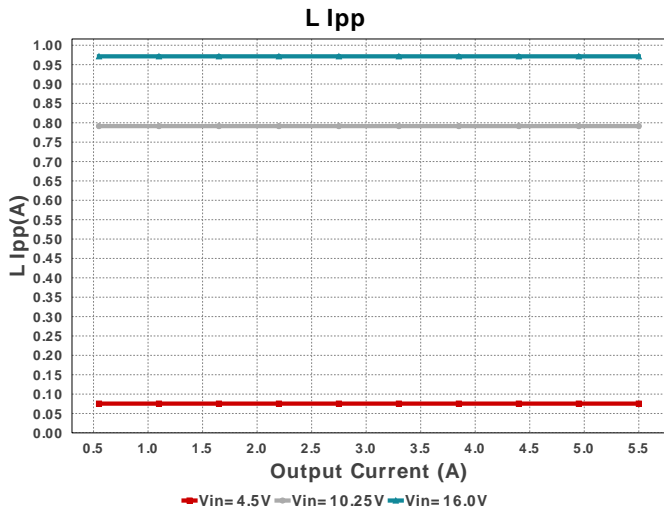
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Cadj	MuRata	GRM1555C1H102JA01J Series= C0G/NP0	Cap= 1.0 nF VDC= 50.0 V IRMS= 0.0 A	1	\$0.01	0402 3 mm ²
Cff	MuRata	GRM1885C1H242JA01D Series= C0G/NP0	Cap= 2.4 nF ESR= 1.0 mOhm VDC= 50.0 V IRMS= 0.0 A	1	\$0.02	0603 5 mm ²
Cin	Panasonic	25SVPF100M Series= SVPF	Cap= 100.0 uF ESR= 24.0 mOhm VDC= 25.0 V IRMS= 3.2 A	1	\$0.47	 CAPSMT_62_E7 106 mm ²
Cout	AVX	TPSB476K010R0250 Series= TPS	Cap= 47.0 uF ESR= 250.0 mOhm VDC= 10.0 V IRMS= 525.0 mA	1	\$0.18	 3528-21 17 mm ²
Cvcc	Taiyo Yuden	TMK212BJ474KD-T Series= X5R	Cap= 470.0 nF ESR= 1.0 mOhm VDC= 20.0 V IRMS= 0.0 A	1	\$0.02	0805 7 mm ²
D1	Diodes Inc.	STPS30M60	VF@Io= 600.0 mV VRRM= 60.0 V	1	\$0.79	 TO-220AB 79 mm ²
L1	Bourns	SRP1250-6R8M	L= 6.8 uH 16.0 mOhm	1	\$0.77	 SRP1250 253 mm ²
M1	Texas Instruments	CSD25402Q3A	VdsMax= -20.0 V IdsMax= -75.0 Amps	1	\$0.26	 DNH0008A 18 mm ²

Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Radj	Vishay-Dale	CRCW04022K37FKED Series= CRCW..e3	Res= 2.37 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm ²
Rfb1	Vishay-Dale	CRCW04024K12FKED Series= CRCW..e3	Res= 4.12 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm ²
Rfb2	Yageo	RC0201FR-0710KL Series= ?	Res= 10.0 kOhm Power= 50.0 mW Tolerance= 1.0%	1	\$0.01	 0201 2 mm ²
Rsns	Stackpole Electronics Inc	CSR1206FK10L0 Series= ?	Res= 10.0 mOhm Power= 500.0 mW Tolerance= 1.0%	1	\$0.12	 1206 11 mm ²
Rt	Vishay-Dale	CRCW040249K9FKED Series= CRCW..e3	Res= 49.9 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm ²
U1	Texas Instruments	LM25085MY/NOPB	Switcher	1	\$0.62	 MUY08A 24 mm ²









Operating Values

#	Name	Value	Category	Description
1.	BOM Count	14		Total Design BOM count
2.	Total BOM	\$3.3		Total BOM Cost
3.	Cin IRMS	2.48 A	Capacitor	Input capacitor RMS ripple current
4.	Cin Pd	147.62 mW	Capacitor	Input capacitor power dissipation
5.	Cout IRMS	280.417 mA	Capacitor	Output capacitor RMS ripple current
6.	Cout Pd	19.658 mW	Capacitor	Output capacitor power dissipation
7.	D1 Tj	107.687 degC	Diode	D1 junction temperature
8.	Diode Pd	1.295 W	Diode	Diode power dissipation
9.	IC Pd	82.971 mW	IC	IC power dissipation
10.	IC Tj	33.817 degC	IC	IC junction temperature
11.	IC Tolerance	25.0 mV	IC	IC Feedback Tolerance

#	Name	Value	Category	Description
12.	ICThetaJA	46.0 degC/W	IC	IC junction-to-ambient thermal resistance
13.	Iin Avg	1.652 A	IC	Average input current
14.	L Ipp	971.393 mA	Inductor	Peak-to-peak inductor ripple current
15.	L Pd	605.0 mW	Inductor	Inductor power dissipation
16.	M1 Pd	323.69 mW	Mosfet	M1 MOSFET total power dissipation
17.	M1 PdCond	83.253 mW	Mosfet	M1 MOSFET conduction losses
18.	M1 PdSw	240.44 mW	Mosfet	M1 MOSFET switching losses
19.	M1 Tj	47.782 degC	Mosfet	M1 MOSFET junction temperature
20.	Cin Pd	147.62 mW	Power	Input capacitor power dissipation
21.	Cout Pd	19.658 mW	Power	Output capacitor power dissipation
22.	Diode Pd	1.295 W	Power	Diode power dissipation
23.	IC Pd	82.971 mW	Power	IC power dissipation
24.	L Pd	605.0 mW	Power	Inductor power dissipation
25.	M1 Pd	323.69 mW	Power	M1 MOSFET total power dissipation
26.	M1 PdCond	83.253 mW	Power	M1 MOSFET conduction losses
27.	M1 PdSw	240.44 mW	Power	M1 MOSFET switching losses
28.	Total Pd	2.776 W	Power	Total Power Dissipation
29.	Duty Cycle	28.397 %	System	Duty cycle
30.	Efficiency	89.494 %	System	Steady state efficiency
31.	FootPrint	534.0 mm ²	System	Total Foot Print Area of BOM components
32.	Frequency	437.298 kHz	System	Switching frequency
33.	Iout	5.5 A	System	Iout operating point
34.	Mode	CCM	System	Conduction Mode
35.	Pout	23.65 W	System	Total output power
36.	SW Ipk	5.986 A	System	Peak switch current
37.	Vin	16.0 V	System	Vin operating point
38.	Vout	4.3 V	System	Operational Output Voltage
39.	Vout Actual	4.284 V	System	Vout Actual calculated based on selected voltage divider resistors
40.	Vout Tolerance	3.459 %	System	Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable
41.	Vout p-p	242.92 mV	System	Peak-to-peak output ripple voltage

Design Inputs

Name	Value	Description
Iout	5.5	Maximum Output Current
VinMax	16.0	Maximum input voltage
VinMin	4.5	Minimum input voltage
Vout	4.3	Output Voltage
base_pn	LM25085	Base Product Number
source	DC	Input Source Type
Ta	30.0	Ambient temperature
UserFsw	437.0 k	Customer Selected Frequency

WEBENCH® Assembly

Component Testing

Some published data on components in datasheets such as Capacitor ESR and Inductor DC resistance is based on conservative values that will guarantee that the components always exceed the specification. For design purposes it is usually better to work with typical values. Since this data is not always available it is a good practice to measure the Capacitance and ESR values of C_{in} and C_{out} , and the inductance and DC resistance of L1 before assembly of the board. Any large discrepancies in values should be electrically simulated in WEBENCH to check for instabilities and thermally simulated in WebTHERM to make sure critical temperatures are not exceeded.

Soldering Component to Board

If board assembly is done in house it is best to tack down one terminal of a component on the board then solder the other terminal. For surface mount parts with large tabs, such as the DPAK, the tab on the back of the package should be pre-tinned with solder, then tacked into place by one of the pins. To solder the tab down to the board place the iron down on the board while resting against the tab, heating both surfaces simultaneously. Apply light pressure to the top of the plastic case until the solder flows around the part and the part is flush with the PCB. If the solder is not flowing around the board you may need a higher wattage iron (generally 25W to 30W is enough).

Initial Startup of Circuit

It is best to initially power up the board by setting the input supply voltage to the lowest operating input voltage 4.5V and set the input supply's current limit to zero. With the input supply off connect up the input supply to V_{in} and GND. Connect a digital volt meter and a load if needed to set the minimum load of the design from V_{out} and GND. Turn on the input supply and slowly turn up the current limit on the input supply. If the voltage starts to rise on the input supply continue increasing the input supply current limit while watching the output voltage. If the current increases on the input supply, but the voltage remains near zero, then there may be a short or a component misplaced on the board. Power down the board and visually inspect for solder bridges and recheck the diode and capacitor polarities. Once the power supply circuit is operational then more extensive testing may include full load testing, transient load and line tests to compare with simulation results.

Load Testing

The setup is the same as the initial startup, except that an additional digital voltmeter is connected between V_{in} and GND, a load is connected between V_{out} and GND and a current meter is connected in series between V_{out} and the load. The load must be able to handle at least rated output power + 50% (7.5 watts for this design). Ideally the load is supplied in the form of a variable load test unit. It can also be done in the form of suitably large power resistors. When using an oscilloscope to measure waveforms on the prototype board, the ground leads of the oscilloscope probes should be as short as possible and the area of the loop formed by the ground lead should be kept to a minimum. This will help reduce ground lead inductance and eliminate EMI noise that is not actually present in the circuit.



Design Assistance

1. For a Constant On Time device to be stable, we need to provide a ripple at the feedback comparator. There are various methods to implement the ripple. Depending on the circuit complexity vs. the allowable ripple, we have three options to choose from. The simplest option, 'Low Complexity', would require only a high ESR cap at the output. This means that the BOM count will be small, but the output voltage ripple will be quite large. The 'optimal solution' would require a feed-forward cap in parallel with the upper feedback resistor to AC couple the ripple to the feedback node. This increases the BOM count slightly, but now we have more control over the output voltage ripple. If the output voltage requirement is very tight, then the best option is to go for the 'Low Output Ripple' solution. In this option we can go with very low ESR output caps and have very good control over the output voltage ripple

2. Master key : E7CA1A52D00A5E86[v1]

3. **LM25085** Product Folder : <http://www.ti.com/product/LM25085> : contains the data sheet and other resources.

Important Notice and Disclaimer

TI provides technical and reliability data (including datasheets), design resources (including reference designs), application or other design advice, web tools, safety information, and other resources AS IS and with all faults, and disclaims all warranties. These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, or other requirements.

These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

Providing these resources does not expand or otherwise alter TI's applicable Terms of Sale or other applicable terms available either on ti.com or provided in conjunction with TI products.