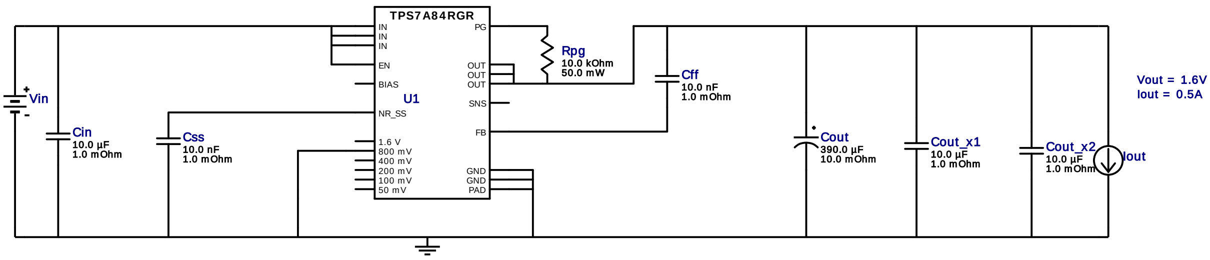
VinMin = 3.3V
VinMax = 3.3V
Vout = 1.6V
Iout = 0.5A

Device = TPS7A8400RGRR
Topology = LDO
Created = 2023-01-18 20:39:11.742
BOM Cost = $2.31
BOM Count = 8
Total Pd = 0.86W

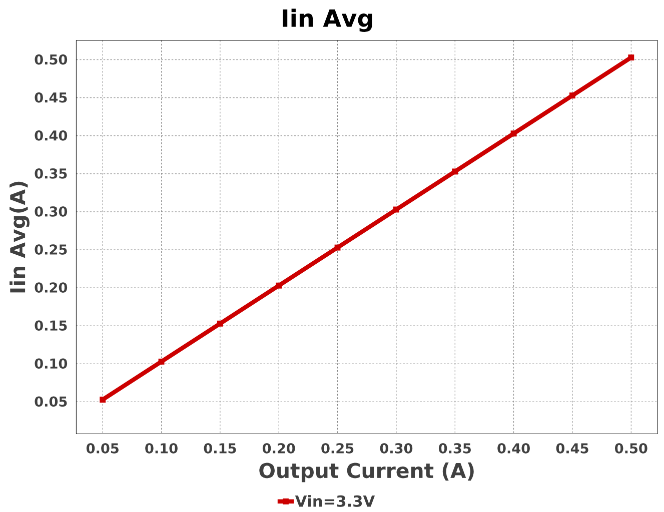
# WEBENCH ® Design Report

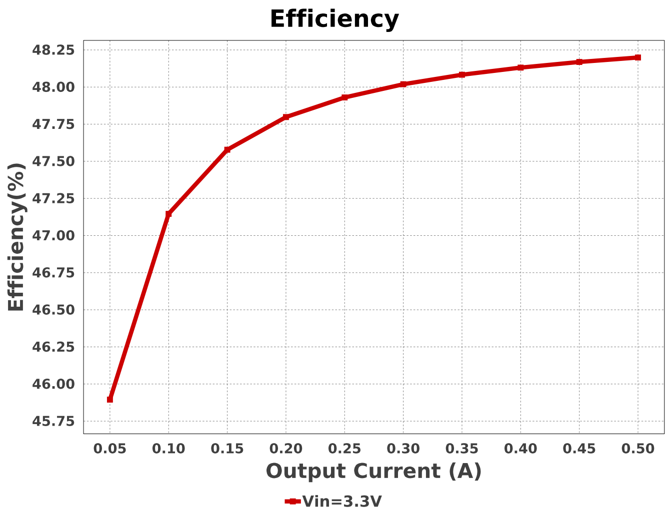
Design : 222 TPS7A8400RGRR
TPS7A8400RGRR 3.3V-3.3V to 1.60V @ 0.5A

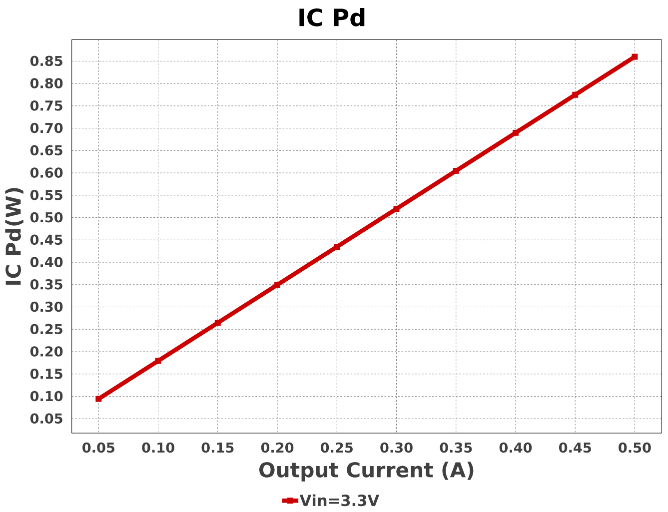


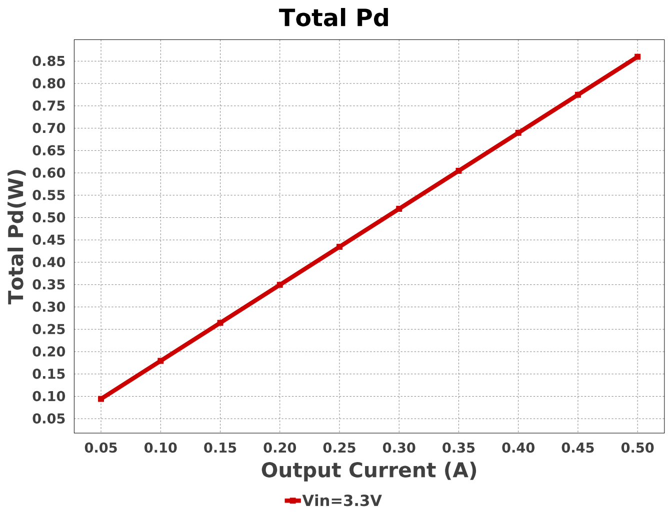

## Electrical BOM

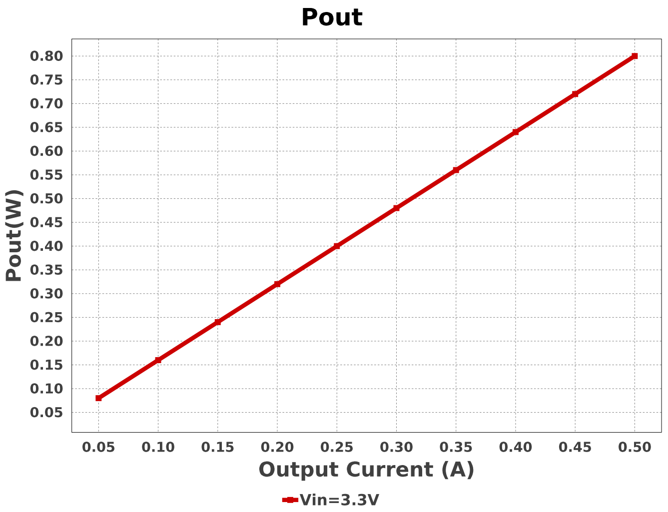
| Name | Manufacturer | Part Number | Properties | Qty | Price | Footprint |
|---|---|---|---|---|---|---|
| Cff | MuRata | GRM033R70J103KA01D<br>Series= X7R | Cap= 10.0 nF<br>ESR= 1.0 mOhm<br>VDC= 6.3 V<br>IRMS= 0.0 A | 1 | $0.01 | 0201 2 mm$^2$ |
| Cin | MuRata | GRJ155R60J106ME11D<br>Series= X5R | Cap= 10.0 uF<br>ESR= 1.0 mOhm<br>VDC= 6.3 V<br>IRMS= 0.0 A | 1 | $0.02 | 0402_070 3 mm$^2$ |
| Cout | Chemi-Con | APXF2R5ARA391MF61G<br>Series= PXF | Cap= 390.0 uF<br>ESR= 10.0 mOhm<br>VDC= 2.5 V<br>IRMS= 3.9 A | 1 | $0.25 | CAPSMT_62_F61 74 mm$^2$ |
| Cout_x1 | Taiyo Yuden | LMK212BJ106KG-T<br>Series= X5R | Cap= 10.0 uF<br>ESR= 1.0 mOhm<br>VDC= 10.0 V<br>IRMS= 0.0 A | 1 | $0.02 | 0805 7 mm$^2$ |
| Cout_x2 | Taiyo Yuden | LMK212BJ106KG-T<br>Series= X5R | Cap= 10.0 uF<br>ESR= 1.0 mOhm<br>VDC= 10.0 V<br>IRMS= 0.0 A | 1 | $0.02 | 0805 7 mm$^2$ |
| Css | MuRata | GRM033R70J103KA01D<br>Series= X7R | Cap= 10.0 nF<br>ESR= 1.0 mOhm<br>VDC= 6.3 V<br>IRMS= 0.0 A | 1 | $0.01 | 0201 2 mm$^2$ |
| Rpg | Yageo | RC0201FR-0710KL<br>Series= ? | Res= 10.0 kOhm<br>Power= 50.0 mW<br>Tolerance= 1.0% | 1 | $0.01 | 0201 2 mm$^2$ |
| U1 | Texas Instruments | TPS7A8400RGRR | Switcher | 1 | $1.97 | RGR0020A 20 mm$^2$ |

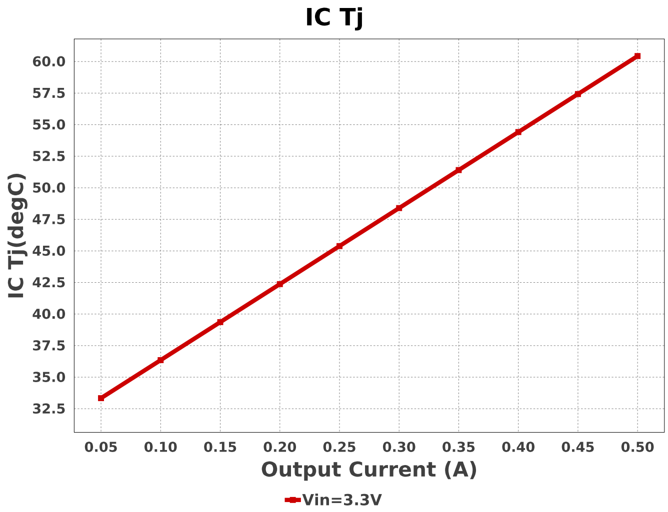

Iin Avg
Iin Avg(A)
0.05 0.10 0.15 0.20 0.25 0.30 0.35 0.40 0.45 0.50
0.05 0.10 0.15 0.20 0.25 0.30 0.35 0.40 0.45 0.50
Output Current (A)
Vin=3.3V

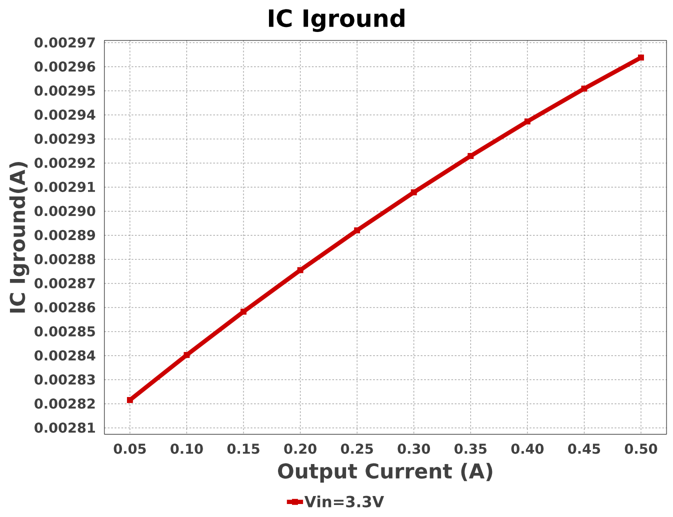

Efficiency
Efficiency(%)
45.75 46.00 46.25 46.50 46.75 47.00 47.25 47.50 47.75 48.00 48.25
0.05 0.10 0.15 0.20 0.25 0.30 0.35 0.40 0.45 0.50
Output Current (A)
Vin=3.3V

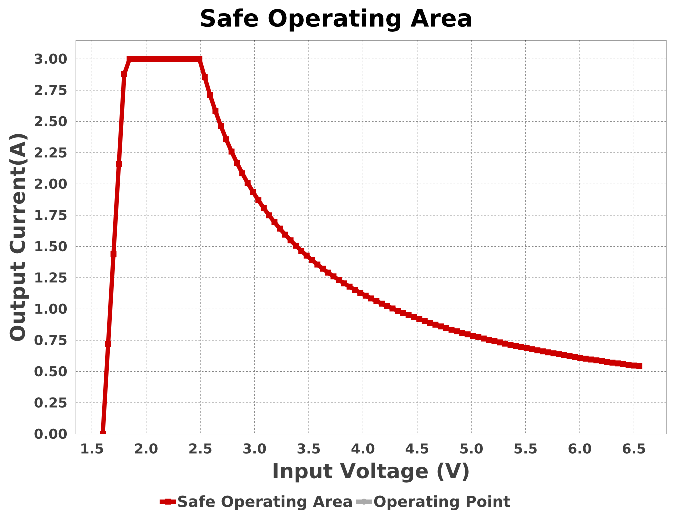

IC Pd
IC Pd(W)
0.05 0.10 0.15 0.20 0.25 0.30 0.35 0.40 0.45 0.50 0.55 0.60 0.65 0.70 0.75 0.80 0.85
0.05 0.10 0.15 0.20 0.25 0.30 0.35 0.40 0.45 0.50
Output Current (A)
Vin=3.3V

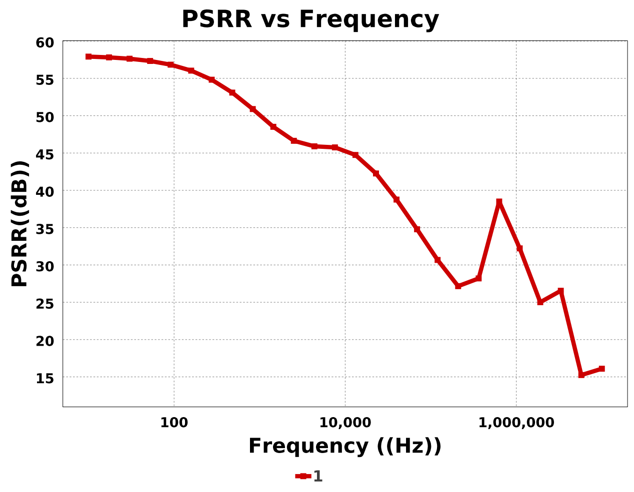

Total Pd
Total Pd(W)
0.05 0.10 0.15 0.20 0.25 0.30 0.35 0.40 0.45 0.50 0.55 0.60 0.65 0.70 0.75 0.80 0.85
0.05 0.10 0.15 0.20 0.25 0.30 0.35 0.40 0.45 0.50
Output Current (A)
Vin=3.3V


Pout
Pout(W)
0.05 0.10 0.15 0.20 0.25 0.30 0.35 0.40 0.45 0.50 0.55 0.60 0.65 0.70 0.75 0.80
0.05 0.10 0.15 0.20 0.25 0.30 0.35 0.40 0.45 0.50
Output Current (A)
Vin=3.3V


IC Tj
IC Tj(degC)
32.5 35.0 37.5 40.0 42.5 45.0 47.5 50.0 52.5 55.0 57.5 60.0
0.05 0.10 0.15 0.20 0.25 0.30 0.35 0.40 0.45 0.50
Output Current (A)
Vin=3.3V




## Operating Values

| # | Name | Value | Category | Description |
|---|---|---|---|---|
| 1. | IC Iground | 2.964 mA | IC | IC ground current |
| 2. | IC Pd | 859.781 mW | IC | IC power dissipation |
| 3. | IC Tj | 60.436 degC | IC | IC junction temperature |
| 4. | IC Tolerance | 8.0 mV | IC | IC Feedback Tolerance |
| 5. | ICThetaJA | 35.4 degC/W | IC | IC junction-to-ambient thermal resistance |
| 6. | Iin Avg | 502.96 mA | IC | Average input current |
| 7. | IOUT_OP | 500.0 mA | Op Point | Iout operating point |
| 8. | Input Ripple Frequency | 100.0 kHz | Op Point | Input Source Ripple Frequency for PSRR Calculation |
| 9. | PSRR est. | -32.033 dB | Op Point | Power Supply Rejection Ratio estimated |
| 10. | VIN_OP | 3.3 V | Op Point | Vin operating point |
| 11. | Total Pd | 859.781 mW | Power | Total Power Dissipation |
| 12. | BOM Count | 8 | System Information | Total Design BOM count |
| 13. | Efficiency | 48.199 % | System Information | Steady state efficiency |
| 14. | FootPrint | 117.0 $mm^2$ | System Information | Total Foot Print Area of BOM components |
| 15. | Pout | 800.0 mW | System Information | Total output power |
| 16. | Total BOM | $2.31 | System Information | Total BOM Cost |
| 17. | Vin p-p | 33.0 mV | System Information | Input Source ripple voltage |
| 18. | Vout | 1.6 V | System Information | Operational Output Voltage |
| 19. | Vout Tolerance | 500.0 m% | System Information | Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable |
| 20. | Vout p-p | 825.791 µV | System Information | Peak-to-peak output ripple voltage |

## Design Inputs

| Name | Value | Description |
| --- | --- | --- |
| Iout | 500.0 m | Maximum Output Current |
| VinMax | 3.3 | Maximum input voltage |
| VinMin | 3.3 | Minimum input voltage |
| Vout | 1.6 | Output Voltage |
| base_pn | TPS7A84 | Base Product Number |
| source | DC | Input Source Type |
| Ta | 30.0 | Ambient temperature |

# WEBENCH® Assembly

## Component Testing

Some published data on components in datasheets such as Capacitor ESR and Inductor DC resistance is based on conservative values that will guarantee that the components always exceed the specification. For design purposes it is usually better to work with typical values. Since this data is not always available it is a good practice to measure the Capacitance and ESR values of Cin and Cout, and the inductance and DC resistance of L1 before assembly of the board. Any large discrepancies in values should be electrically simulated in WEBENCH to check for instabilities and thermally simulated in WebTHERM to make sure critical temperatures are not exceeded.

## Soldering Component to Board

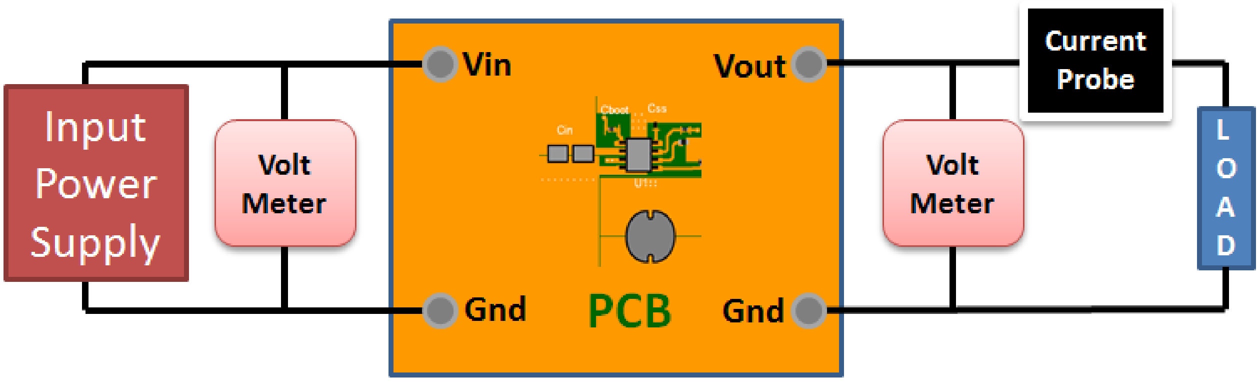
If board assembly is done in house it is best to tack down one terminal of a component on the board then solder the other terminal. For surface mount parts with large tabs, such as the DPAK, the tab on the back of the package should be pre-tinned with solder, then tacked into place by one of the pins. To solder the tab town to the board place the iron down on the board while resting against the tab, heating both surfaces simultaneously. Apply light pressure to the top of the plastic case until the solder flows around the part and the part is flush with the PCB. If the solder is not flowing around the board you may need a higher wattage iron (generally 25W to 30W is enough).

## Initial Startup of Circuit

It is best to initially power up the board by setting the input supply voltage to the lowest operating input voltage 3.3V and set the input supply's current limit to zero. With the input supply off connect up the input supply to Vin and GND. Connect a digital volt meter and a load if needed to set the minimum Iout of the design from Vout and GND. Turn on the input supply and slowly turn up the current limit on the input supply. If the voltage starts to rise on the input supply continue increasing the input supply current limit while watching the output voltage. If the current increases on the input supply, but the voltage remains near zero, then there may be a short or a component misplaced on the board. Power down the board and visually inspect for solder bridges and recheck the diode and capacitor polarities. Once the power supply circuit is operational then more extensive testing may include full load testing, transient load and line tests to compare with simulation results.

## Load Testing

The setup is the same as the initial startup, except that an additional digital voltmeter is connected between Vin and GND, a load is connected between Vout and GND and a current meter is connected in series between Vout and the load. The load must be able to handle at least rated output power + 50% ( 7.5 watts for this design). Ideally the load is supplied in the form of a variable load test unit. It can also be done in the form of suitably large power resistors. When using an oscilloscope to measure waveforms on the prototype board, the ground leads of the oscilloscope probes should be as short as possible and the area of the loop formed by the ground lead should be kept to a minimum. This will help reduce ground lead inductance and eliminate EMI noise that is not actually present in the circuit.




## Design Assistance

1.

2. Master key : CE46597237B4319F[v1]

3. **TPS7A84** Product Folder : http://www.ti.com/product/TPS7A84 : contains the data sheet and other resources.

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