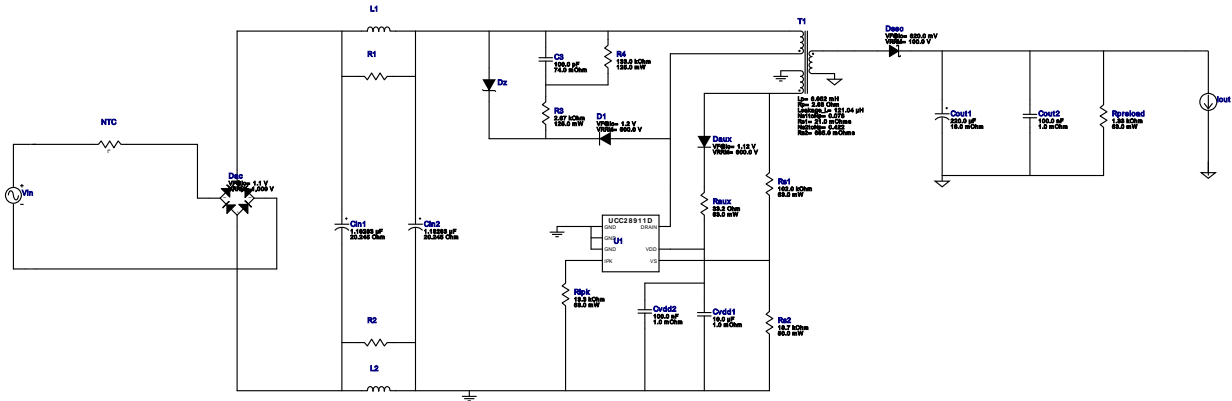


VinMin = 50.0V  
 VinMax = 300.0V  
 Vout = 3.3V  
 Iout = 0.1A

Device = UCC28911D  
 Topology = Flyback  
 Created = 2023-06-12 11:30:01.433  
 BOM Cost = NA  
 BOM Count = 22  
 Total Pd = 0.24W

# WEBENCH® Design Report

Design : 242 UCC28911D  
 UCC28911D 50V-300V to 3.30V @ 0.1A

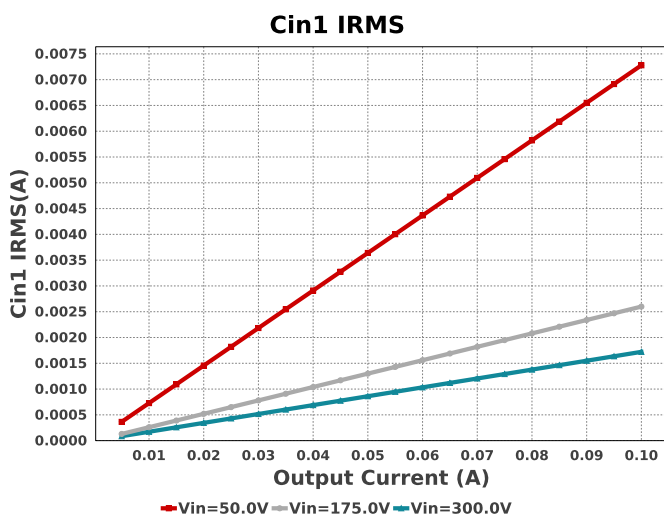
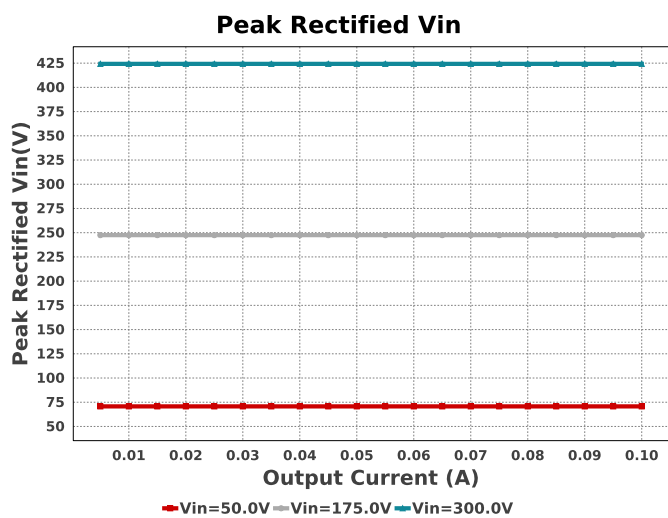
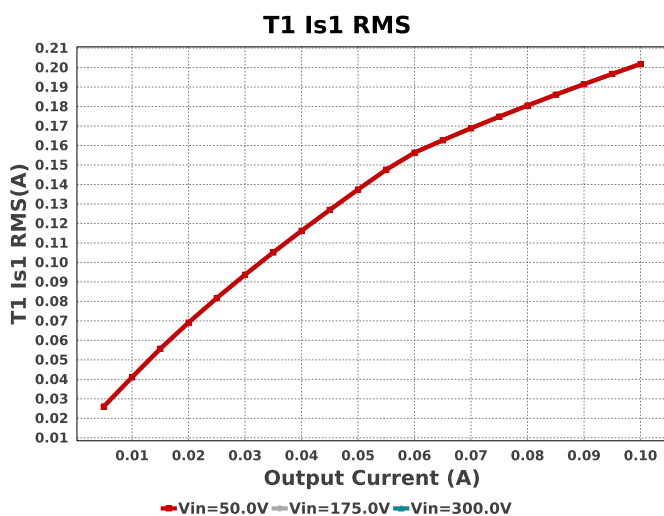
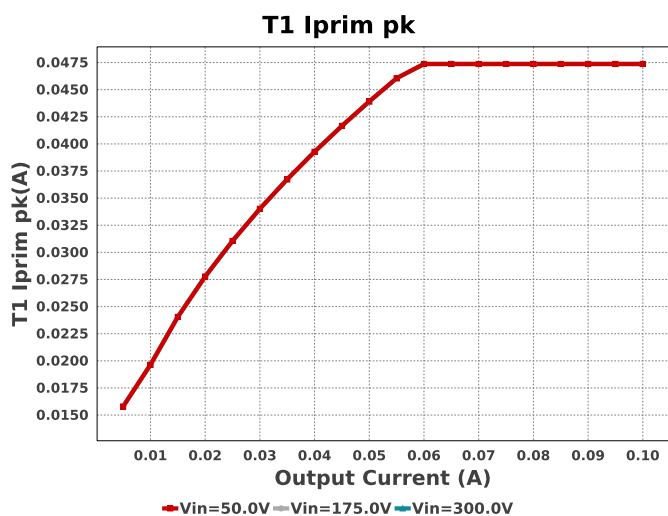
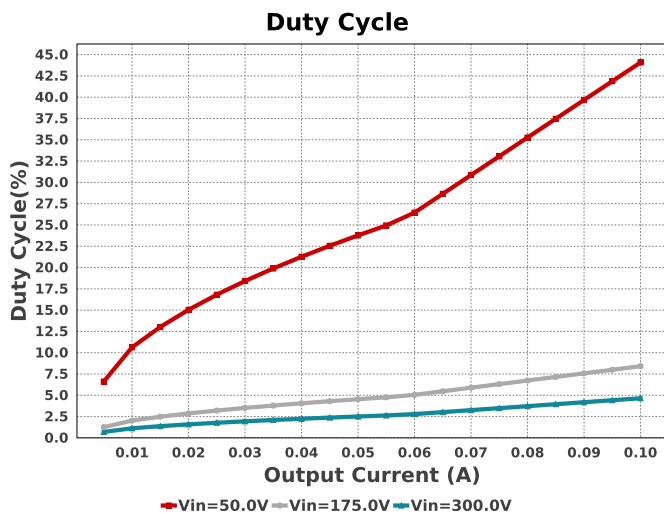
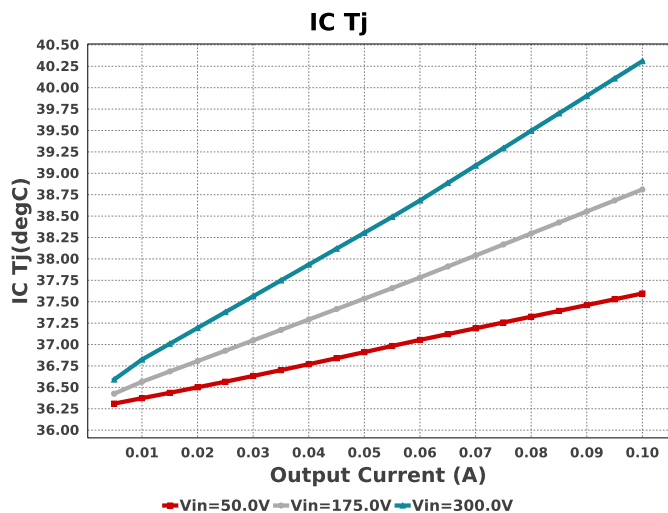


1. The EMI filter shown in the schematic is a placeholder. It has not yet been designed for the application.

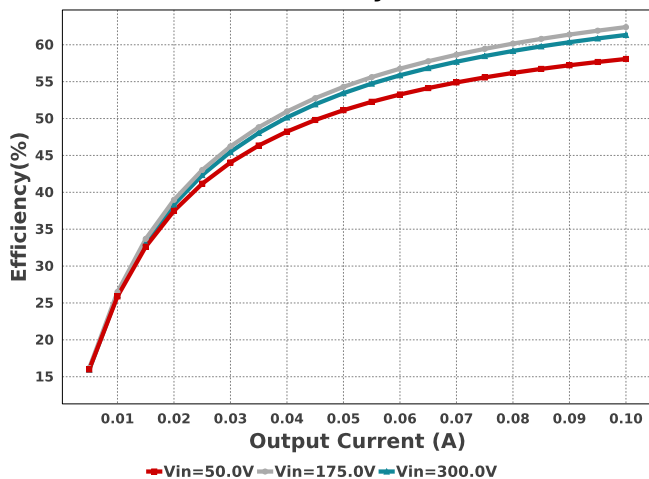
## Electrical BOM

Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
C3	Kemet	C0805C101J1GACTU Series= C0G/NP0	Cap= 100.0 pF ESR= 74.0 mOhm VDC= 100.0 V IRMS= 524.0 mA	1	\$0.02	0805 7 mm <sup>2</sup>
Cin1	CUSTOM	CUSTOM Series= ?	Cap= 1.18283 uF ESR= 20.2455 Ohm VDC= 445.47 V IRMS= 8.7343 mA	1	NA	CUSTOM 0 mm <sup>2</sup>
Cin2	CUSTOM	CUSTOM Series= ?	Cap= 1.18283 uF ESR= 20.2455 Ohm VDC= 445.47 V IRMS= 8.7343 mA	1	NA	CUSTOM 0 mm <sup>2</sup>
Cout1	Panasonic	10TPE220MIL Series= TPE	Cap= 220.0 uF ESR= 18.0 mOhm VDC= 10.0 V IRMS= 2.8 A	1	\$0.76	7343-31 59 mm <sup>2</sup>
Cout2	MuRata	GRM155R71C104KA88D Series= X7R	Cap= 100.0 nF ESR= 1.0 mOhm VDC= 16.0 V IRMS= 0.0 A	1	\$0.01	0402 3 mm <sup>2</sup>
Cvdd1	MuRata	GRT31CR61H106KE01L Series= X5R	Cap= 10.0 uF ESR= 1.0 mOhm VDC= 50.0 V IRMS= 0.0 A	1	\$0.18	1206_180 11 mm <sup>2</sup>
Cvdd2	Yageo	CC0805KRX7R9BB104 Series= X7R	Cap= 100.0 nF ESR= 1.0 mOhm VDC= 50.0 V IRMS= 0.0 A	1	\$0.02	0805 7 mm <sup>2</sup>
D1	ON Semiconductor	1N4937G	VF@Io= 1.2 V VRRM= 600.0 V	1	\$0.04	DO-41 43 mm <sup>2</sup>

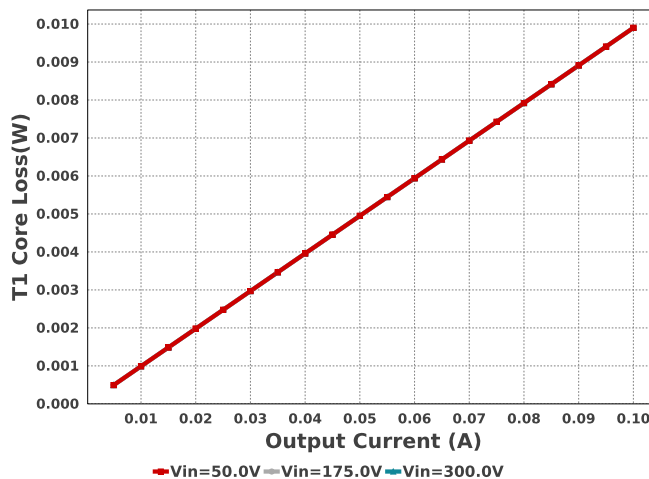
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Dac	Vishay-Semiconductor	DF10SA	VF@Io= 1.1 V VRRM= 1,000.0 V	1	\$0.28	 DF-S 99 mm <sup>2</sup>
Daux	Bourns	CD214C-F3600	VF@Io= 1.12 V VRRM= 600.0 V	1	\$0.23	 SMC 83 mm <sup>2</sup>
Dsec	Vishay-Semiconductor	SS2PH10-M3	VF@Io= 620.0 mV VRRM= 100.0 V	1	\$0.15	 DO-220AA 14 mm <sup>2</sup>
Dz	Diodes Inc.	SMBJ78A-13-F	Zener	1	\$0.10	 SMB 44 mm <sup>2</sup>
NTC	Ametherm	SL0310001 Series= miniAMP	Thermistor	1	\$0.26	 SL03 16 mm <sup>2</sup>
R3	Panasonic	ERJ-6ENF2671V Series= ERJ-6E	Res= 2.67 kOhm Power= 125.0 mW Tolerance= 1.0%	1	\$0.01	 0805 7 mm <sup>2</sup>
R4	Vishay-Dale	CRCW0805133KFKEA Series= CRCW..e3	Res= 133.0 kOhm Power= 125.0 mW Tolerance= 1.0%	1	\$0.01	 0805 7 mm <sup>2</sup>
Raux	Vishay-Dale	CRCW040233R2FKED Series= CRCW..e3	Res= 33.2 Ohm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm <sup>2</sup>
Ripk	Vishay-Dale	CRCW040213K3FKED Series= CRCW..e3	Res= 13.3 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm <sup>2</sup>
Rpreload	Vishay-Dale	CRCW04021K33FKED Series= CRCW..e3	Res= 1.33 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm <sup>2</sup>
Rs1	Vishay-Dale	CRCW0402102KFKED Series= CRCW..e3	Res= 102.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm <sup>2</sup>
Rs2	Yageo	RC0201FR-0718K7L Series= ?	Res= 18.7 kOhm Power= 50.0 mW Tolerance= 1.0%	1	\$0.01	 0201 2 mm <sup>2</sup>
T1	CUSTOM	CUSTOM	Lp= 6.052 mH Rp= 2.68 Ohm Leakage_L= 121.04 µH Ns1toNp= 77.518 m Rs1= 21.0 mOhms Ns2toNp= 0.422 Rs2= 685.0 mOhms	1	NA	CUSTOM 0 mm <sup>2</sup>
U1	Texas Instruments	UCC28911D	Switcher	1	\$0.58	 D0007A 55 mm <sup>2</sup>



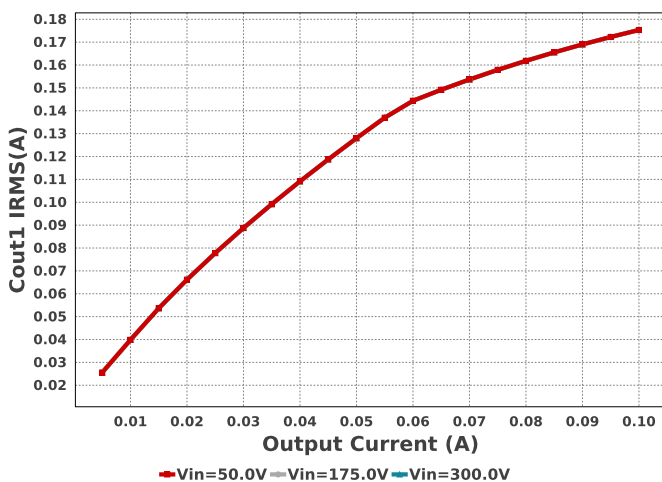
**Efficiency**



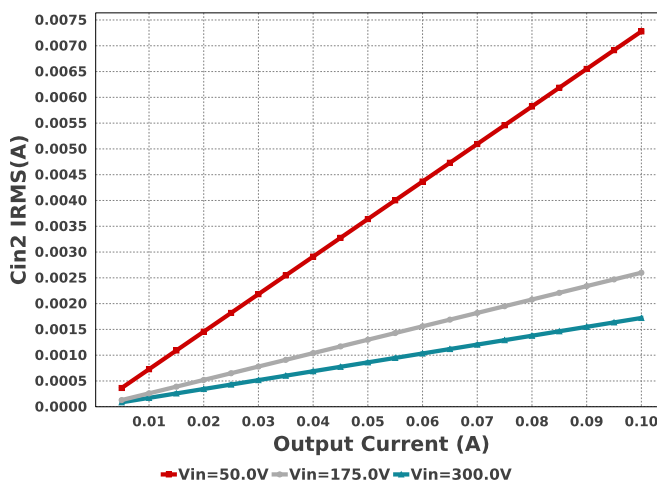
**T1 Core Loss**



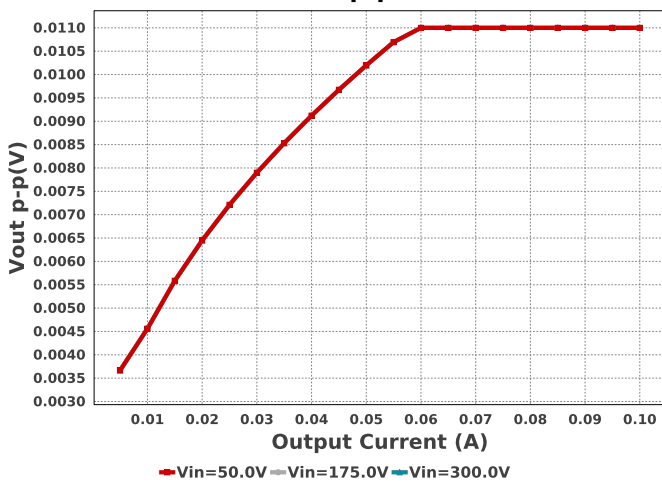
**Cout1 IRMS**



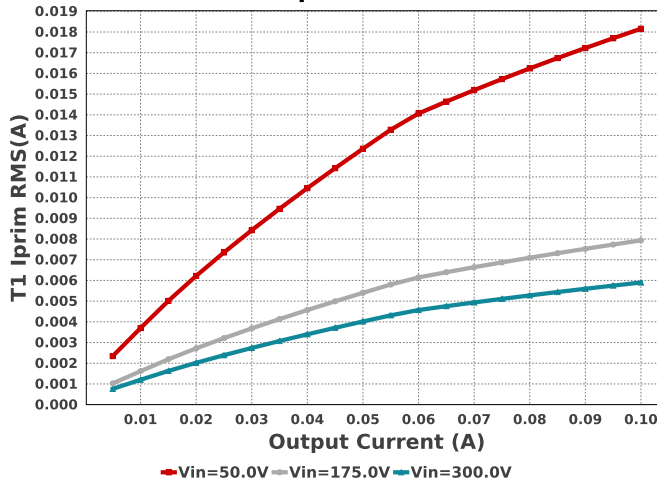
**Cin2 IRMS**

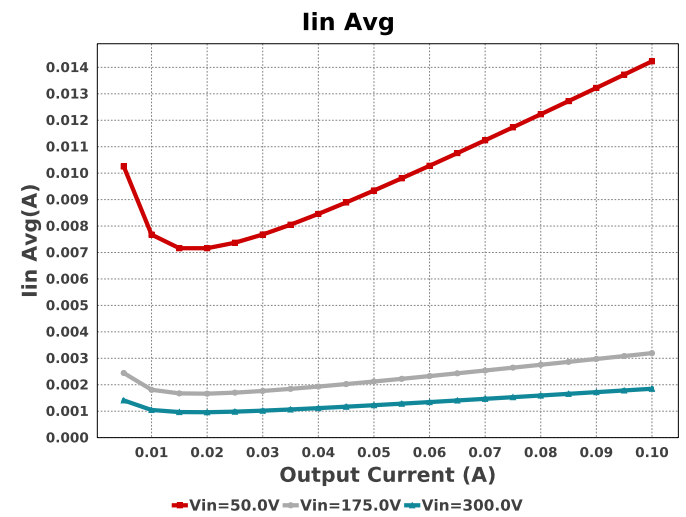
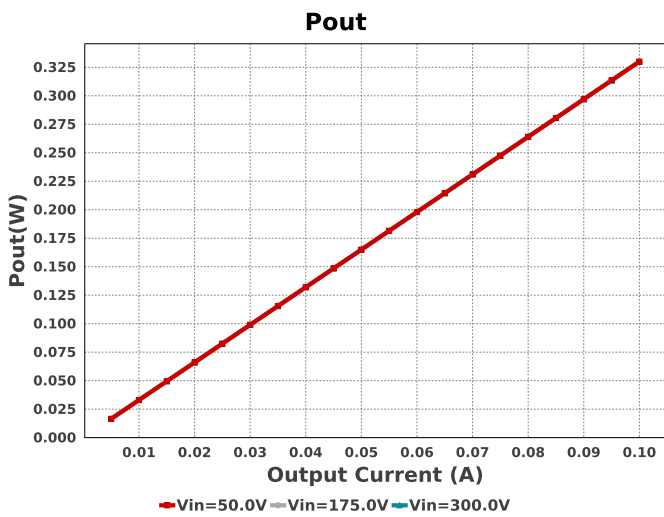
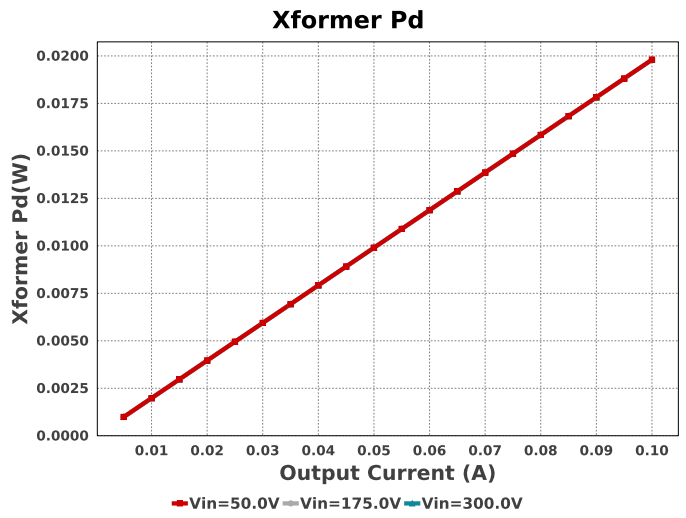
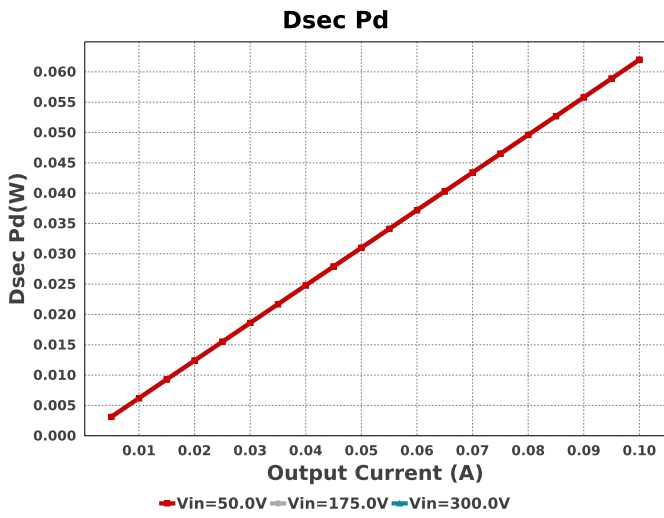
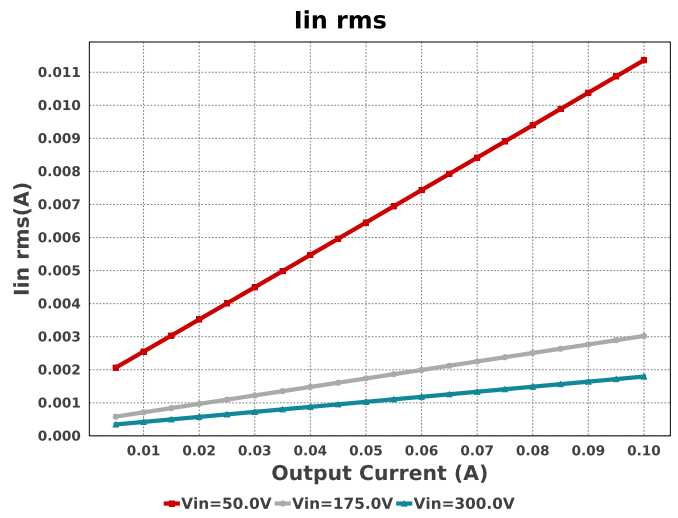
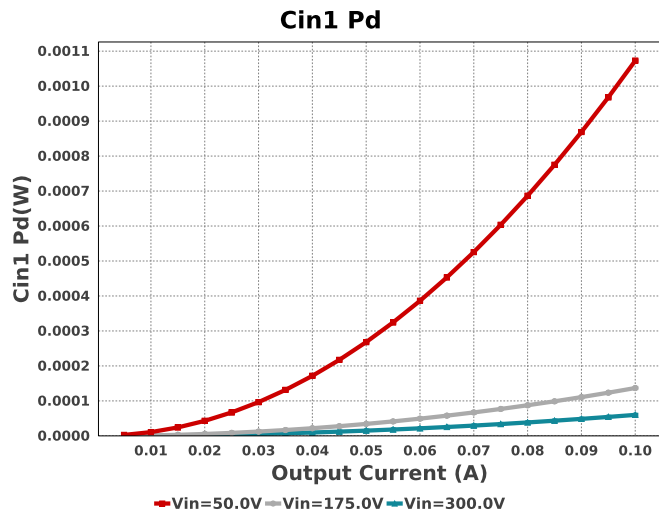


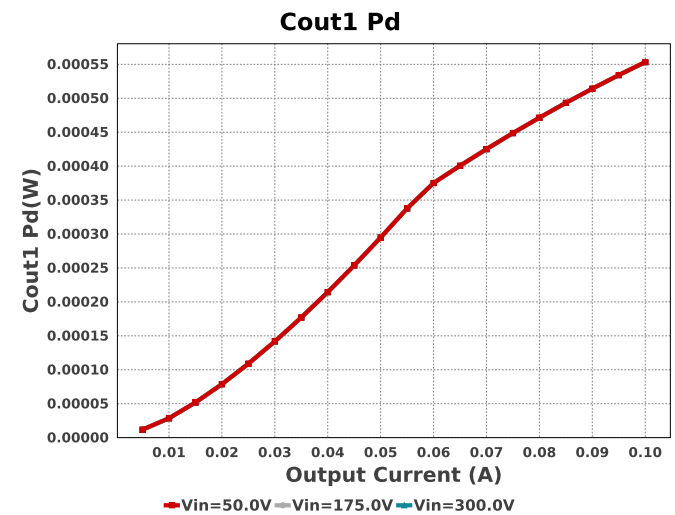
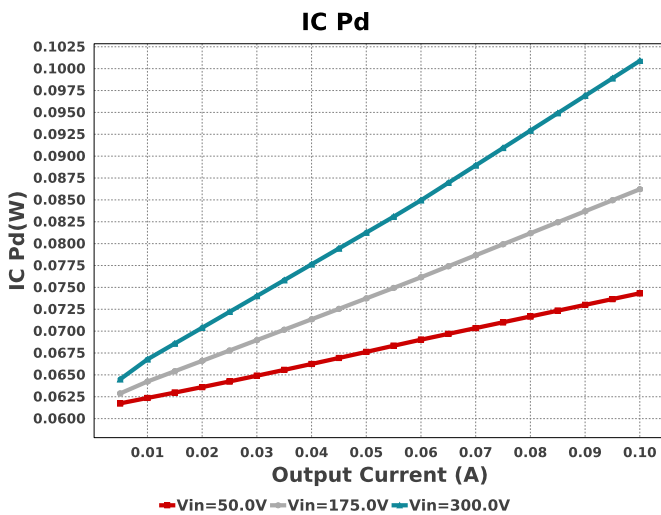
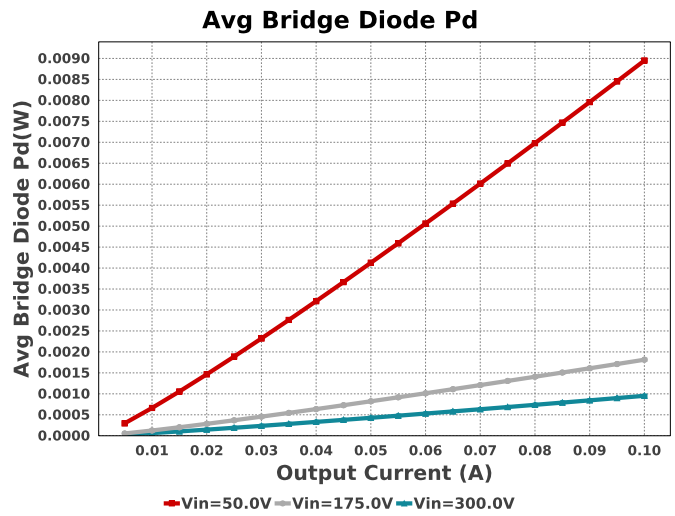
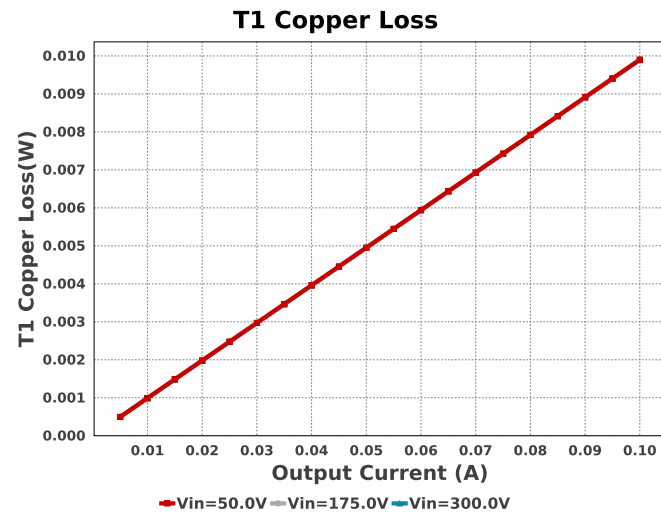
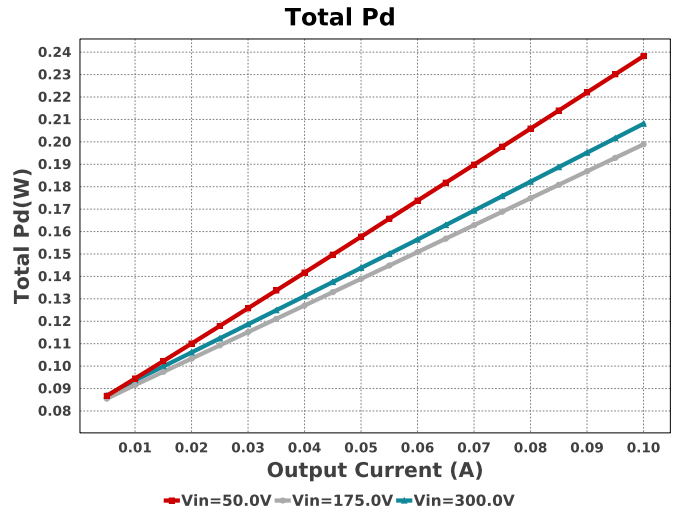
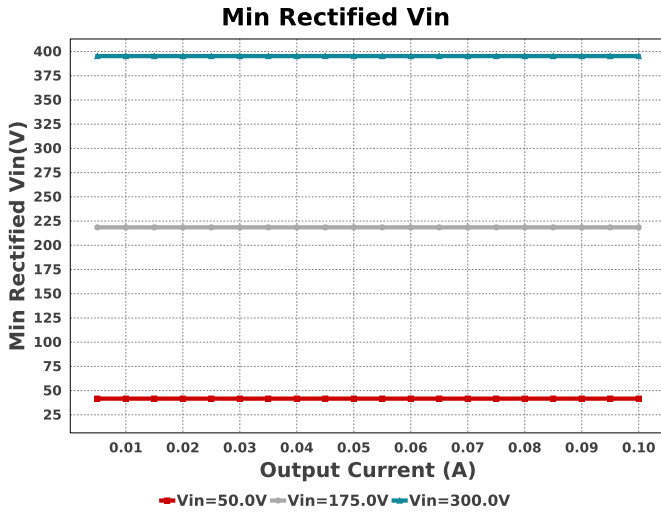
**Vout p-p**

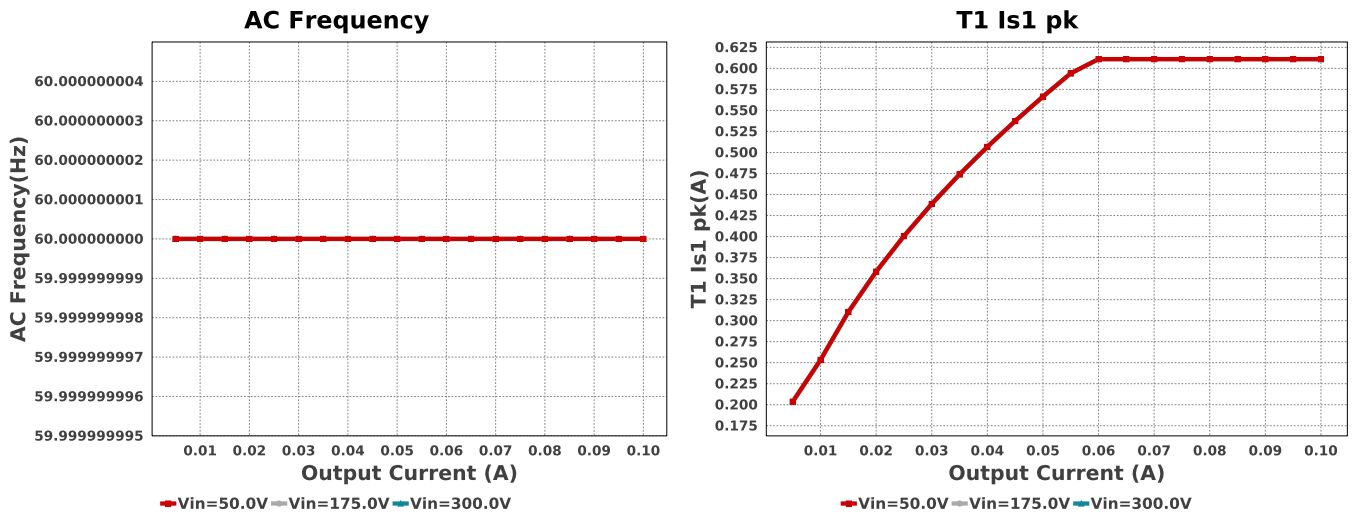


**T1 Iprim RMS**









## Operating Values

#	Name	Value	Category	Description
1.	Cin1 IRMS	7.279 mA	Capacitor	Input Capacitor Cin1 RMS Ripple Current
2.	Cin1 Pd	1.073 mW	Capacitor	Average Power Dissipation in the Input Capacitor Cin1
3.	Cin2 IRMS	7.279 mA	Capacitor	Input Capacitor Cin2 RMS Ripple Current
4.	Cout1 IRMS	175.321 mA	Capacitor	Output capacitor1 RMS ripple current
5.	Cout1 Pd	553.27 $\mu$ W	Capacitor	Output capacitor1 power dissipation
6.	Avg Bridge Diode Pd	8.949 mW	Diode	Average Power Dissipation in the Bridge Diode over the AC Line Period
7.	D1 trr	300.0 ns	Diode	D1 Reverse Recovery Time
8.	Daux trr	30.0 ns	Diode	Auxiliary Diode Reverse Recovery Time
9.	Dsec Pd	62.0 mW	Diode	Secondary Diode Power Dissipation
10.	Dsec trr	0.0 ns	Diode	Output Diode Reverse Recovery Time
11.	IC Pd	74.326 mW	IC	IC power dissipation
12.	IC Tj	37.596 degC	IC	IC junction temperature
13.	ICThetaJA	102.2 degC/W	IC	IC junction-to-ambient thermal resistance
14.	Iin Avg	14.23 mA	IC	Average input current
15.	Avg Bridge Diode Pd	8.949 mW	Power	Average Power Dissipation in the Bridge Diode over the AC Line Period
16.	Cin1 Pd	1.073 mW	Power	Average Power Dissipation in the Input Capacitor Cin1
17.	Cout1 Pd	553.27 $\mu$ W	Power	Output capacitor1 power dissipation
18.	Dsec Pd	62.0 mW	Power	Secondary Diode Power Dissipation
19.	IC Pd	74.326 mW	Power	IC power dissipation
20.	T1 Copper Loss	9.9 mW	Power	Transformer Copper Loss Power Dissipation
21.	T1 Core Loss	9.9 mW	Power	Transformer Core Loss Power Dissipation
22.	Total Pd	238.244 mW	Power	Total Power Dissipation
23.	Xformer Pd	19.8 mW	Power	Transformer power dissipation
24.	AC Frequency	60.0 Hz	System	Input AC frequency
25.	BOM Count	22	System	Total Design BOM count
26.	Duty Cycle	44.081 %	System	Duty cycle at the Minimum voltage seen at the rectified input
27.	Efficiency	58.074 %	System	Steady state efficiency
28.	FootPrint	542.0 mm <sup>2</sup>	System	Total Foot Print Area of BOM components
29.	Frequency	64.15 kHz	System	Approximate switching frequency
30.	Iin rms	11.365 mA	System	RMS Input Current
31.	Iout	100.0 mA	System	Iout operating point
32.	Min Rectified Vin	41.719 V	System	Minimum voltage seen at rectified input
33.	Mode	DCM	System	Conduction Mode
34.	Peak Rectified Vin	70.71 V	System	Peak voltage seen at rectified input
35.	Pout	330.0 mW	System	Total output power
36.	Total BOM	NA	System	Total BOM Cost
37.	Vin_RMS	50.0 V	System	Vin operating point

#	Name	Value	Category	Description
38.	Vout	3.3 V	System Information	Operational Output Voltage
39.	Vout Tolerance	242.42 m%	System Information	Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable
40.	Vout p-p	10.999 mV	System Information	Peak-to-peak output ripple voltage
41.	T1 Copper Loss	9.9 mW	Transformer	Transformer Copper Loss Power Dissipation
42.	T1 Core Loss	9.9 mW	Transformer	Transformer Core Loss Power Dissipation
43.	T1 Iprim RMS	18.157 mA	Transformer	Transformer Primary RMS Current
44.	T1 Iprim pk	47.368 mA	Transformer	Transformer Primary Peak Current
45.	T1 Is1 RMS	201.835 mA	Transformer	Transformer Secondary1 RMS Current
46.	T1 Is1 pk	611.061 mA	Transformer	Transformer Secondary1 Peak Current
47.	Xformer Pd	19.8 mW	Transformer	Transformer power dissipation

## Design Inputs

Name	Value	Description
Iout	100.0 m	Maximum Output Current
VinMax	300.0	Maximum input voltage
VinMin	50.0	Minimum input voltage
Vout	3.3	Output Voltage
acFrequency	60.0	AC Frequency
base_pn	UCC28911	Base Product Number
source	AC	Input Source Type
Ta	30.0	Ambient temperature



## WEBENCH® Assembly

### Component Testing

Some published data on components in datasheets such as Capacitor ESR and Inductor DC resistance is based on conservative values that will guarantee that the components always exceed the specification. For design purposes it is usually better to work with typical values. Since this data is not always available it is a good practice to measure the Capacitance and ESR values of  $C_{in}$  and  $C_{out}$ , and the inductance and DC resistance of L1 before assembly of the board. Any large discrepancies in values should be electrically simulated in WEBENCH to check for instabilities and thermally simulated in WebTHERM to make sure critical temperatures are not exceeded.

### Soldering Component to Board

If board assembly is done in house it is best to tack down one terminal of a component on the board then solder the other terminal. For surface mount parts with large tabs, such as the DPAK, the tab on the back of the package should be pre-tinned with solder, then tacked into place by one of the pins. To solder the tab down to the board place the iron down on the board while resting against the tab, heating both surfaces simultaneously. Apply light pressure to the top of the plastic case until the solder flows around the part and the part is flush with the PCB. If the solder is not flowing around the board you may need a higher wattage iron (generally 25W to 30W is enough).

### Initial Startup of Circuit

It is best to initially power up the board by setting the input supply voltage to the lowest operating input voltage 50.0V and set the input supply's current limit to zero. With the input supply off connect up the input supply to  $V_{in}$  and GND. Connect a digital volt meter and a load if needed to set the minimum load of the design from  $V_{out}$  and GND. Turn on the input supply and slowly turn up the current limit on the input supply. If the voltage starts to rise on the input supply continue increasing the input supply current limit while watching the output voltage. If the current increases on the input supply, but the voltage remains near zero, then there may be a short or a component misplaced on the board. Power down the board and visually inspect for solder bridges and recheck the diode and capacitor polarities. Once the power supply circuit is operational then more extensive testing may include full load testing, transient load and line tests to compare with simulation results.

### Load Testing

The setup is the same as the initial startup, except that an additional digital voltmeter is connected between  $V_{in}$  and GND, a load is connected between  $V_{out}$  and GND and a current meter is connected in series between  $V_{out}$  and the load. The load must be able to handle at least rated output power + 50% ( 7.5 watts for this design). Ideally the load is supplied in the form of a variable load test unit. It can also be done in the form of suitably large power resistors. When using an oscilloscope to measure waveforms on the prototype board, the ground leads of the oscilloscope probes should be as short as possible and the area of the loop formed by the ground lead should be kept to a minimum. This will help reduce ground lead inductance and eliminate EMI noise that is not actually present in the circuit.



### Design Assistance

1. Master key : 93728AD00F2D205F[v1]
2. **UCC28911** Product Folder : <http://www.ti.com/product/UCC28911> : contains the data sheet and other resources.

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