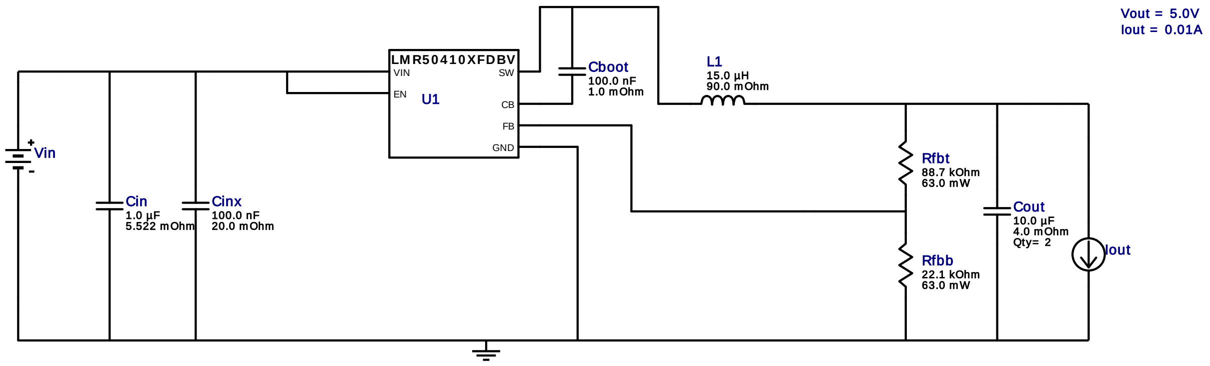
VinMin = 6.0V
VinMax = 24.0V
Vout = 5.0V
Iout = 0.01A

Device = LMR50410XFDBVR
Topology = Buck
Created = 2023-03-02 22:45:58.011
BOM Cost = $0.75
BOM Count = 9
Total Pd = 0.12W

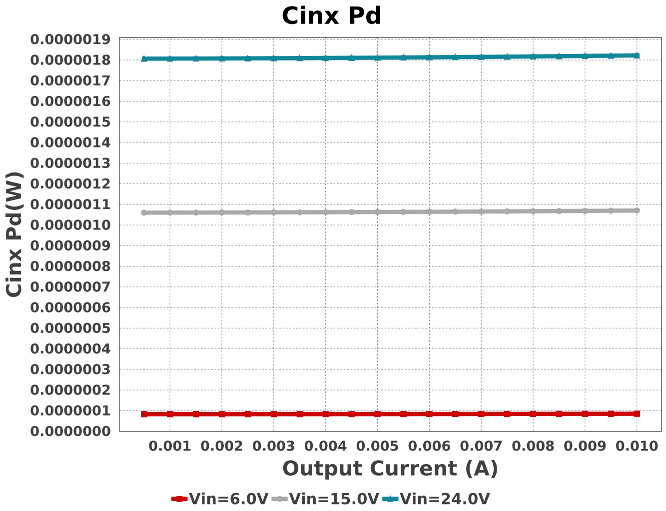
WEBENCH ® Design Report

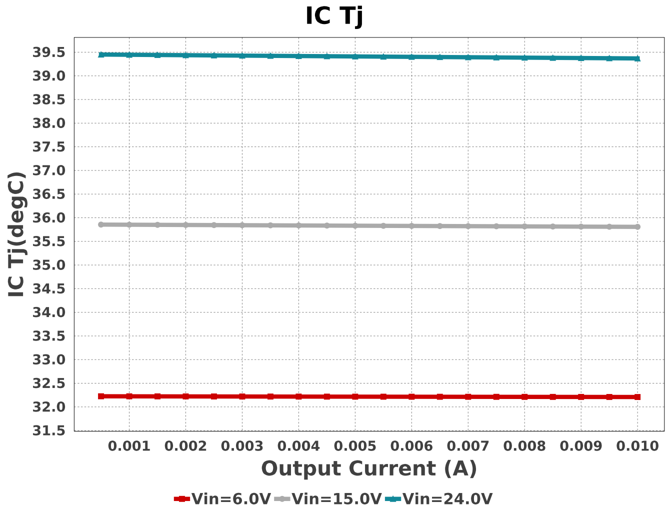
Design : 7839 LMR50410XFDBVR
LMR50410XFDBVR 6V-36V to 5.00V @ 1A

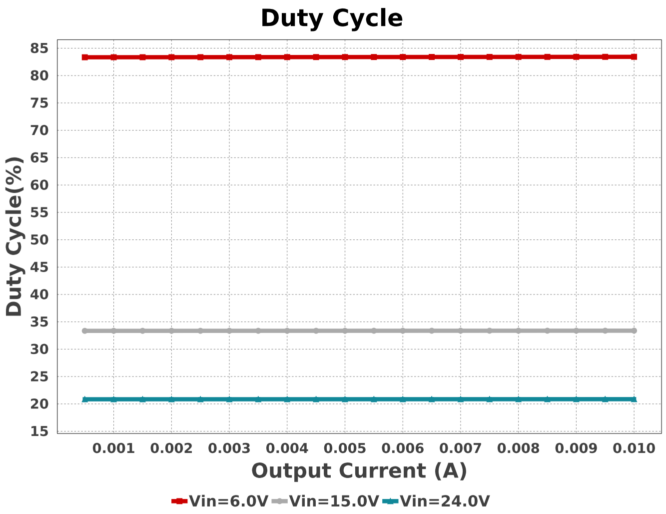


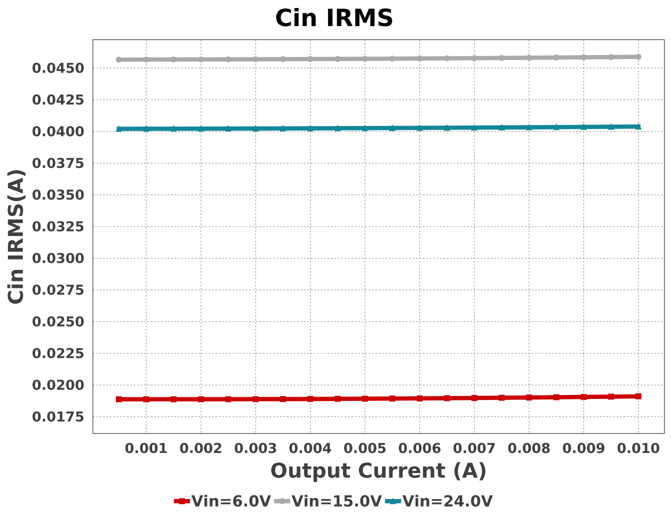
Electrical BOM

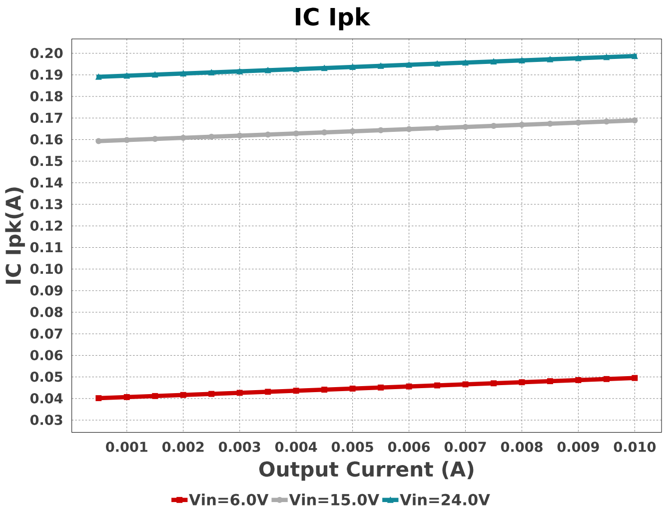
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Cboot	MuRata	GRM155R71C104KA88D Series= X7R	Cap= 100.0 nF ESR= 1.0 mOhm VDC= 16.0 V IRMS= 0.0 A	1	$0.01	0402 3 mm^2
Cin	TDK	C1608X7R1V105K080AC Series= X7R	Cap= 1.0 uF ESR= 5.522 mOhm VDC= 35.0 V IRMS= 2.2162 A	1	$0.05	0603 5 mm^2
Cinx	MuRata	GRM188R71H104KA93D Series= X7R	Cap= 100.0 nF ESR= 20.0 mOhm VDC= 50.0 V IRMS= 3.8 A	1	$0.02	0603 5 mm^2
Cout	MuRata	GRM31CR71E106KA12L Series= X7R	Cap= 10.0 uF ESR= 4.0 mOhm VDC= 25.0 V IRMS= 6.0 A	2	$0.06	1206_180 11 mm^2
L1	NIC Components	NPI75C150MTRF	L= 15.0 µH 90.0 mOhm	1	$0.11	IND_NPI75C 94 mm^2
Rfbb	Vishay-Dale	CRCW040222K1FKED Series= CRCW..e3	Res= 22.1 kOhm Power= 63.0 mW Tolerance= 1.0%	1	$0.01	0402 3 mm^2
Rfbt	Vishay-Dale	CRCW040288K7FKED Series= CRCW..e3	Res= 88.7 kOhm Power= 63.0 mW Tolerance= 1.0%	1	$0.01	0402 3 mm^2
U1	Texas Instruments	LMR50410XFDBVR	Switcher	1	$0.42	DBV0006A 15 mm^2

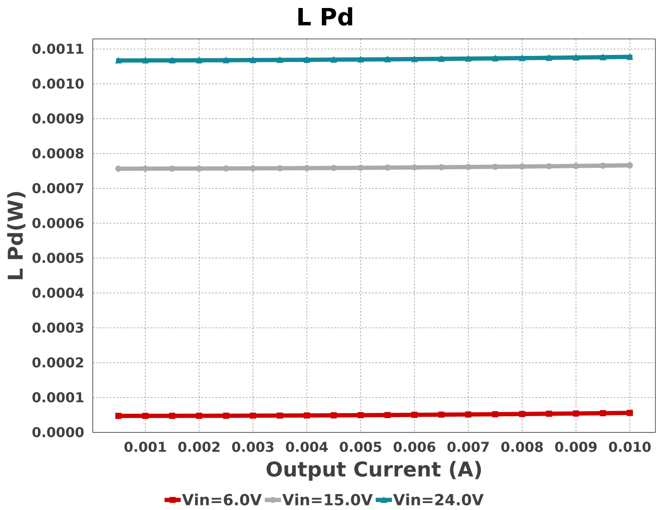

Cinx Pd
Cinx Pd(W)
Output Current (A)
Vin=6.0V
Vin=15.0V
Vin=24.0V

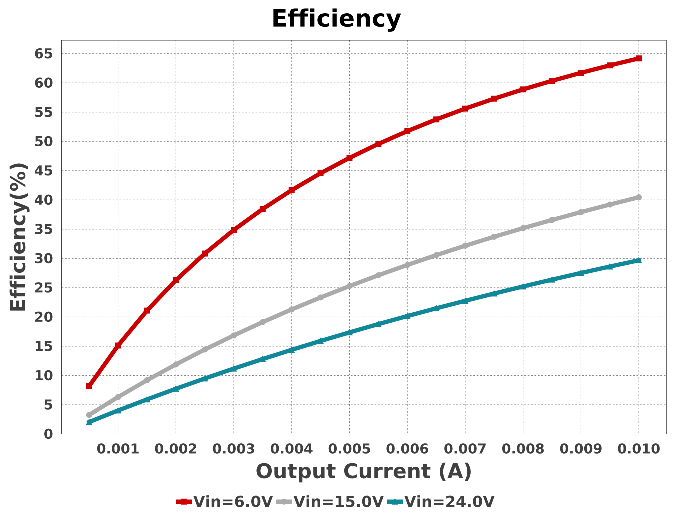

IC Tj
IC Tj(degC)
Output Current (A)
Vin=6.0V
Vin=15.0V
Vin=24.0V

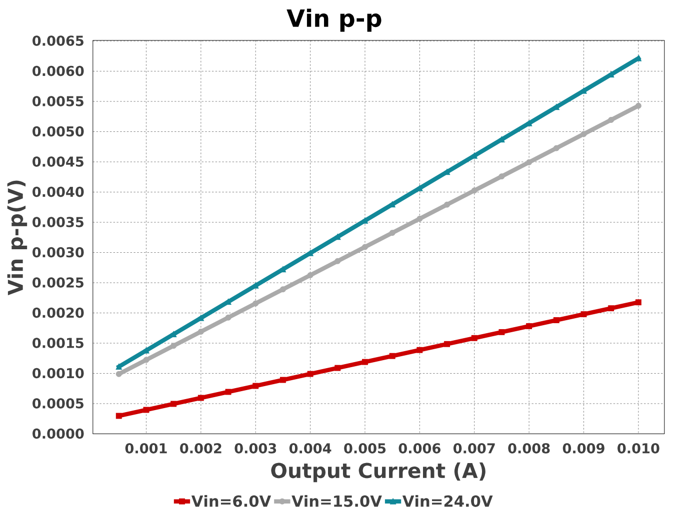

Duty Cycle
Duty Cycle(%)
Output Current (A)
Vin=6.0V
Vin=15.0V
Vin=24.0V

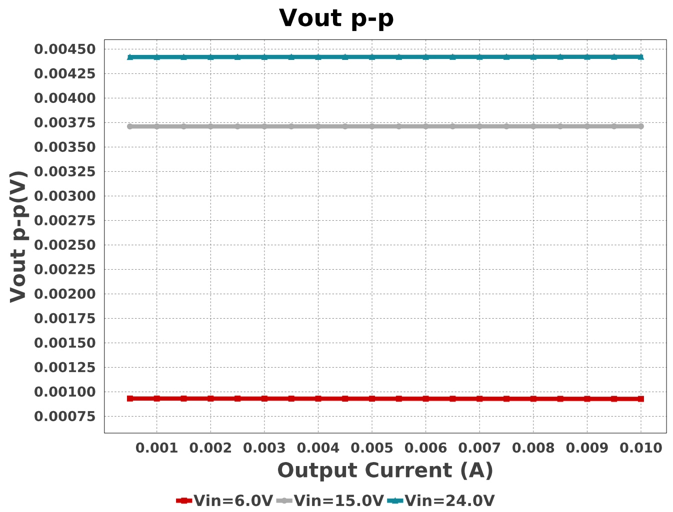

Cin IRMS
Cin IRMS(A)
Output Current (A)
Vin=6.0V
Vin=15.0V
Vin=24.0V

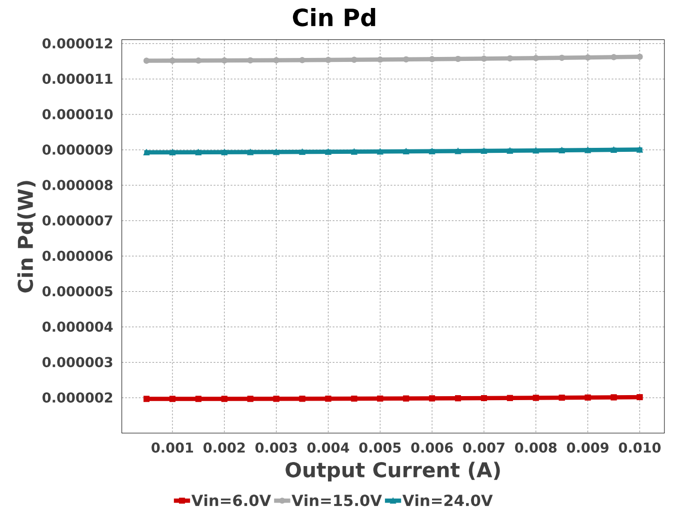

IC Ipk
IC Ipk(A)
Output Current (A)
Vin=6.0V
Vin=15.0V
Vin=24.0V

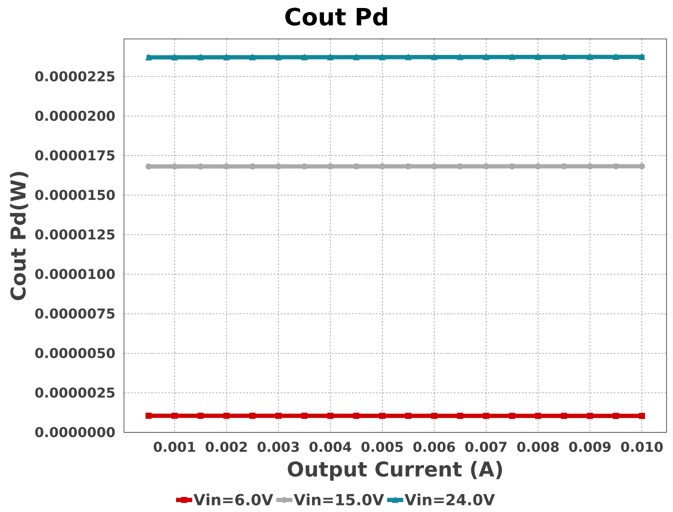

L Pd
L Pd(W)
Output Current (A)
Vin=6.0V
Vin=15.0V
Vin=24.0V

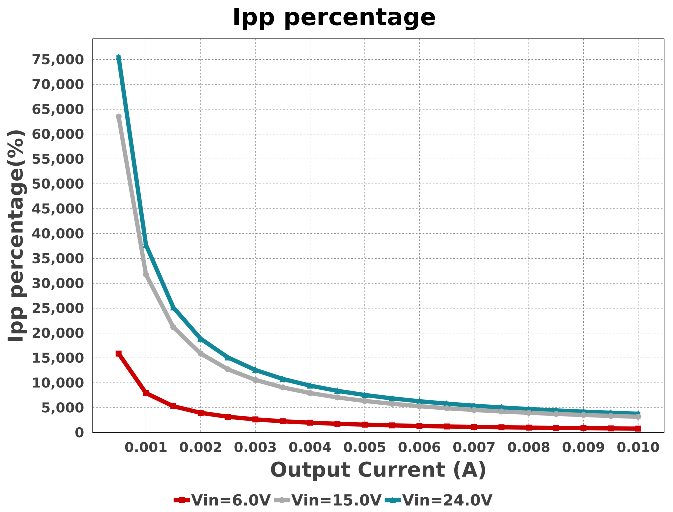

Efficiency
Efficiency(%)
Output Current (A)
Vin=6.0V Vin=15.0V Vin=24.0V

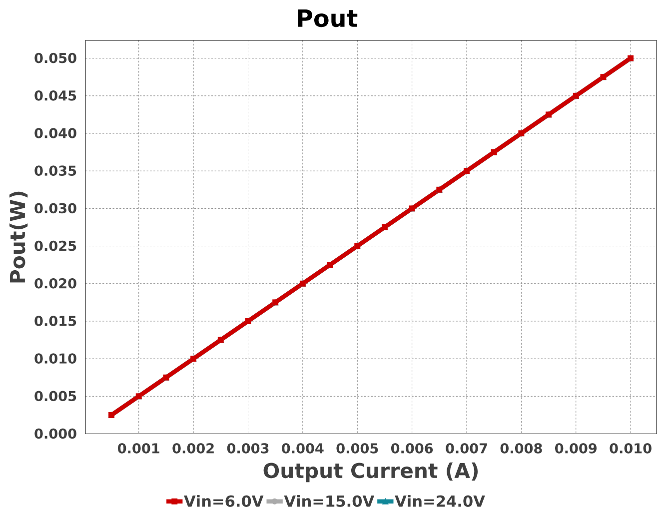

Vin p-p
Vin p-p(V)
Output Current (A)
Vin=6.0V Vin=15.0V Vin=24.0V

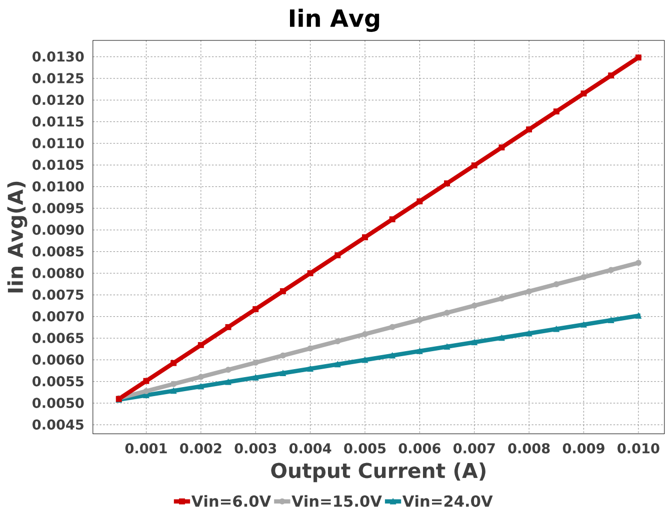

Vout p-p
Vout p-p(V)
Output Current (A)
Vin=6.0V Vin=15.0V Vin=24.0V

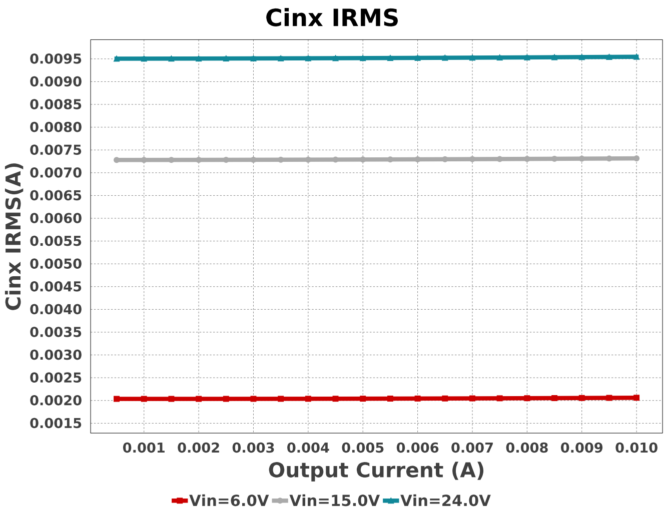

Cin Pd
Cin Pd(W)
Output Current (A)
Vin=6.0V Vin=15.0V Vin=24.0V

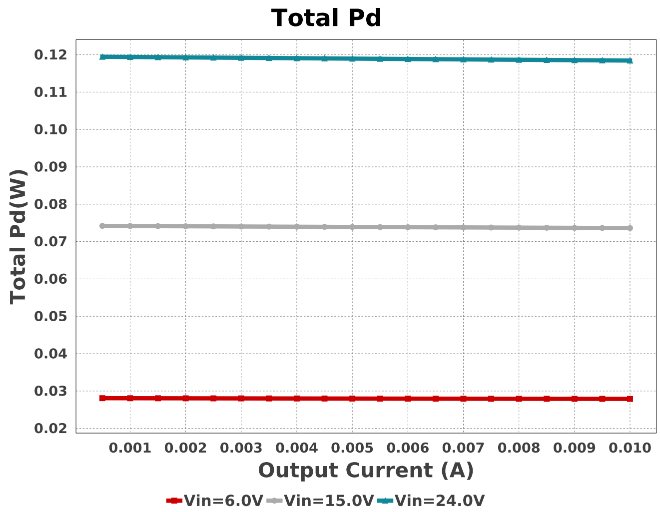

Cout Pd
Cout Pd(W)
Output Current (A)
Vin=6.0V Vin=15.0V Vin=24.0V

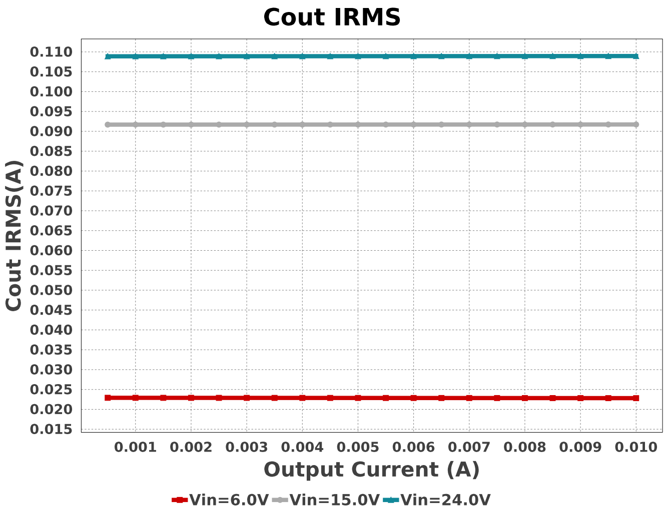

Ipp percentage
Ipp percentage(%)
Output Current (A)
Vin=6.0V Vin=15.0V Vin=24.0V

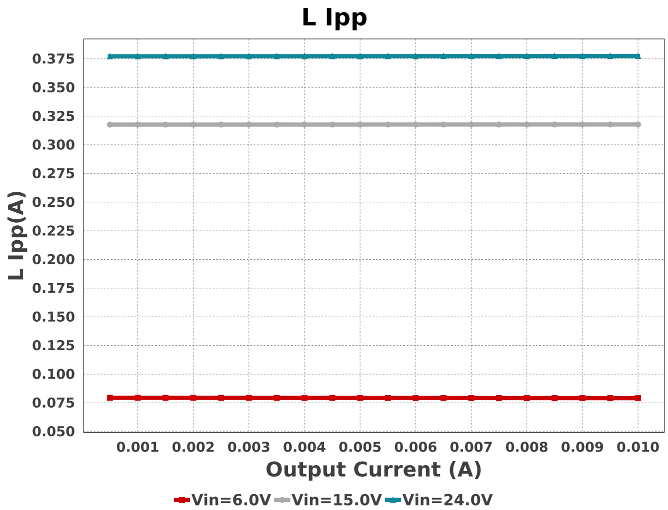

Pout
Pout(W)
0.050
0.045
0.040
0.035
0.030
0.025
0.020
0.015
0.010
0.005
0.000
0.001 0.002 0.003 0.004 0.005 0.006 0.007 0.008 0.009 0.010
Output Current (A)
Vin=6.0V Vin=15.0V Vin=24.0V

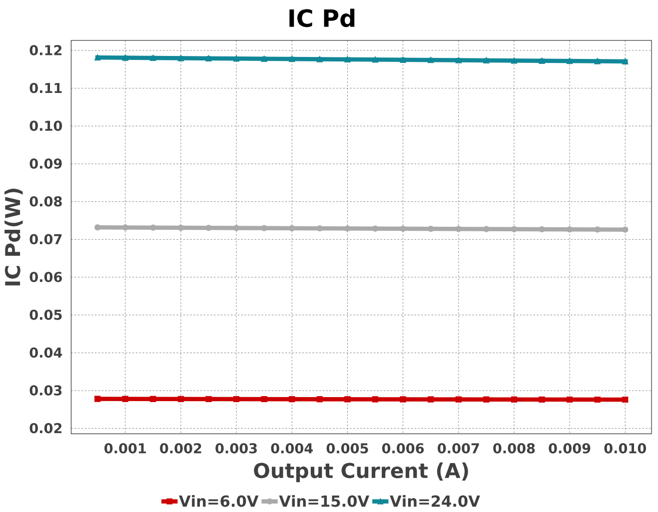

Iin Avg
Iin Avg(A)
0.0130
0.0125
0.0120
0.0115
0.0110
0.0105
0.0100
0.0095
0.0090
0.0085
0.0080
0.0075
0.0070
0.0065
0.0060
0.0055
0.0050
0.0045
0.001 0.002 0.003 0.004 0.005 0.006 0.007 0.008 0.009 0.010
Output Current (A)
Vin=6.0V Vin=15.0V Vin=24.0V

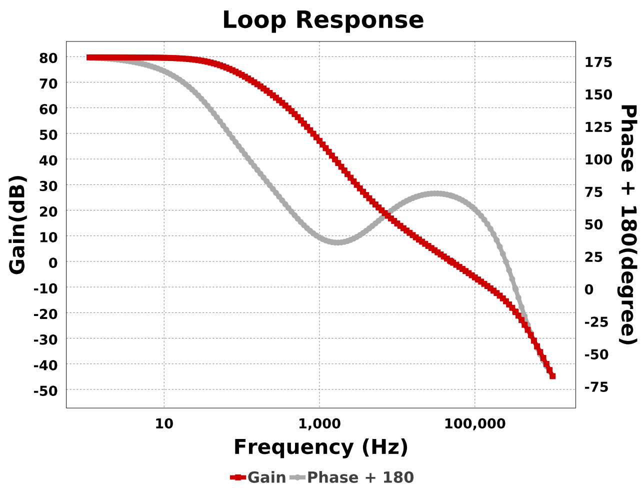

Cinx IRMS
Cinx IRMS(A)
0.0095
0.0090
0.0085
0.0080
0.0075
0.0070
0.0065
0.0060
0.0055
0.0050
0.0045
0.0040
0.0035
0.0030
0.0025
0.0020
0.0015
0.001 0.002 0.003 0.004 0.005 0.006 0.007 0.008 0.009 0.010
Output Current (A)
Vin=6.0V Vin=15.0V Vin=24.0V


Total Pd
Total Pd(W)
0.12
0.11
0.10
0.09
0.08
0.07
0.06
0.05
0.04
0.03
0.02
0.001 0.002 0.003 0.004 0.005 0.006 0.007 0.008 0.009 0.010
Output Current (A)
Vin=6.0V Vin=15.0V Vin=24.0V


Cout IRMS
Cout IRMS(A)
0.110
0.105
0.100
0.095
0.090
0.085
0.080
0.075
0.070
0.065
0.060
0.055
0.050
0.045
0.040
0.035
0.030
0.025
0.020
0.015
0.001 0.002 0.003 0.004 0.005 0.006 0.007 0.008 0.009 0.010
Output Current (A)
Vin=6.0V Vin=15.0V Vin=24.0V


L Ipp
L Ipp(A)
0.375
0.350
0.325
0.300
0.275
0.250
0.225
0.200
0.175
0.150
0.125
0.100
0.075
0.050
0.001 0.002 0.003 0.004 0.005 0.006 0.007 0.008 0.009 0.010
Output Current (A)
Vin=6.0V Vin=15.0V Vin=24.0V





Operating Values

#	Name	Value	Category	Description
1.	Cin IRMS	40.388 mA	Capacitor	Input capacitor RMS ripple current
2.	Cin Pd	9.008 µW	Capacitor	Input capacitor power dissipation
3.	Cinx IRMS	9.546 mA	Capacitor	Bulk capacitor RMS ripple current
4.	Cinx Pd	1.822 µW	Capacitor	Bulk capacitor power dissipation
5.	Cout IRMS	108.956 mA	Capacitor	Output capacitor RMS ripple current
6.	Cout Pd	23.743 µW	Capacitor	Output capacitor power dissipation
7.	IC Ipk	198.718 mA	IC	Peak switch current in IC
8.	IC Pd	117.1 mW	IC	IC power dissipation
9.	IC Tj	39.368 degC	IC	IC junction temperature
10.	IC Tolerance	15.0 mV	IC	IC Feedback Tolerance
11.	ICThetaJA Effective	80.0 degC/W	IC	Effective IC Junction-to-Ambient Thermal Resistance
12.	Iin Avg	7.018 mA	IC	Average input current
13.	Ipp percentage	3.774 k%	Inductor	Inductor ripple current percentage (with respect to average inductor current)
14.	L Ipp	377.44 mA	Inductor	Peak-to-peak inductor ripple current
15.	L Pd	1.077 mW	Inductor	Inductor power dissipation
16.	Cin Pd	9.008 µW	Power	Input capacitor power dissipation
17.	Cinx Pd	1.822 µW	Power	Bulk capacitor power dissipation
18.	Cout Pd	23.743 µW	Power	Output capacitor power dissipation
19.	IC Pd	117.1 mW	Power	IC power dissipation
20.	L Pd	1.077 mW	Power	Inductor power dissipation
21.	Total Pd	118.441 mW	Power	Total Power Dissipation
22.	BOM Count	9	System Information	Total Design BOM count
23.	Cross Freq	49.598 kHz	System Information	Bode plot crossover frequency
24.	Duty Cycle	20.864 %	System Information	Duty cycle
25.	Efficiency	29.684 %	System Information	Steady state efficiency
26.	FootPrint	149.0 mm^2	System Information	Total Foot Print Area of BOM components
27.	Frequency	700.0 kHz	System Information	Switching frequency
28.	Gain Marg	-19.807 dB	System Information	Bode Plot Gain Margin
29.	Iout	10.0 mA	System Information	Iout operating point
30.	Low Freq Gain	79.694 dB	System Information	Gain at 1Hz
31.	Mode	FCCM	System Information	Conduction Mode
32.	Phase Marg	72.285 deg	System Information	Bode Plot Phase Margin
33.	Pout	50.0 mW	System Information	Total output power
34.	Total BOM	$0.75	System Information	Total BOM Cost
35.	Vin	24.0 V	System Information	Vin operating point
36.	Vin p-p	6.214 mV	System Information	Peak-to-peak input voltage

#	Name	Value	Category	Description
37.	Vout	5.0 V	System Information	Operational Output Voltage
38.	Vout Actual	5.014 V	System Information	Vout Actual calculated based on selected voltage divider resistors
39.	Vout Tolerance	3.142 %	System Information	Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable
40.	Vout p-p	4.422 mV	System Information	Peak-to-peak output ripple voltage

Design Inputs

Name	Value	Description
Iout	10.0 m	Maximum Output Current
VinMax	24.0	Maximum input voltage
VinMin	6.0	Minimum input voltage
Vout	5.0	Output Voltage
base_pn	LMR50410XF	Base Product Number
source	DC	Input Source Type
Ta	30.0	Ambient temperature

WEBENCH® Assembly

Component Testing

Some published data on components in datasheets such as Capacitor ESR and Inductor DC resistance is based on conservative values that will guarantee that the components always exceed the specification. For design purposes it is usually better to work with typical values. Since this data is not always available it is a good practice to measure the Capacitance and ESR values of Cin and Cout, and the inductance and DC resistance of L1 before assembly of the board. Any large discrepancies in values should be electrically simulated in WEBENCH to check for instabilities and thermally simulated in WebTHERM to make sure critical temperatures are not exceeded.

Soldering Component to Board

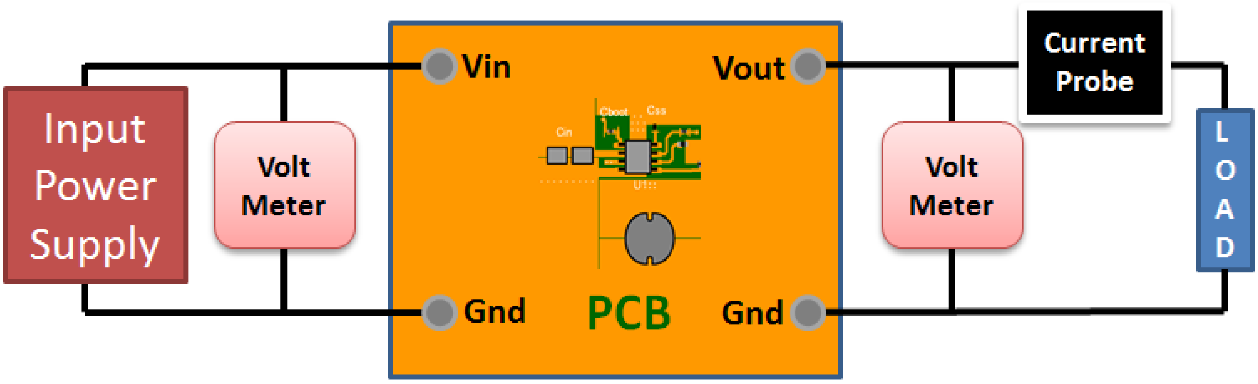
If board assembly is done in house it is best to tack down one terminal of a component on the board then solder the other terminal. For surface mount parts with large tabs, such as the DPAK, the tab on the back of the package should be pre-tinned with solder, then tacked into place by one of the pins. To solder the tab town to the board place the iron down on the board while resting against the tab, heating both surfaces simultaneously. Apply light pressure to the top of the plastic case until the solder flows around the part and the part is flush with the PCB. If the solder is not flowing around the board you may need a higher wattage iron (generally 25W to 30W is enough).

Initial Startup of Circuit

It is best to initially power up the board by setting the input supply voltage to the lowest operating input voltage 6.0V and set the input supply's current limit to zero. With the input supply off connect up the input supply to Vin and GND. Connect a digital volt meter and a load if needed to set the minimum Iout of the design from Vout and GND. Turn on the input supply and slowly turn up the current limit on the input supply. If the voltage starts to rise on the input supply continue increasing the input supply current limit while watching the output voltage. If the current increases on the input supply, but the voltage remains near zero, then there may be a short or a component misplaced on the board. Power down the board and visually inspect for solder bridges and recheck the diode and capacitor polarities. Once the power supply circuit is operational then more extensive testing may include full load testing, transient load and line tests to compare with simulation results.

Load Testing

The setup is the same as the initial startup, except that an additional digital voltmeter is connected between Vin and GND, a load is connected between Vout and GND and a current meter is connected in series between Vout and the load. The load must be able to handle at least rated output power + 50% (7.5 watts for this design). Ideally the load is supplied in the form of a variable load test unit. It can also be done in the form of suitably large power resistors. When using an oscilloscope to measure waveforms on the prototype board, the ground leads of the oscilloscope probes should be as short as possible and the area of the loop formed by the ground lead should be kept to a minimum. This will help reduce ground lead inductance and eliminate EMI noise that is not actually present in the circuit.

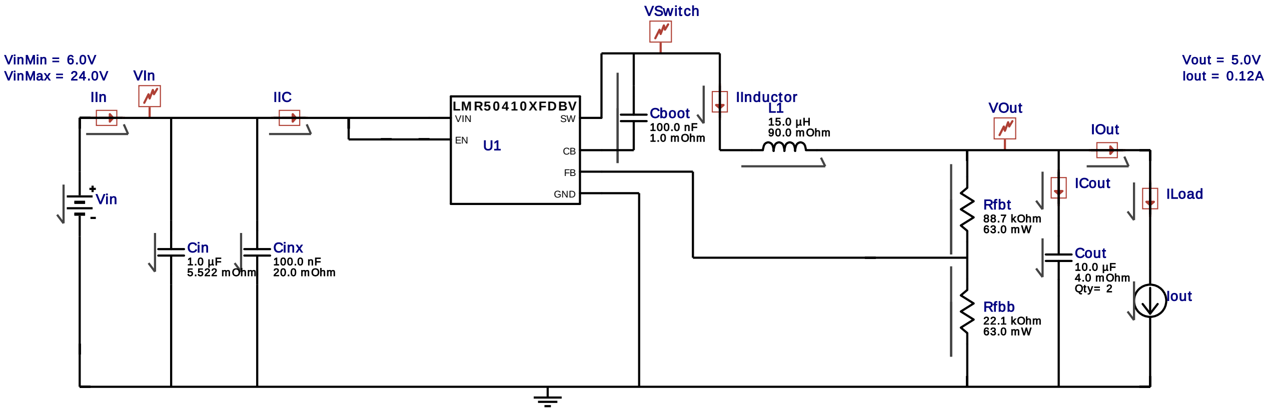


WEBENCH® Electrical Simulation Report

Design Id = 7839

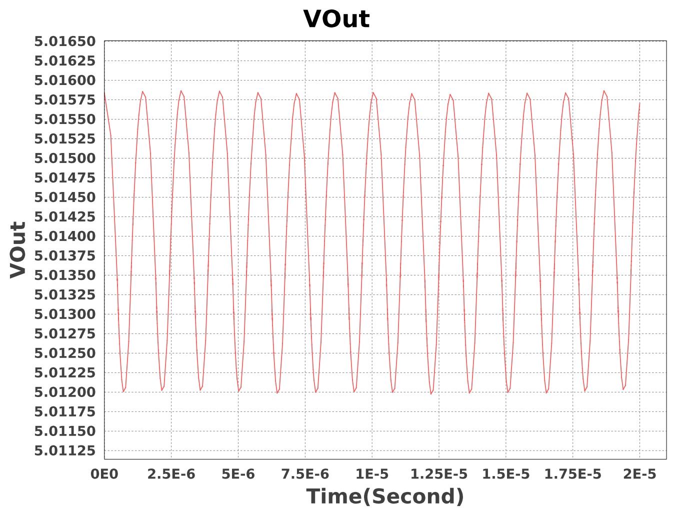
sim_id = 2

Simulation Type = Steady State



Simulation Parameters

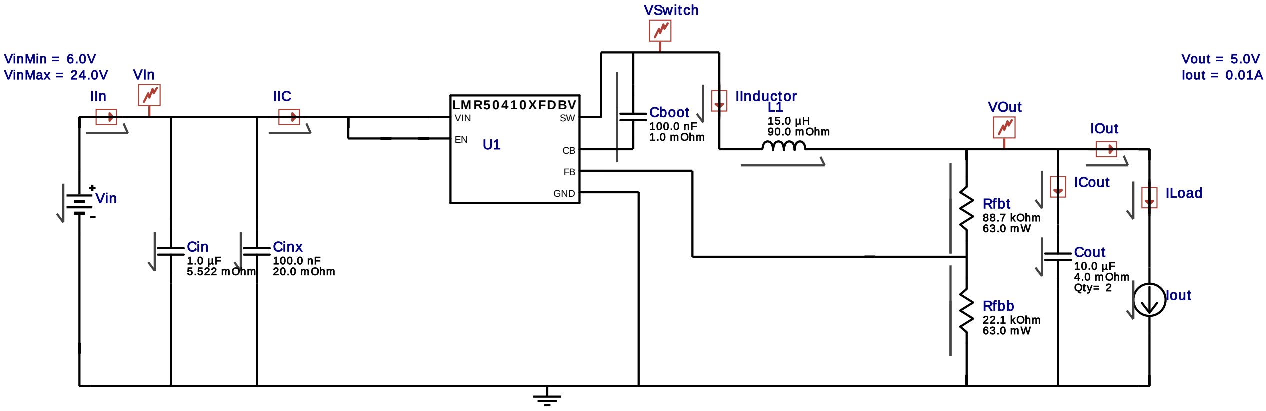
#	Name	Parameter Name	Description	Values
1.	L1	IC	Initial Current	0.12 A
2.	Iout	I	Output Current	0.12 A



Design Id = 7839

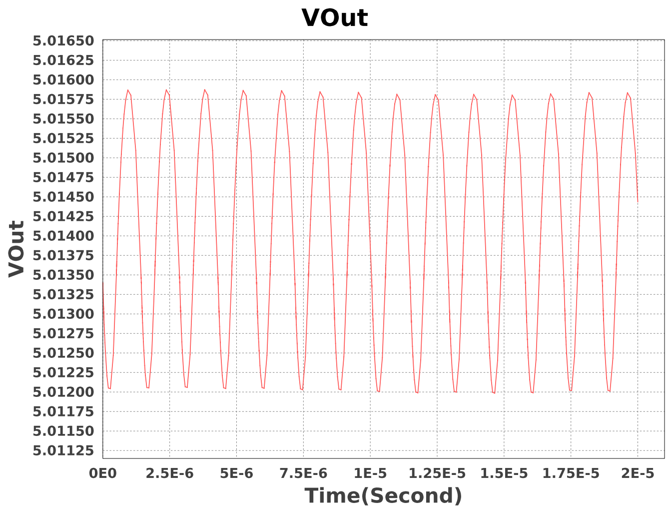
sim_id = 3

Simulation Type = Steady State



Simulation Parameters

#	Name	Parameter Name	Description	Values
1.	L1	IC	Initial Current	0.01 A
2.	Iout	I	Output Current	0.01 A



Design Assistance

1. Master key : AE3011FD2EE8F4EB[v1]

2. **LMR50410XF** Product Folder : http://www.ti.com/product/LMR50410 : contains the data sheet and other resources.

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