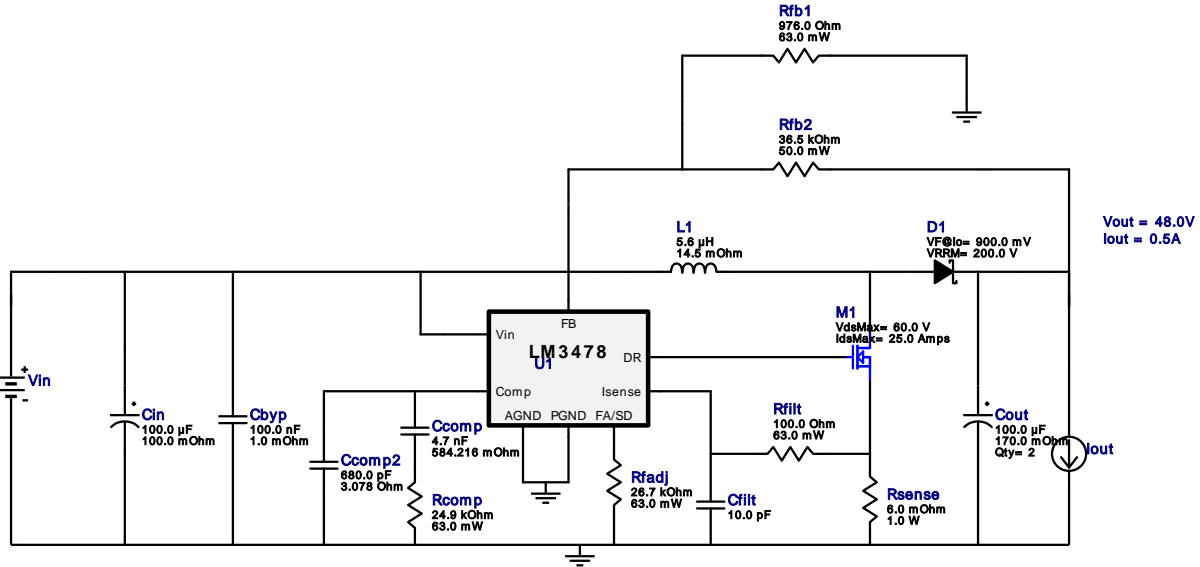


VinMin = 4.9V
 VinMax = 5.1V
 Vout = 48.0V
 Iout = 0.5A

Device = LM3478MM/NOPB
 Topology = Boost
 Created = 2023-12-01 03:28:35.160
 BOM Cost = \$6.04
 BOM Count = 17
 Total Pd = 1.37W

WEBENCH® Design Report

Design : 8 LM3478MM/NOPB
 LM3478MM/NOPB 4.9V-5.1V to 48.00V @ 0.5A

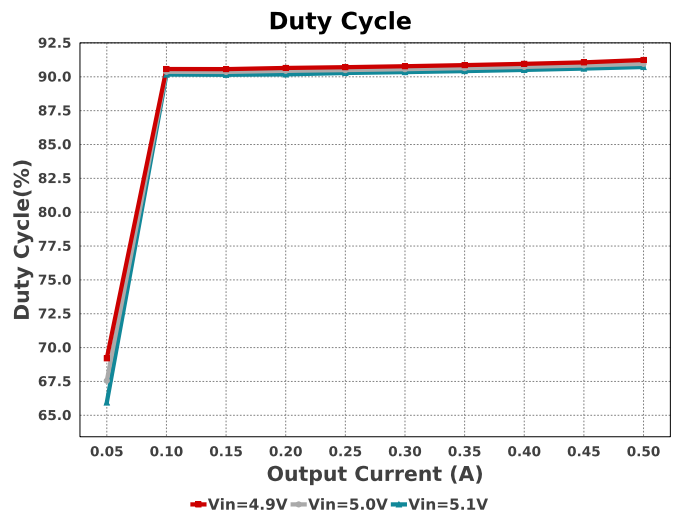
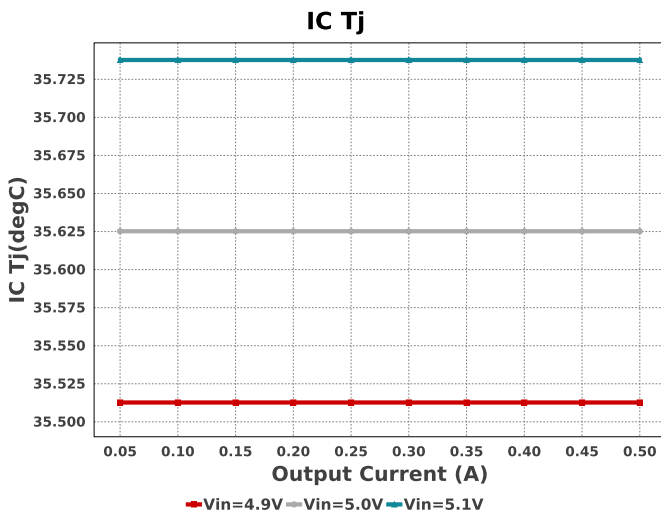


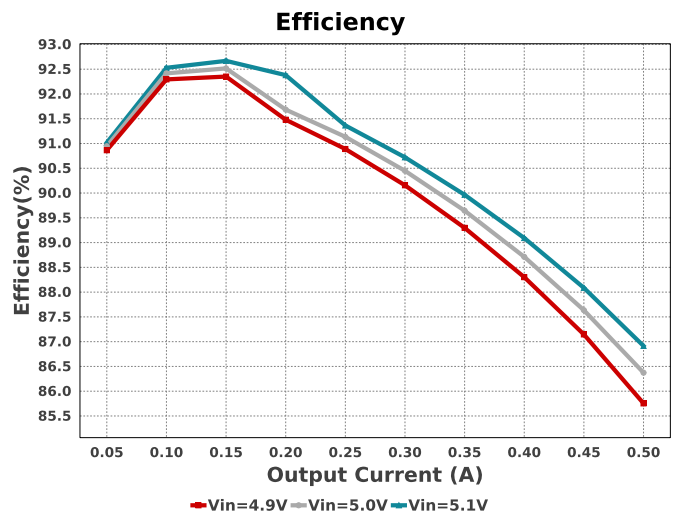
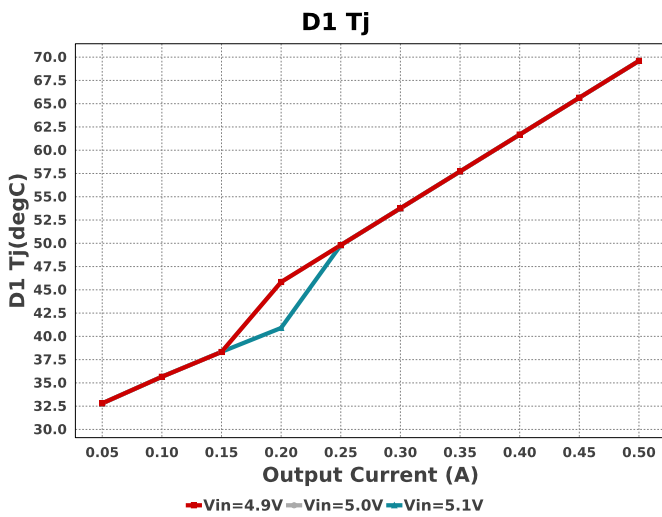
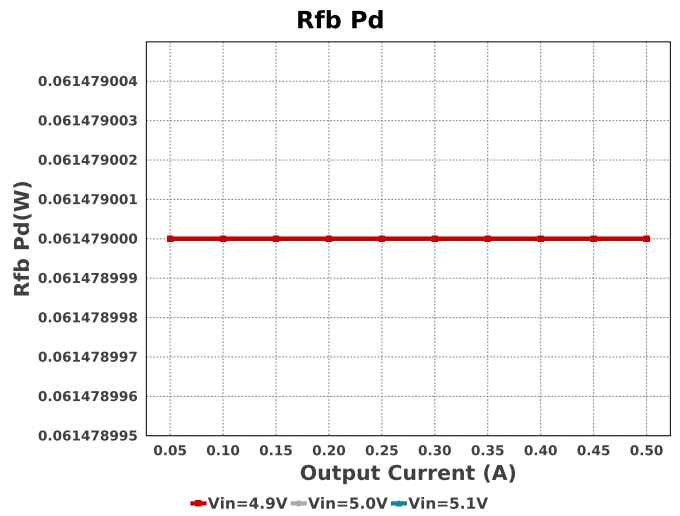
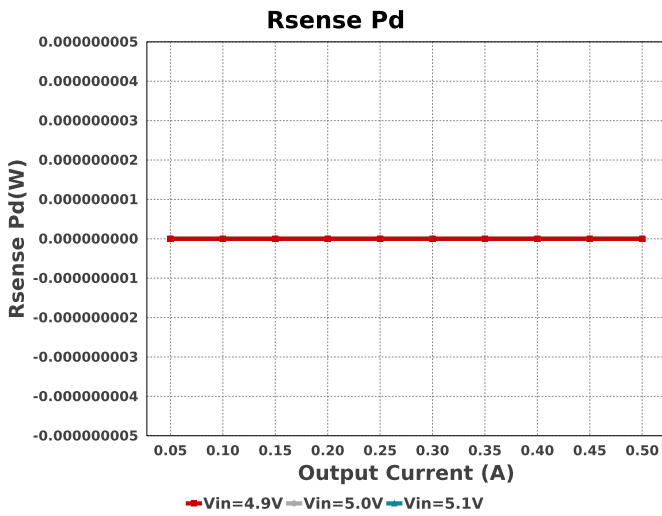
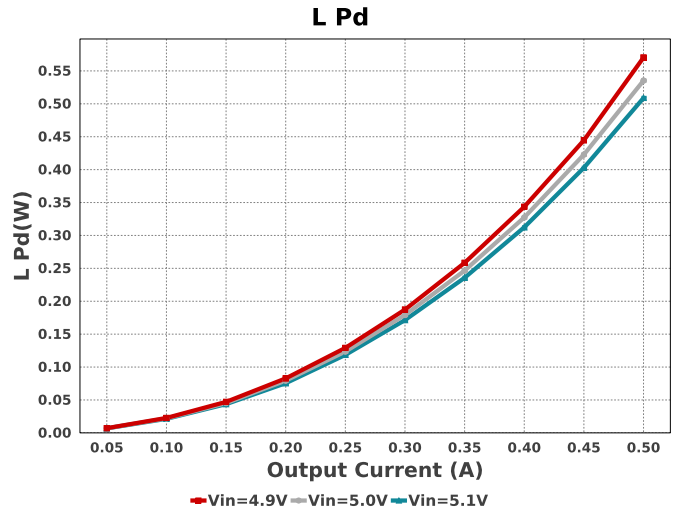
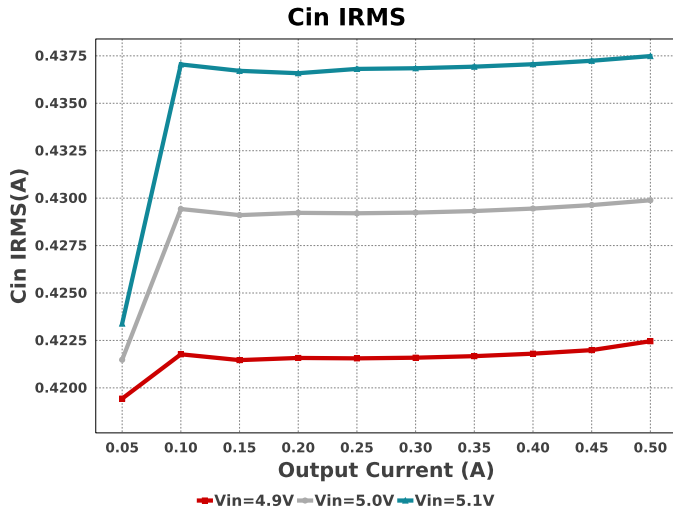
1. With the low turn of voltage of the LM34x8 your power supply may current limit before you reach your working input voltage. If this happens, or to preempt this from happening, you can include a low pass RC filter from input voltage to Vin on the IC. Make sure the rise time on the RC network is slower than your supply's rise time.

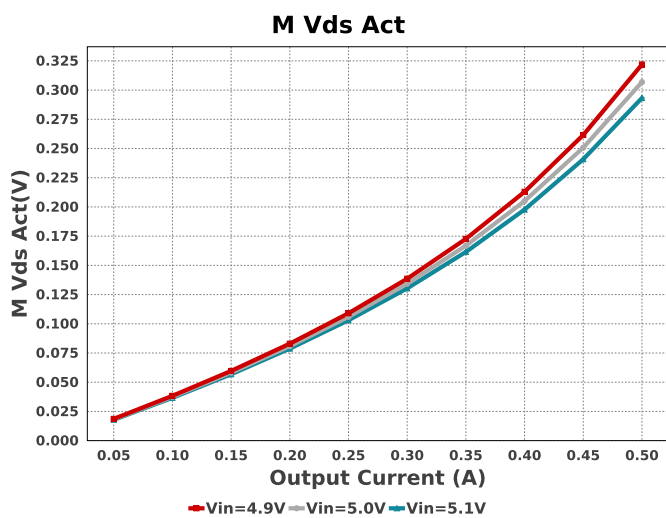
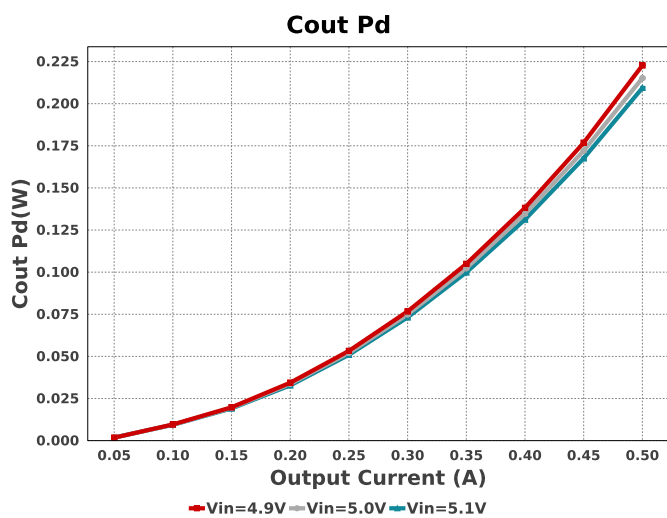
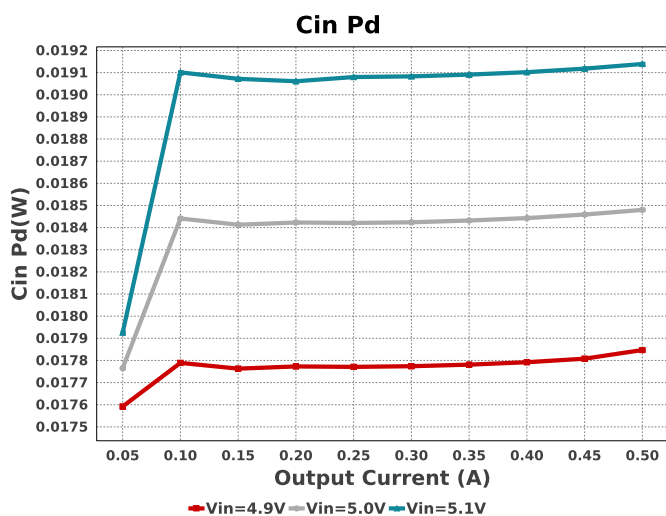
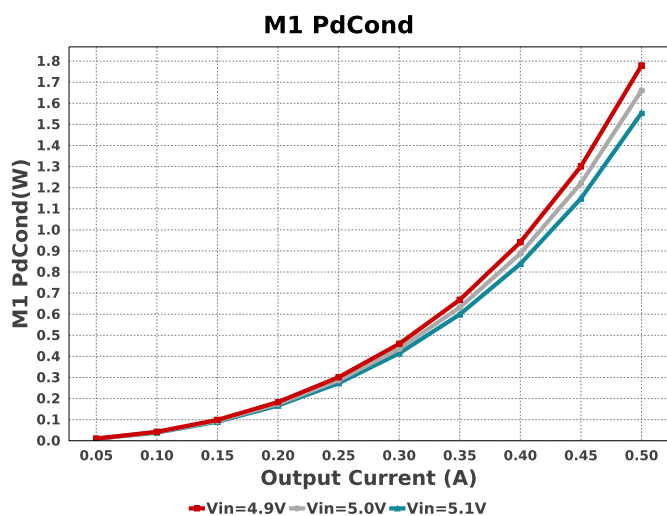
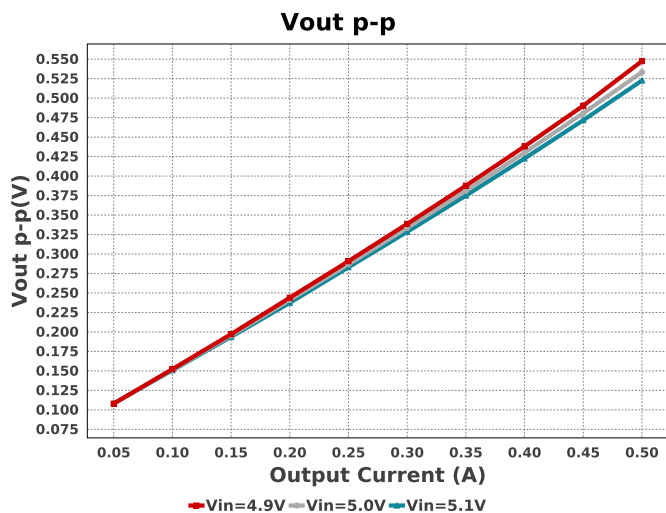
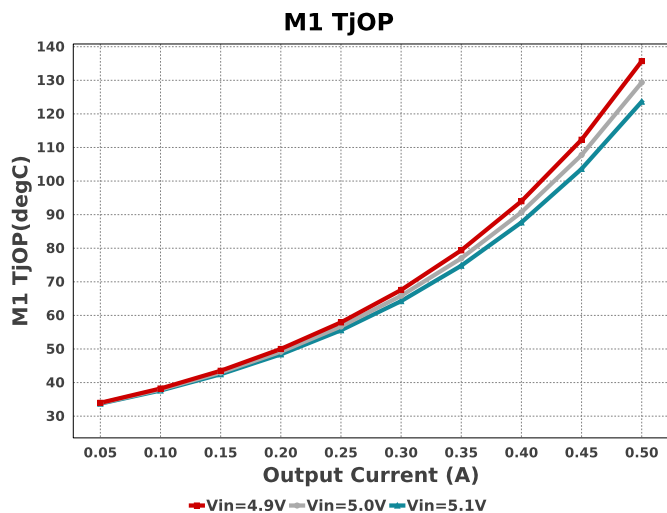
Electrical BOM

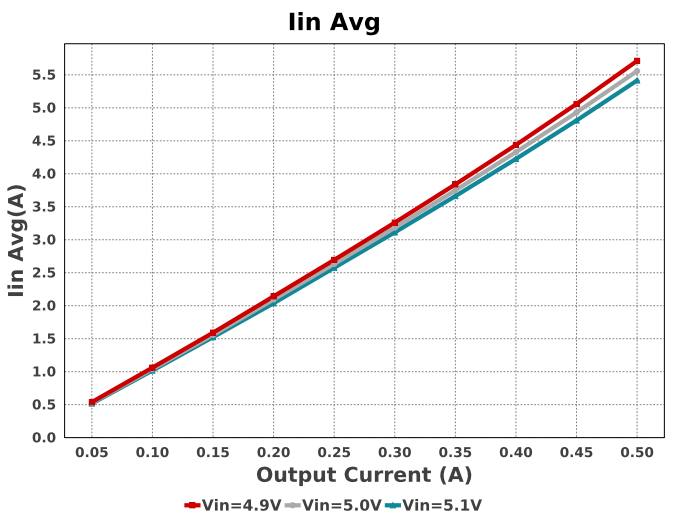
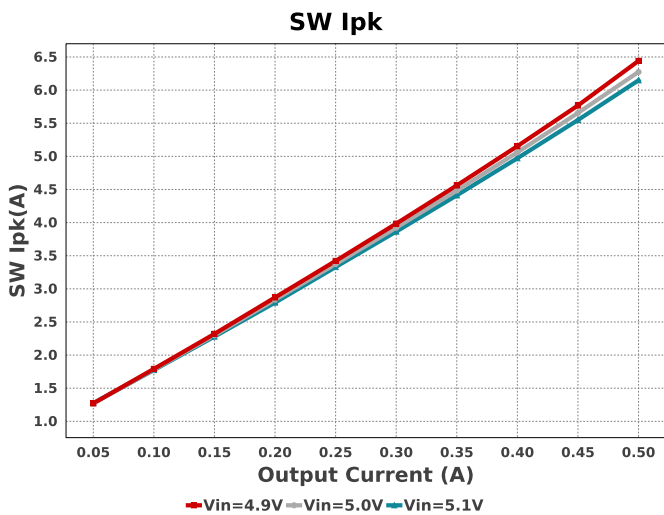
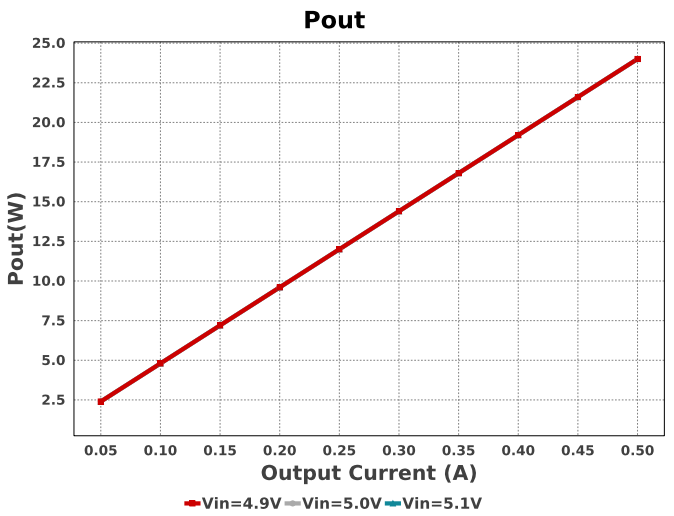
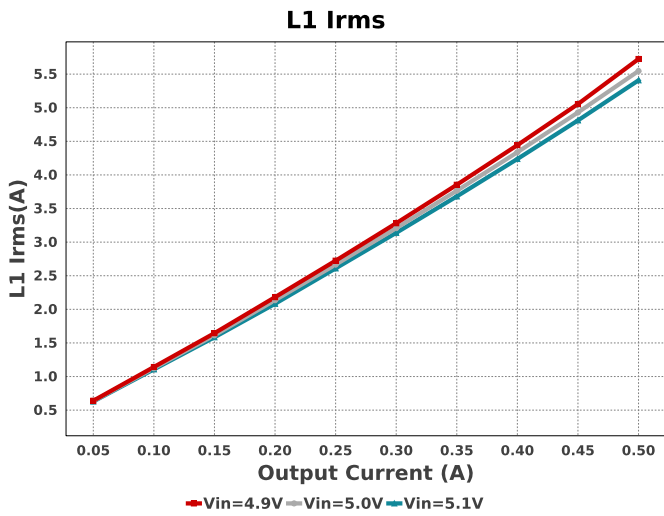
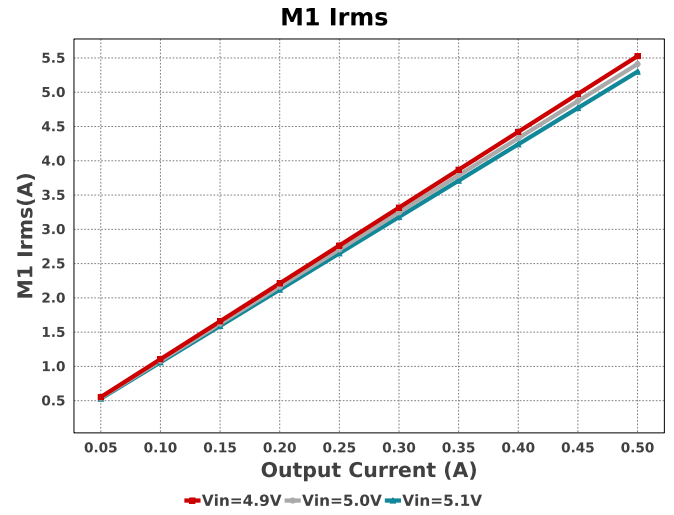
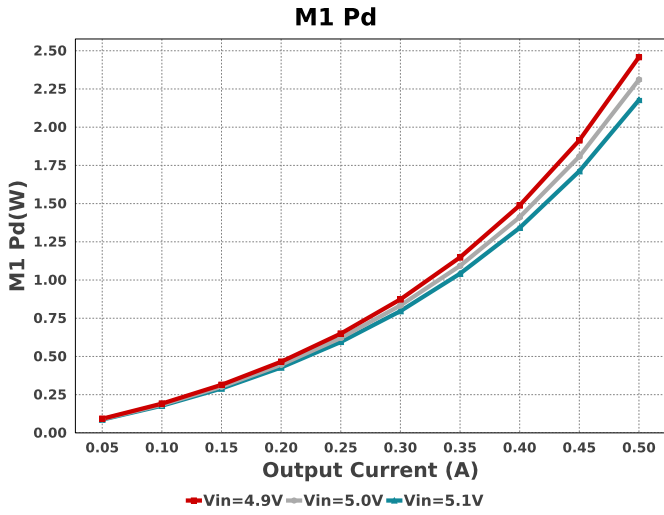
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Cbyp	MuRata	GRM155R70J104KA01D Series= X7R	Cap= 100.0 nF ESR= 1.0 mOhm VDC= 6.3 V IRMS= 0.0 A	1	\$0.01	0402 3 mm ²
Ccomp	TDK	CGA1A2X7R1C472K030BA Series= X7R	Cap= 4.7 nF ESR= 584.22 mOhm VDC= 16.0 V IRMS= 218.326 mA	1	\$0.01	0201_033 2 mm ²
Ccomp2	TDK	CGA2B2X7R1H681K050BA Series= X7R	Cap= 680.0 pF ESR= 3.0778 Ohm VDC= 50.0 V IRMS= 257.63 mA	1	\$0.01	0402 3 mm ²
Cfilt	Yageo	CC0805JRNPO9BN100 Series= C0G/NP0	Cap= 10.0 pF VDC= 50.0 V IRMS= 0.0 A	1	\$0.01	0805 7 mm ²
Cin	Vishay-Sprague	593D107X0010D2TE3 Series= 593D	Cap= 100.0 uF ESR= 100.0 mOhm VDC= 10.0 V IRMS= 1.22 A	1	\$0.30	7343-31 59 mm ²
Cout	Panasonic	EEV-FK2A101M Series= FK	Cap= 100.0 uF ESR= 170.0 mOhm VDC= 100.0 V IRMS= 793.0 mA	2	\$1.44	SM_RADIAL_J16 399 mm ²

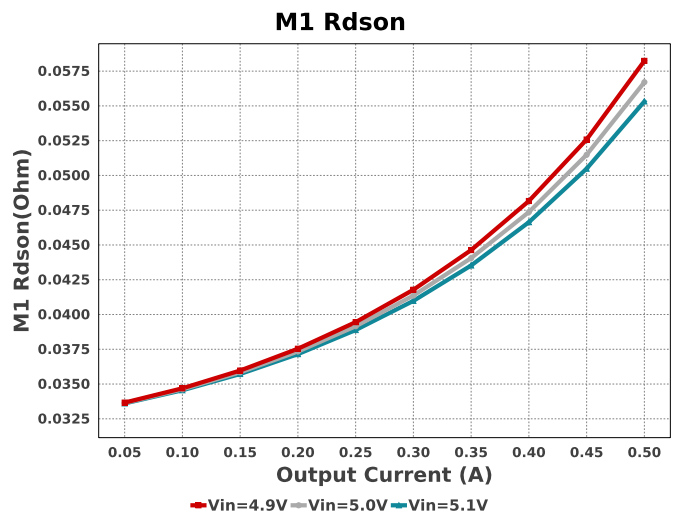
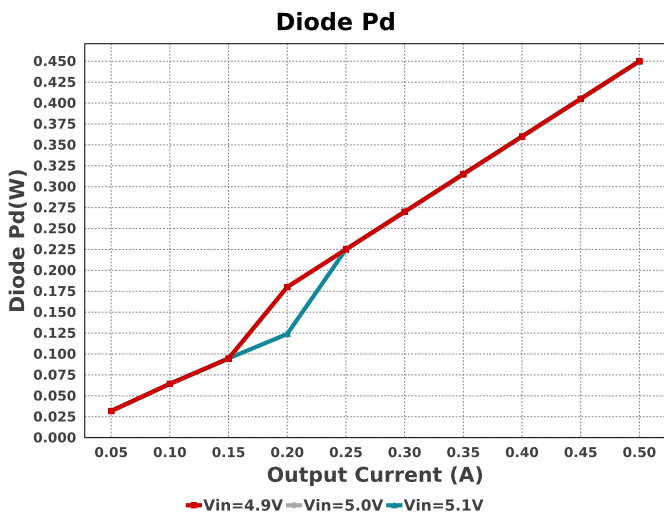
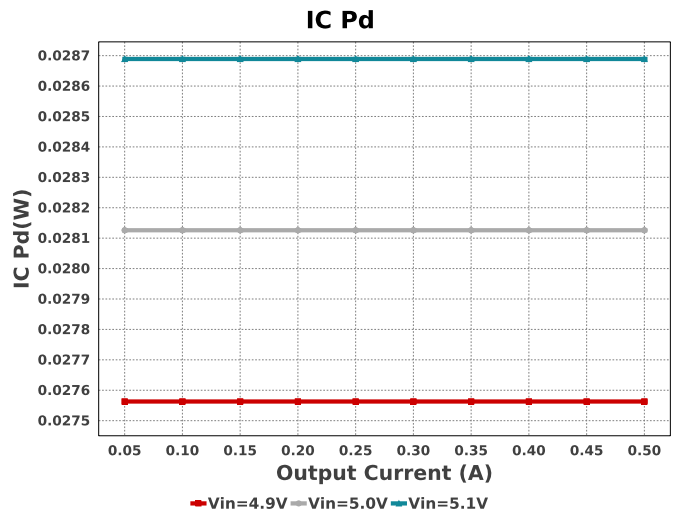
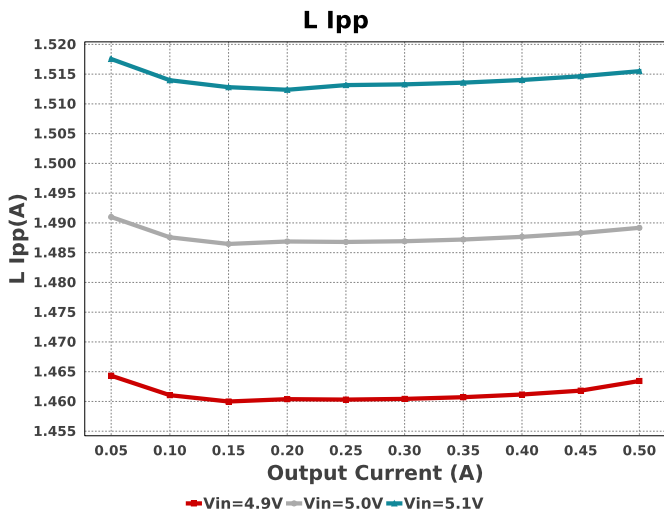
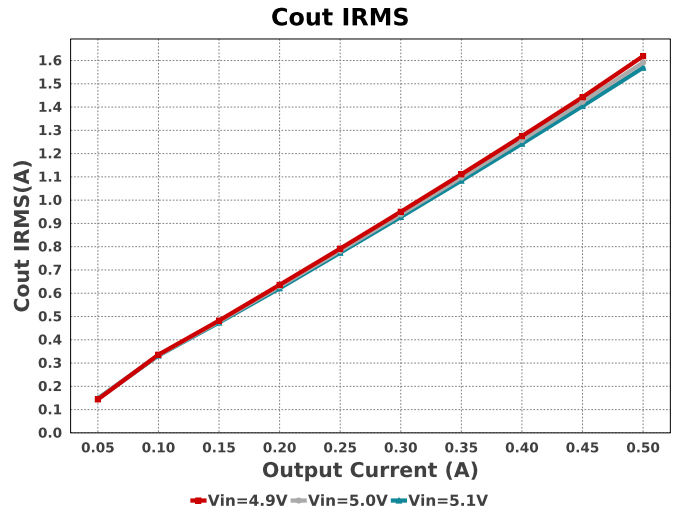
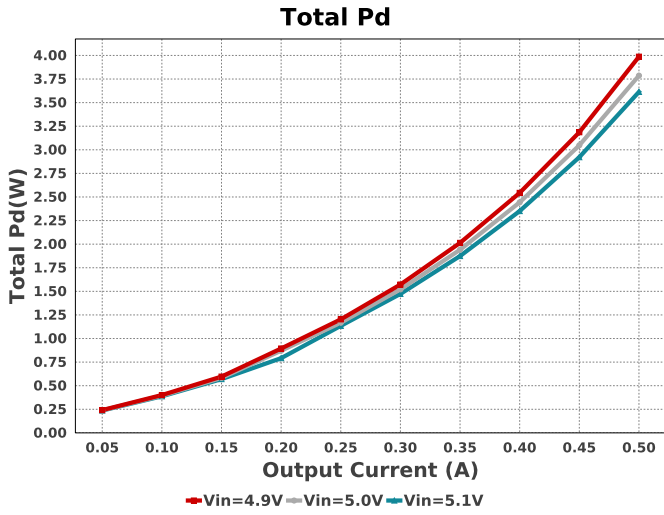
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
D1	SMC Diode Solutions	SK220ATR	VF@Io= 900.0 mV VRRM= 200.0 V	1	\$0.06	 SMA 37 mm ²
L1	Coilcraft	XAL6060-562MEB	L= 5.6 µH 14.5 mOhm	1	\$0.82	 XAL6060 72 mm ²
M1	ON Semiconductor	NVMFS5C682NLWFAFT3G	VdsMax= 60.0 V IdsMax= 25.0 Amps	1	\$0.64	FP- NVMFS5C682NLWFAFT3G_DFN5- MFG 0 mm ²
Rcomp	Vishay-Dale	CRCW040224K9FKED Series= CRCW..e3	Res= 24.9 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm ²
Rfadj	Vishay-Dale	CRCW040226K7FKED Series= CRCW..e3	Res= 26.7 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm ²
Rfb1	Vishay-Dale	CRCW0402976RFKED Series= CRCW..e3	Res= 976.0 Ohm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm ²
Rfb2	Yageo	RC0201FR-0736K5L Series= ?	Res= 36.5 kOhm Power= 50.0 mW Tolerance= 1.0%	1	\$0.01	 0201 2 mm ²
Rfilt	Vishay-Dale	CRCW0402100RFKED Series= CRCW..e3	Res= 100.0 Ohm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm ²
Rsense	Susumu Co Ltd	PRL1632-R006-F-T1 Series= PRL1632	Res= 6.0 mOhm Power= 1.0 W Tolerance= 1.0%	1	\$0.20	 0612 11 mm ²
U1	Texas Instruments	LM3478MM/NOPB	Switcher	1	\$1.05	 MUA08A 24 mm ²

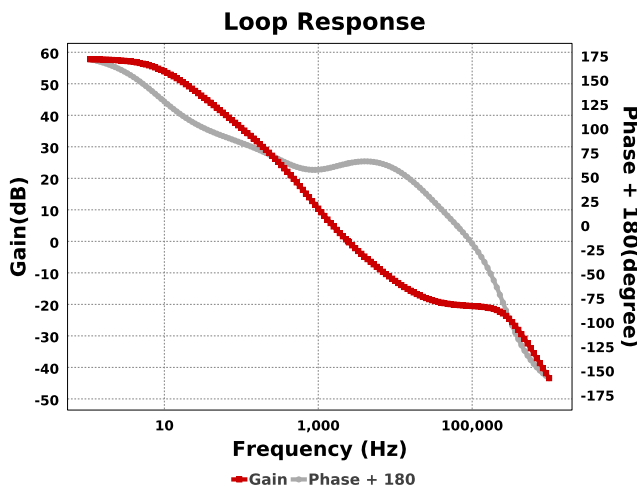
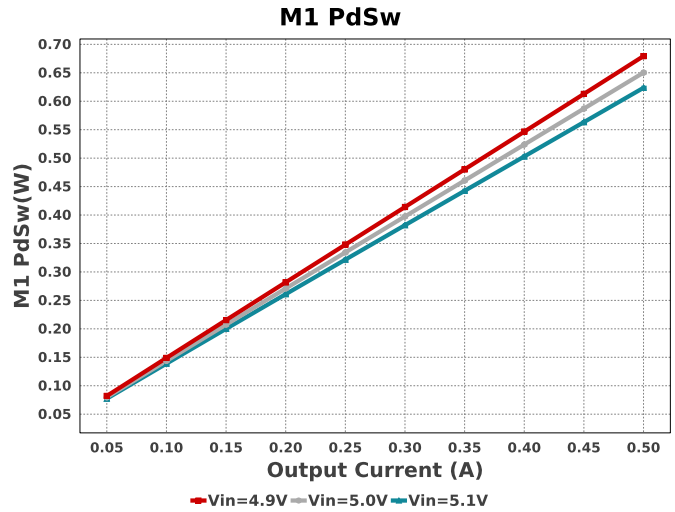
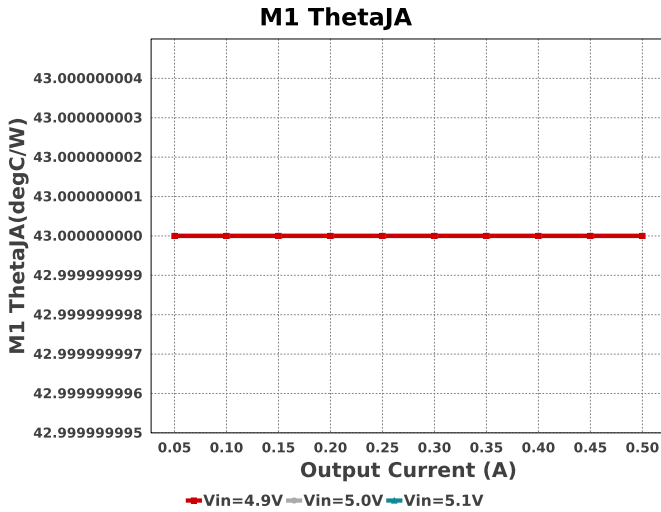












Operating Values

#	Name	Value	Category	Description
1.	Cin IRMS	418.388 mA	Capacitor	Input capacitor RMS ripple current
2.	Cin Pd	17.505 mW	Capacitor	Input capacitor power dissipation
3.	Cout IRMS	1.531 A	Capacitor	Output capacitor RMS ripple current
4.	Cout Pd	199.31 mW	Capacitor	Output capacitor power dissipation
5.	D1 Tj	69.6 degC	Diode	D1 junction temperature
6.	Diode Pd	450.0 mW	Diode	Diode power dissipation
7.	IC Pd	27.685 mW	IC	IC power dissipation
8.	IC Tj	35.537 degC	IC	IC junction temperature
9.	IC Tolerance	24.3 mV	IC	IC Feedback Tolerance
10.	ICThetaJA	200.0 degC/W	IC	IC junction-to-ambient thermal resistance
11.	Iin Avg	5.177 A	IC	Average input current
12.	L Ipp	1.449 A	Inductor	Peak-to-peak inductor ripple current
13.	L Pd	465.57 mW	Inductor	Inductor power dissipation
14.	L1 Irms	5.173 A	Inductor	Inductor ripple current
15.	M Vds Act	322.768 mV	Mosfet	M Vds
16.	M1 Irms	5.527 A	Mosfet	M1 MOSFET Irms
17.	M1 Pd	2.474 W	Mosfet	M1 MOSFET total power dissipation
18.	M1 PdCond	1.784 W	Mosfet	M1 MOSFET conduction losses
19.	M1 PdSw	689.71 mW	Mosfet	M1 MOSFET switching losses
20.	M1 Rdson	58.396 mOhm	Mosfet	Drain-Source On-resistance
21.	M1 ThetaJA	43.0 degC/W	Mosfet	MOSFET junction-to-ambient thermal resistance
22.	M1 TjOP	136.37 degC	Mosfet	M1 MOSFET junction temperature
23.	Cin Pd	17.505 mW	Power	Input capacitor power dissipation
24.	Cout Pd	199.31 mW	Power	Output capacitor power dissipation
25.	Diode Pd	450.0 mW	Power	Diode power dissipation
26.	IC Pd	27.685 mW	Power	IC power dissipation
27.	L Pd	465.57 mW	Power	Inductor power dissipation
28.	M1 Pd	2.474 W	Power	M1 MOSFET total power dissipation
29.	M1 PdCond	1.784 W	Power	M1 MOSFET conduction losses
30.	M1 PdSw	689.71 mW	Power	M1 MOSFET switching losses
31.	Rfb Pd	61.479 mW	Power	Rfb Power Dissipation
32.	Rsense Pd	143.94 mW	Power	LED Current Rsns Power Dissipation

#	Name	Value	Category	Description
33.	Total Pd	1.366 W	Power	Total Power Dissipation
34.	Rfb Pd	61.479 mW	Resistor	Rfb Power Dissipation
35.	Rsense Pd	143.94 mW	Resistor	LED Current Rsns Power Dissipation
36.	BOM Count	17	System Information	Total Design BOM count
37.	Cross Freq	2.524 kHz	System Information	Bode plot crossover frequency
38.	Duty Cycle	90.302 %	System Information	Duty cycle
39.	Efficiency	94.616 %	System Information	Steady state efficiency
40.	FootPrint	1.129 k mm ²	System Information	Total Foot Print Area of BOM components
41.	Frequency	541.734 kHz	System Information	Switching frequency
42.	Gain Marg	-20.604 dB	System Information	Bode Plot Gain Margin
43.	Iout	500.0 mA	System Information	Iout operating point
44.	Low Freq Gain	58.144 dB	System Information	Gain at 1Hz
45.	Mode	CCM	System Information	Conduction Mode
46.	Phase Marg	65.598 deg	System Information	Bode Plot Phase Margin
47.	Pout	24.0 W	System Information	Total output power
48.	SW Ipk	5.88 A	System Information	Peak switch current
49.	Total BOM	\$6.04	System Information	Total BOM Cost
50.	Vin	4.9 V	System Information	Vin operating point
51.	Vout	48.0 V	System Information	Operational Output Voltage
52.	Vout Actual	48.381 V	System Information	Vout Actual calculated based on selected voltage divider resistors
53.	Vout Tolerance	3.934 %	System Information	Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable
54.	Vout p-p	499.835 mV	System Information	Peak-to-peak output ripple voltage

Design Inputs

Name	Value	Description
Iout	500.0 m	Maximum Output Current
VinMax	5.1	Maximum input voltage
VinMin	4.9	Minimum input voltage
Vout	48.0	Output Voltage
base_pn	LM3478	Base Product Number
source	DC	Input Source Type
Ta	30.0	Ambient temperature

WEBENCH® Assembly

Component Testing

Some published data on components in datasheets such as Capacitor ESR and Inductor DC resistance is based on conservative values that will guarantee that the components always exceed the specification. For design purposes it is usually better to work with typical values. Since this data is not always available it is a good practice to measure the Capacitance and ESR values of C_{in} and C_{out} , and the inductance and DC resistance of L1 before assembly of the board. Any large discrepancies in values should be electrically simulated in WEBENCH to check for instabilities and thermally simulated in WebTHERM to make sure critical temperatures are not exceeded.

Soldering Component to Board

If board assembly is done in house it is best to tack down one terminal of a component on the board then solder the other terminal. For surface mount parts with large tabs, such as the DPAK, the tab on the back of the package should be pre-tinned with solder, then tacked into place by one of the pins. To solder the tab down to the board place the iron down on the board while resting against the tab, heating both surfaces simultaneously. Apply light pressure to the top of the plastic case until the solder flows around the part and the part is flush with the PCB. If the solder is not flowing around the board you may need a higher wattage iron (generally 25W to 30W is enough).

Initial Startup of Circuit

It is best to initially power up the board by setting the input supply voltage to the lowest operating input voltage 4.9V and set the input supply's current limit to zero. With the input supply off connect up the input supply to V_{in} and GND. Connect a digital volt meter and a load if needed to set the minimum load of the design from V_{out} and GND. Turn on the input supply and slowly turn up the current limit on the input supply. If the voltage starts to rise on the input supply continue increasing the input supply current limit while watching the output voltage. If the current increases on the input supply, but the voltage remains near zero, then there may be a short or a component misplaced on the board. Power down the board and visually inspect for solder bridges and recheck the diode and capacitor polarities. Once the power supply circuit is operational then more extensive testing may include full load testing, transient load and line tests to compare with simulation results.

Load Testing

The setup is the same as the initial startup, except that an additional digital voltmeter is connected between V_{in} and GND, a load is connected between V_{out} and GND and a current meter is connected in series between V_{out} and the load. The load must be able to handle at least rated output power + 50% (7.5 watts for this design). Ideally the load is supplied in the form of a variable load test unit. It can also be done in the form of suitably large power resistors. When using an oscilloscope to measure waveforms on the prototype board, the ground leads of the oscilloscope probes should be as short as possible and the area of the loop formed by the ground lead should be kept to a minimum. This will help reduce ground lead inductance and eliminate EMI noise that is not actually present in the circuit.



Design Assistance

1. Master key : 9B36839A35C1A7FC142149FAD4E67CD3[v1]
2. **LM3478** Product Folder : <http://www.ti.com/product/LM3478> : contains the data sheet and other resources.

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