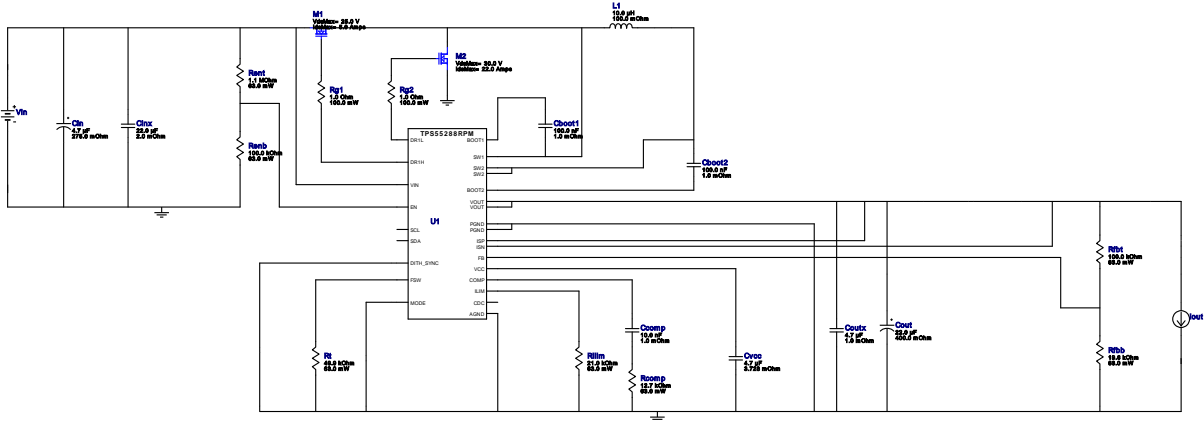


VinMin = 14.5V
 VinMax = 14.5V
 Vout = 6.88V
 Iout = 0.5A

Device = TPS55288QRPMRQ1
 Topology = Buck_Boost
 Created = 2022-07-03 23:27:57.077
 BOM Cost = \$5.76
 BOM Count = 21
 Total Pd = 0.2W

WEBENCH® Design Report

Design : 907 TPS55288QRPMRQ1
 TPS55288QRPMRQ1 14.5V-14.5V to 6.88V @ 4.7A



Design Alerts

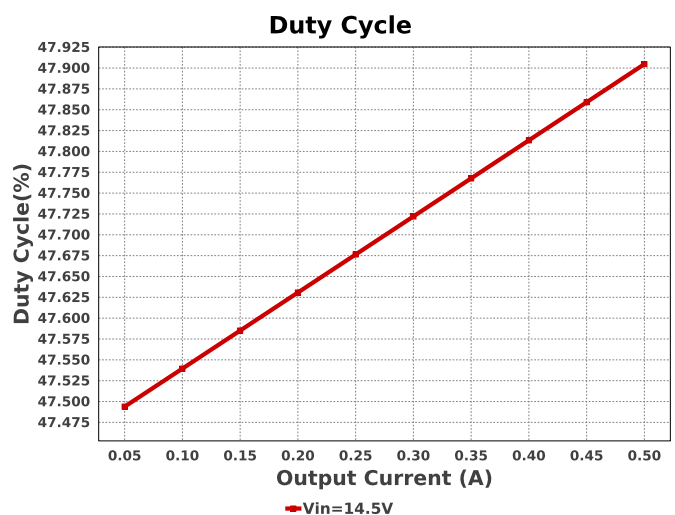
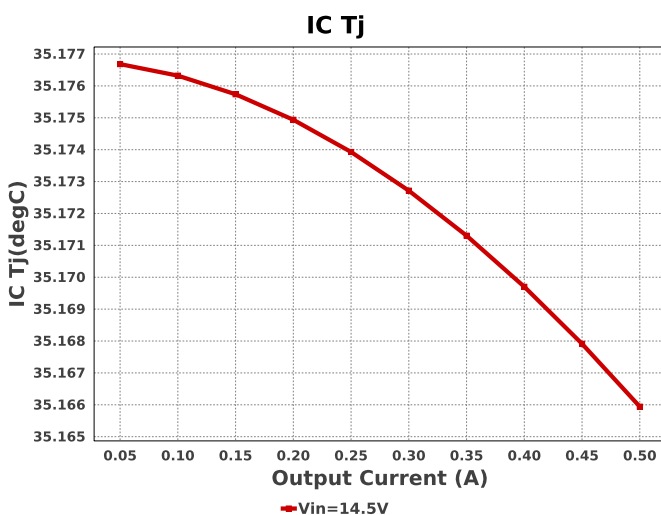
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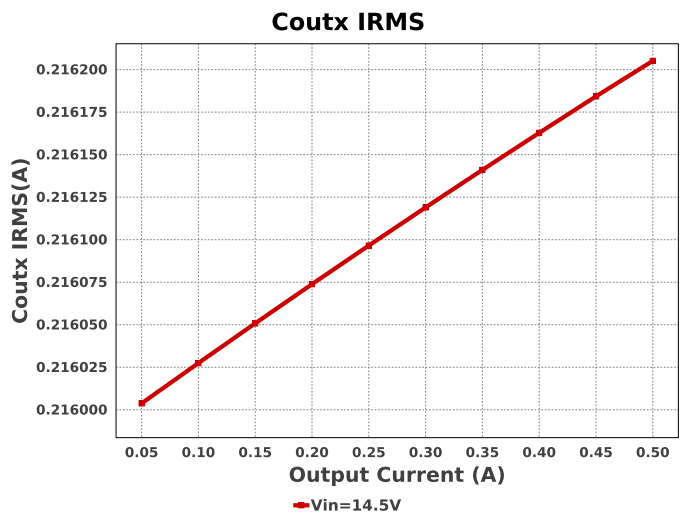
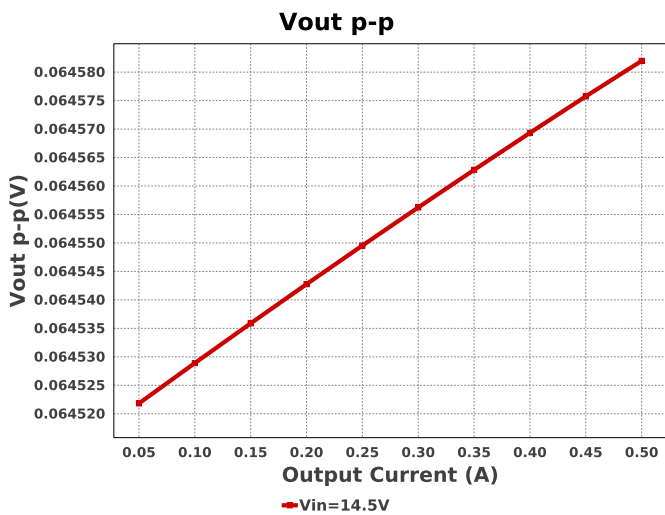
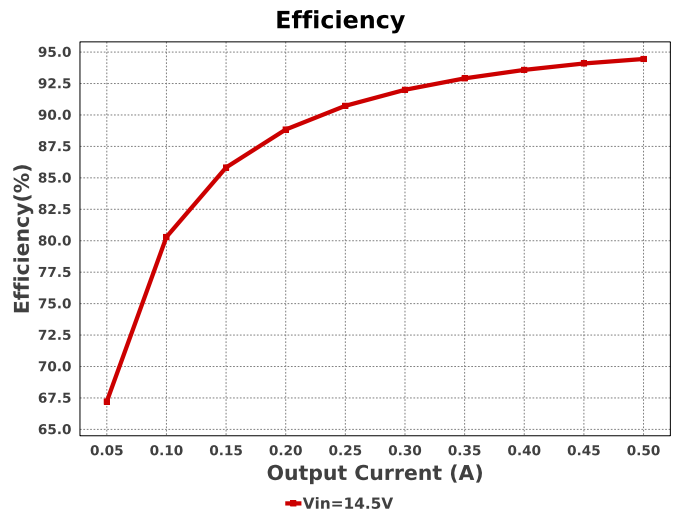
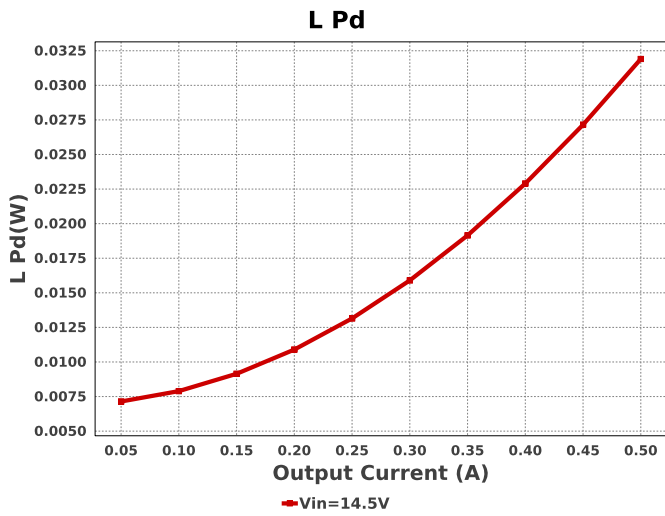
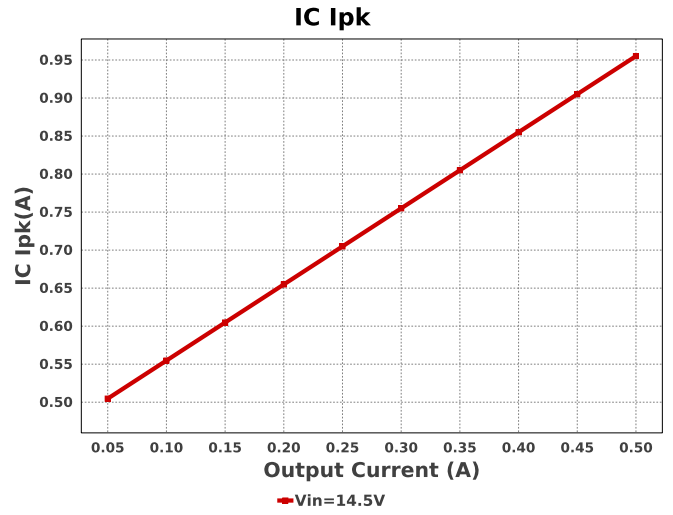
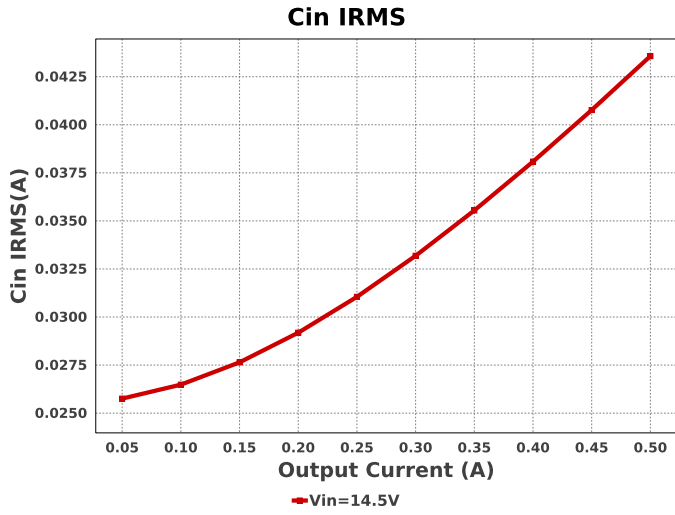
The TPS55288-Q1 is qualified for Automotive applications. All passives and other components selected in this design may not be qualified for Automotive applications. The user is required to verify that all components in the design meet the qualification and safety requirements for their specific application.

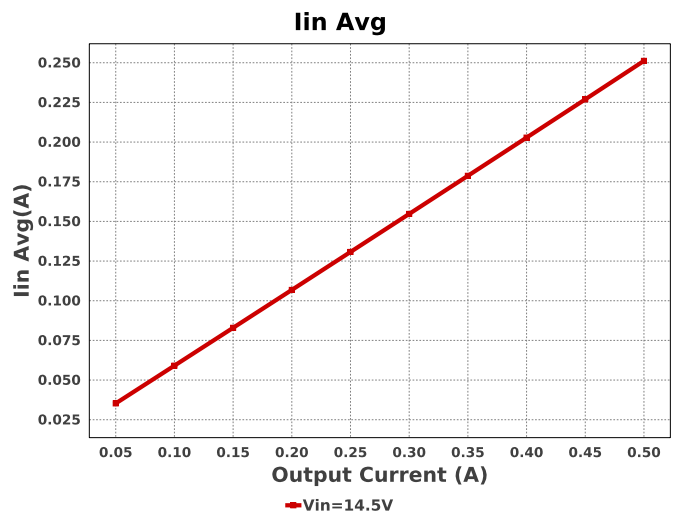
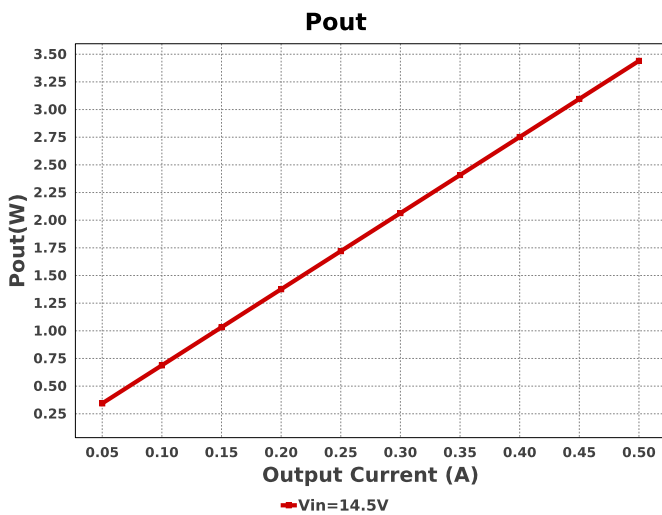
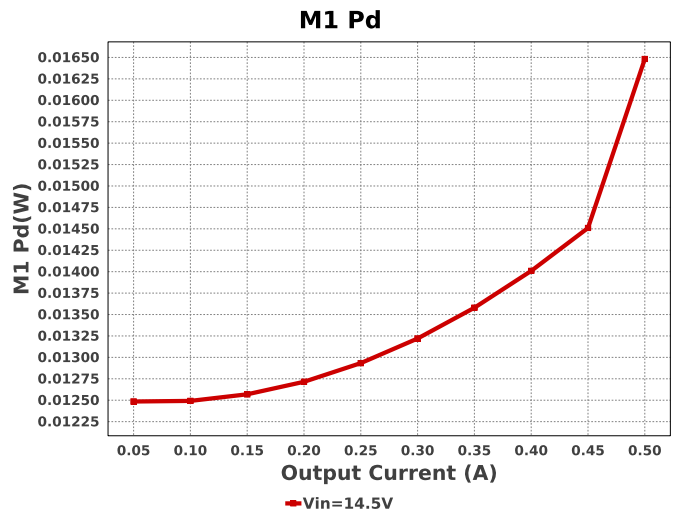
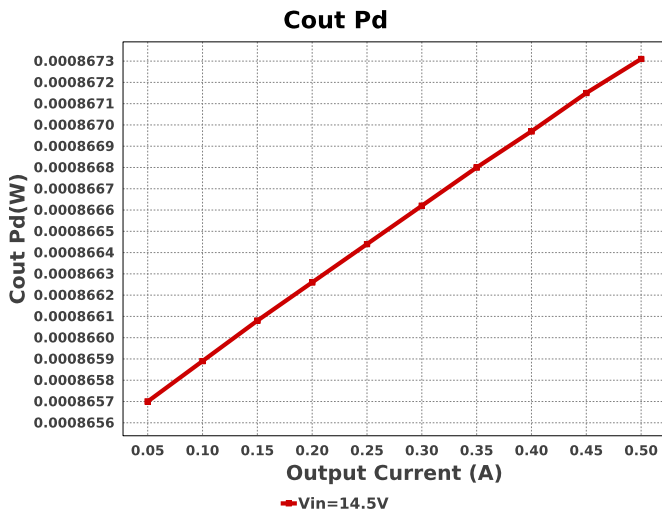
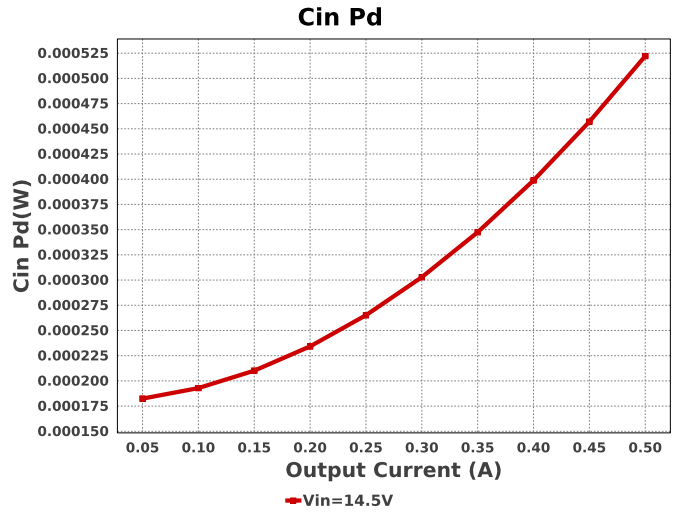
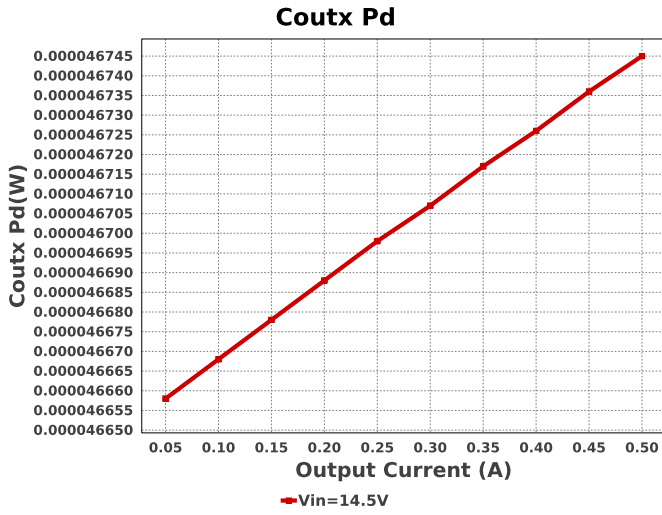
Electrical BOM

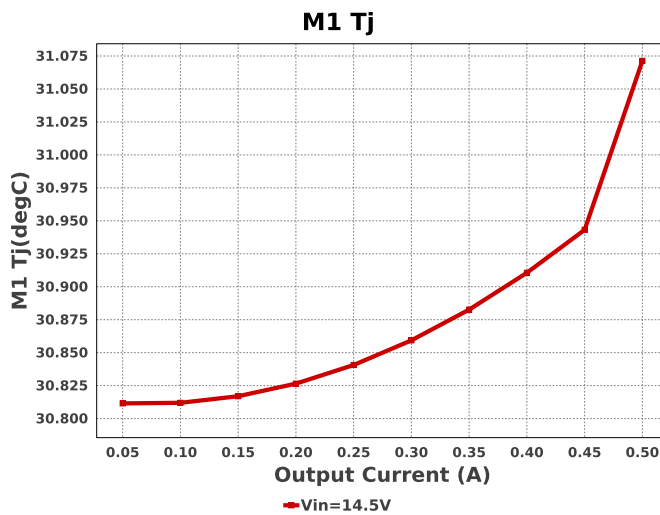
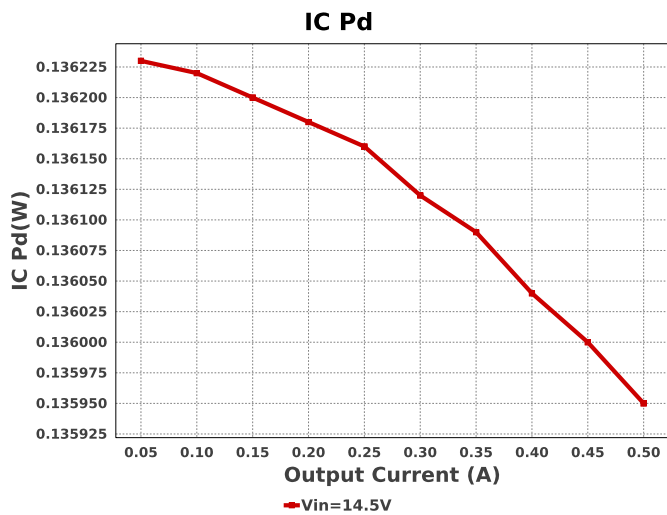
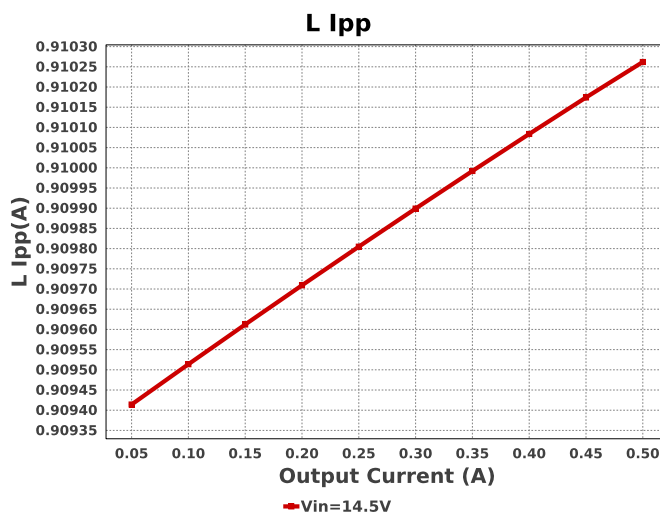
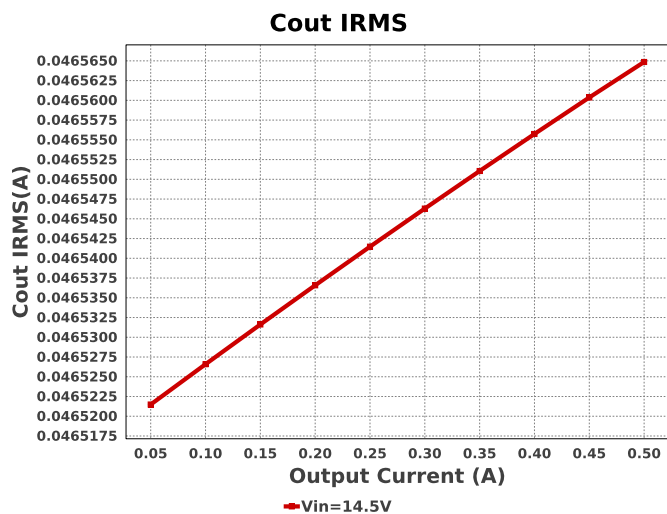
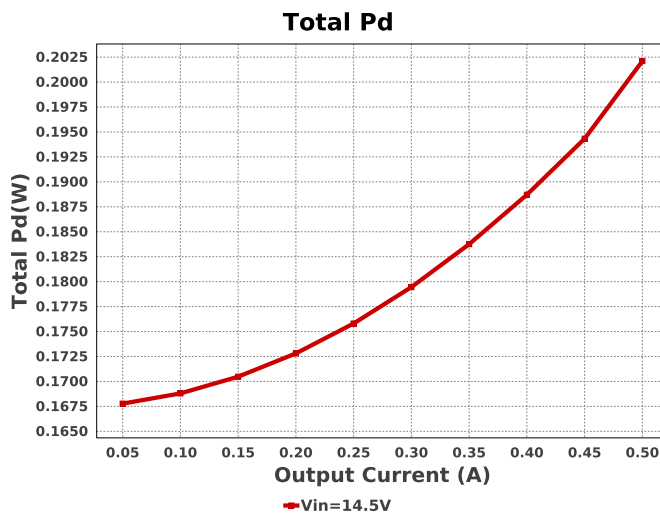
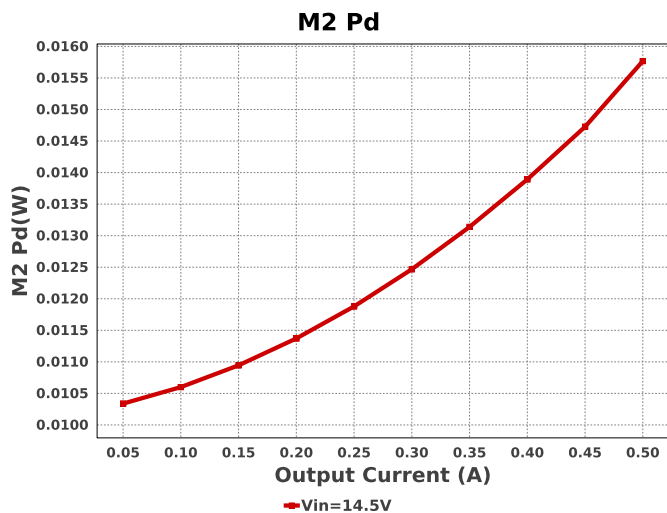
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Cboot1	Taiyo Yuden	EMK107B7104KA-T Series= X7R	Cap= 100.0 nF ESR= 1.0 mOhm VDC= 16.0 V IRMS= 0.0 A	1	\$0.01	0603 5 mm ²
Cboot2	Taiyo Yuden	EMK107B7104KA-T Series= X7R	Cap= 100.0 nF ESR= 1.0 mOhm VDC= 16.0 V IRMS= 0.0 A	1	\$0.01	0603 5 mm ²
Ccomp	MuRata	GRM155R71C103KA01D Series= X7R	Cap= 10.0 nF ESR= 1.0 mOhm VDC= 16.0 V IRMS= 0.0 A	1	\$0.01	0402 3 mm ²
Cin	Kemet	T495D475K050ATE275 Series= T495	Cap= 4.7 uF ESR= 275.0 mOhm VDC= 50.0 V IRMS= 665.0 mA	1	\$0.78	7343-31 59 mm ²
Cinx	MuRata	GRM32ER61E226KE15L Series= X5R	Cap= 22.0 uF ESR= 2.0 mOhm VDC= 25.0 V IRMS= 3.67 A	1	\$0.65	1210 15 mm ²
Cout	AVX	TPSB226K020R0400 Series= TPS	Cap= 22.0 uF ESR= 400.0 mOhm VDC= 20.0 V IRMS= 415.0 mA	1	\$0.28	3528-21 17 mm ²
Coutx	MuRata	GRM32ER71H475KA88L Series= X7R	Cap= 4.7 uF ESR= 1.0 mOhm VDC= 50.0 V IRMS= 6.0 A	1	\$0.42	1210 15 mm ²
Cvcc	TDK	C1608X6S1C475K080AC Series= X6S	Cap= 4.7 uF ESR= 3.728 mOhm VDC= 16.0 V IRMS= 2.69359 A	1	\$0.08	0603 5 mm ²

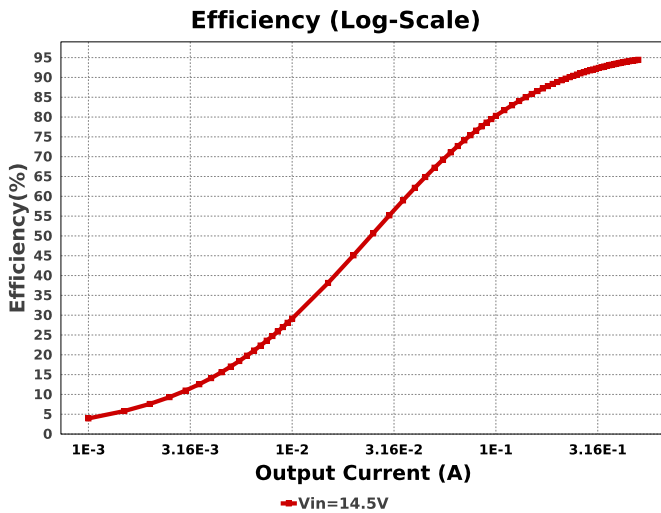
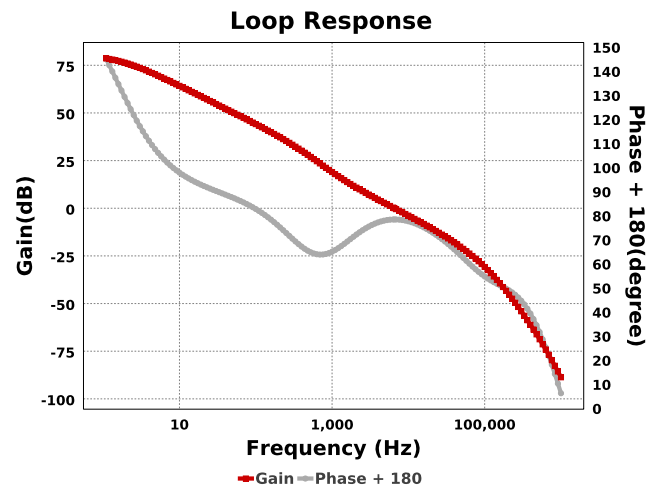
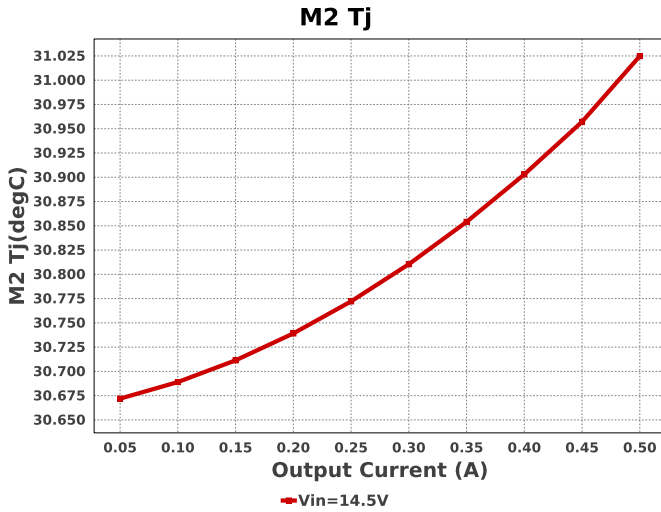
Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
L1	Bourns	SDR0604-100ML	L= 10.0 µH 100.0 mOhm	1	\$0.22	 SDR0604 61 mm²
M1	Texas Instruments	CSD16301Q2	VdsMax= 25.0 V IdsMax= 5.0 Amps	1	\$0.13	DQK0006C 9 mm²
M2	Texas Instruments	CSD17571Q2	VdsMax= 30.0 V IdsMax= 22.0 Amps	1	\$0.11	DQK0006C 9 mm²
Rcomp	Vishay-Dale	CRCW040212K7FKED Series= CRCW..e3	Res= 12.7 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm²
Renb	Vishay-Dale	CRCW0402100KFKED Series= CRCW..e3	Res= 100.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm²
Rent	Vishay-Dale	CRCW04021M10FKED Series= CRCW..e3	Res= 1.1 MOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm²
Rfbb	Vishay-Dale	CRCW040219K6FKED Series= CRCW..e3	Res= 19.6 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm²
Rfbt	Vishay-Dale	CRCW0402100KFKED Series= CRCW..e3	Res= 100.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm²
Rg1	Vishay-Dale	CRCW06031R00FKEA Series= CRCW..e3	Res= 1.0 Ohm Power= 100.0 mW Tolerance= 1.0%	1	\$0.01	 0603 5 mm²
Rg2	Vishay-Dale	CRCW06031R00FKEA Series= CRCW..e3	Res= 1.0 Ohm Power= 100.0 mW Tolerance= 1.0%	1	\$0.01	 0603 5 mm²
Rilim	Vishay-Dale	CRCW040221K0FKED Series= CRCW..e3	Res= 21.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm²
Rt	Vishay-Dale	CRCW040249K9FKED Series= CRCW..e3	Res= 49.9 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	 0402 3 mm²
U1	Texas Instruments	TPS55288QRPMRQ1	Switcher	1	\$2.97	RPM0026A-MFG 22 mm²











Operating Values

#	Name	Value	Category	Description
1.	Cin IRMS	43.573 mA	Capacitor	Input capacitor RMS ripple current
2.	Cin Pd	522.11 μW	Capacitor	Input capacitor power dissipation
3.	Cout IRMS	46.565 mA	Capacitor	Output capacitor RMS ripple current
4.	Cout Pd	867.31 μW	Capacitor	Output capacitor power dissipation
5.	Coutx IRMS	216.205 mA	Capacitor	Output capacitor_x RMS ripple current
6.	Coutx Pd	46.745 μW	Capacitor	Output capacitor_x power loss
7.	IC Ipk	955.131 mA	IC	Peak switch current in IC
8.	IC Pd	135.95 mW	IC	IC power dissipation
9.	IC Tj	35.166 degC	IC	IC junction temperature
10.	IC Tolerance	12.0 mV	IC	IC Feedback Tolerance
11.	ICThetaJA	38.0 degC/W	IC	IC junction-to-ambient thermal resistance
12.	Iin Avg	251.18 mA	IC	Average input current
13.	L Ipp	910.26 mA	Inductor	Peak-to-peak inductor ripple current
14.	L Pd	31.905 mW	Inductor	Inductor power dissipation
15.	M1 Pd	16.482 mW	Mosfet	M1 MOSFET total power dissipation
16.	M1 Tj	31.071 degC	Mosfet	M1 MOSFET junction temperature
17.	M2 Pd	15.767 mW	Mosfet	M2 MOSFET total power dissipation
18.	M2 Tj	31.025 degC	Mosfet	M2 MOSFET junction temperature
19.	Cin Pd	522.11 μW	Power	Input capacitor power dissipation
20.	Cout Pd	867.31 μW	Power	Output capacitor power dissipation
21.	Coutx Pd	46.745 μW	Power	Output capacitor_x power loss
22.	IC Pd	135.95 mW	Power	IC power dissipation
23.	L Pd	31.905 mW	Power	Inductor power dissipation
24.	M1 Pd	16.482 mW	Power	M1 MOSFET total power dissipation
25.	M2 Pd	15.767 mW	Power	M2 MOSFET total power dissipation
26.	Total Pd	202.101 mW	Power	Total Power Dissipation
27.	BOM Count	21	System	Total Design BOM count
28.	Cross Freq	6.574 kHz	System	Bode plot crossover frequency
29.	Duty Cycle	47.905 %	System	Duty cycle

#	Name	Value	Category	Description
30.	Efficiency	94.451 %	System Information	Steady state efficiency
31.	FootPrint	254.0 mm ²	System Information	Total Foot Print Area of BOM components
32.	Frequency	397.614 kHz	System Information	Switching frequency
33.	Gain Marg	-93.306 dB	System Information	Bode Plot Gain Margin
34.	Iout	500.0 mA	System Information	Iout operating point
35.	Low Freq Gain	78.596 dB	System Information	Gain at 1Hz
36.	Mode	CCM	System Information	Conduction Mode
37.	Phase Marg	78.792 deg	System Information	Bode Plot Phase Margin
38.	Pout	3.44 W	System Information	Total output power
39.	Total BOM	\$5.76	System Information	Total BOM Cost
40.	Vin	14.5 V	System Information	Vin operating point
41.	Vout	6.88 V	System Information	Operational Output Voltage
42.	Vout Actual	1.721 V	System Information	Vout Actual calculated based on selected voltage divider resistors
43.	Vout Tolerance	6.016 %	System Information	Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable
44.	Vout p-p	64.582 mV	System Information	Peak-to-peak output ripple voltage
45.	Vref	130.0 h;	System Information	Register VREF

Design Inputs

Name	Value	Description
Iout	500.0 m	Maximum Output Current
VinMax	14.5	Maximum input voltage
VinMin	14.5	Minimum input voltage
Vout	6.88	Output Voltage
base_pn	TPS55288-Q1	Base Product Number
source	DC	Input Source Type
Ta	30.0	Ambient temperature

WEBENCH® Assembly

Component Testing

Some published data on components in datasheets such as Capacitor ESR and Inductor DC resistance is based on conservative values that will guarantee that the components always exceed the specification. For design purposes it is usually better to work with typical values. Since this data is not always available it is a good practice to measure the Capacitance and ESR values of C_{in} and C_{out} , and the inductance and DC resistance of L1 before assembly of the board. Any large discrepancies in values should be electrically simulated in WEBENCH to check for instabilities and thermally simulated in WebTHERM to make sure critical temperatures are not exceeded.

Soldering Component to Board

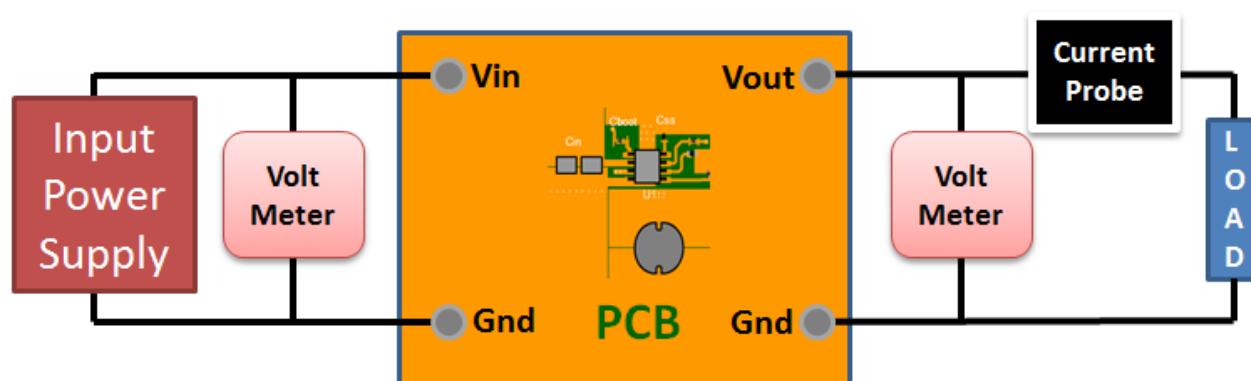
If board assembly is done in house it is best to tack down one terminal of a component on the board then solder the other terminal. For surface mount parts with large tabs, such as the DPAK, the tab on the back of the package should be pre-tinned with solder, then tacked into place by one of the pins. To solder the tab down to the board place the iron down on the board while resting against the tab, heating both surfaces simultaneously. Apply light pressure to the top of the plastic case until the solder flows around the part and the part is flush with the PCB. If the solder is not flowing around the board you may need a higher wattage iron (generally 25W to 30W is enough).

Initial Startup of Circuit

It is best to initially power up the board by setting the input supply voltage to the lowest operating input voltage 14.5V and set the input supply's current limit to zero. With the input supply off connect up the input supply to V_{in} and GND. Connect a digital volt meter and a load if needed to set the minimum load of the design from V_{out} and GND. Turn on the input supply and slowly turn up the current limit on the input supply. If the voltage starts to rise on the input supply continue increasing the input supply current limit while watching the output voltage. If the current increases on the input supply, but the voltage remains near zero, then there may be a short or a component misplaced on the board. Power down the board and visually inspect for solder bridges and recheck the diode and capacitor polarities. Once the power supply circuit is operational then more extensive testing may include full load testing, transient load and line tests to compare with simulation results.

Load Testing

The setup is the same as the initial startup, except that an additional digital voltmeter is connected between V_{in} and GND, a load is connected between V_{out} and GND and a current meter is connected in series between V_{out} and the load. The load must be able to handle at least rated output power + 50% (7.5 watts for this design). Ideally the load is supplied in the form of a variable load test unit. It can also be done in the form of suitably large power resistors. When using an oscilloscope to measure waveforms on the prototype board, the ground leads of the oscilloscope probes should be as short as possible and the area of the loop formed by the ground lead should be kept to a minimum. This will help reduce ground lead inductance and eliminate EMI noise that is not actually present in the circuit.

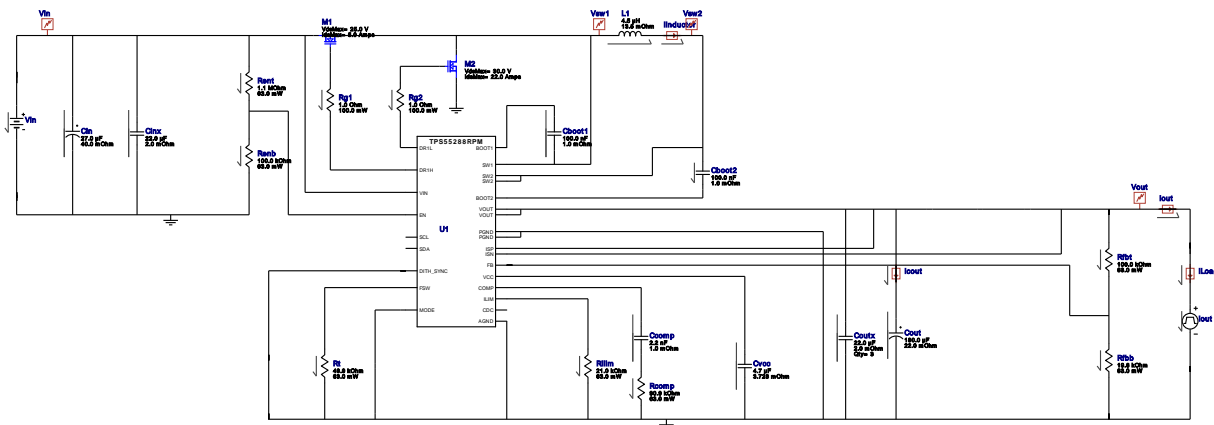


WEBENCH® Electrical Simulation Report

Design Id = 907

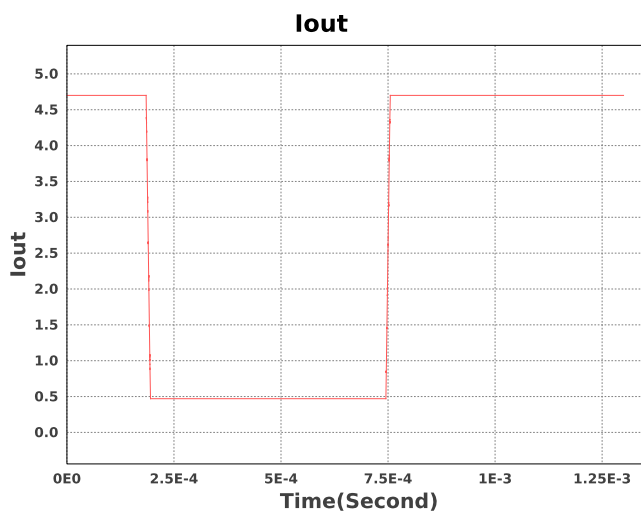
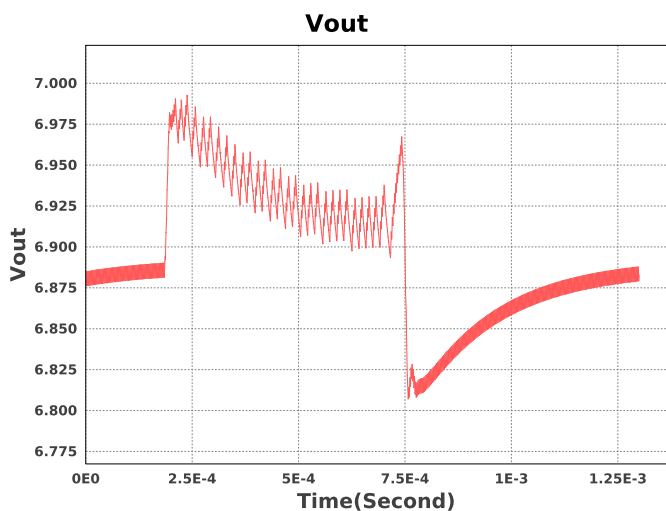
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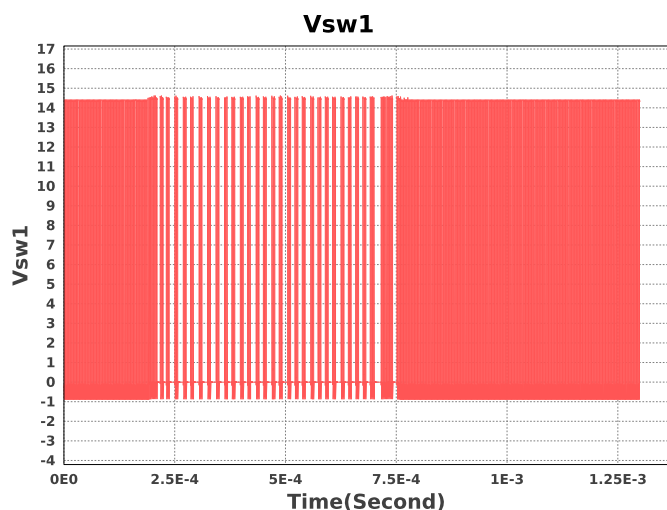
Simulation Type = Load Transient



Simulation Parameters

#	Name	Parameter Name	Description	Values
1.	L1	IC	Initial Current	4.7 I
2.	Iout	signal_type	Signal Type	PULSE
		I1	Initial Load Current	4.7 A
		I2	Minimum Load Current	0.47000000000000003 A
		Td	Initial Time Delay	185u s
		Tf	Fall Time	10u s
		Tr	Rise Time	10u s
		Pw	Pulse Width	5.5E-4 s





Design Assistance

1. Master key : AE01DD1456011B0A[v1]
2. **TPS55288-Q1** Product Folder : <http://www.ti.com/product/TPS55288%2DQ1> : contains the data sheet and other resources.

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