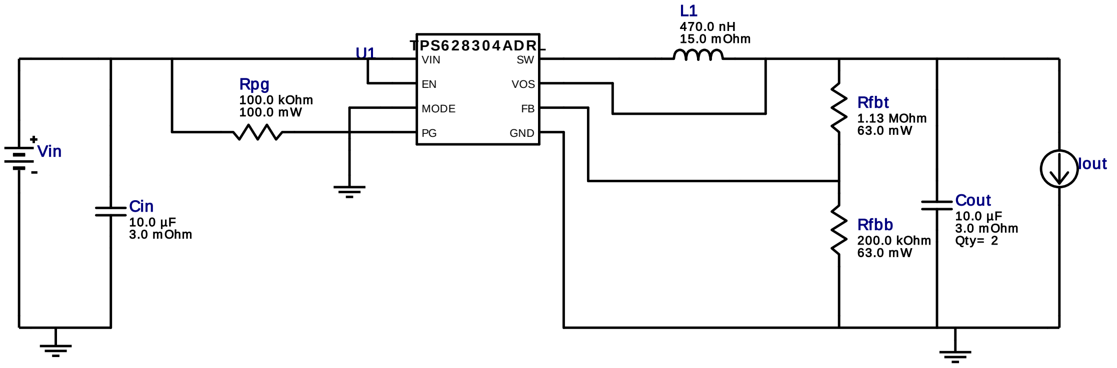
VinMin = 5.0V
VinMax = 5.0V
Vout = 3.3V
Iout = 4.0A

Device = TPS628304ADRLR
Topology = Buck
Created = 2024-01-31 02:51:54.003
BOM Cost = $1.41
BOM Count = 8
Total Pd = 1.04W

WEBENCH ® Design Report

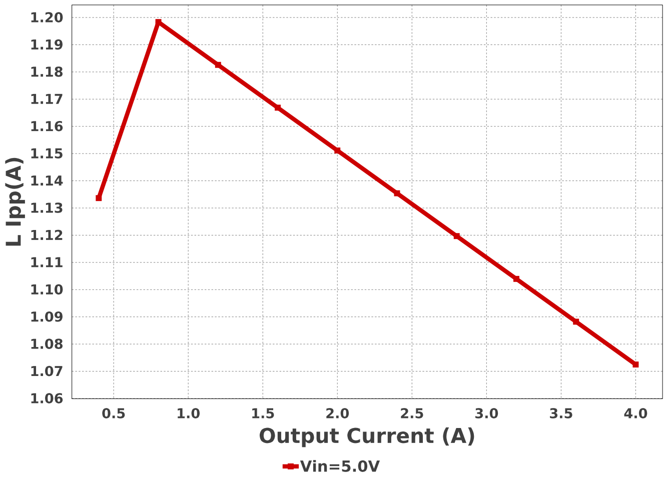
Design : 97 TPS628304ADRLR
TPS628304ADRLR 3.1V-5.5V to 1.80V @ 4A



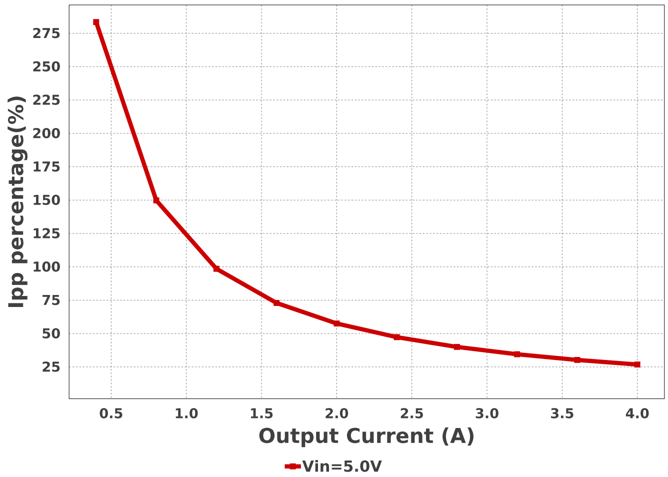
Electrical BOM

Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
Cin	Kemet	C0805C106K8PACTU Series= X5R	Cap= 10.0 uF ESR= 3.0 mOhm VDC= 10.0 V IRMS= 11.43 A	1	$0.03	0805 7 mm^2
Cout	Kemet	C0805C106K8PACTU Series= X5R	Cap= 10.0 uF ESR= 3.0 mOhm VDC= 10.0 V IRMS= 11.43 A	2	$0.03	0805 7 mm^2
L1	Vishay-Dale	IHLP1212AEERR47M11	L= 470.0 nH 15.0 mOhm	1	$0.63	IHLP-1212AE 19 mm^2
Rfbb	Vishay-Dale	CRCW0402200KFKED Series= CRCW..e3	Res= 200.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	$0.01	0402 3 mm^2
Rfbt	Vishay-Dale	CRCW04021M13FKED Series= CRCW..e3	Res= 1.13 MOhm Power= 63.0 mW Tolerance= 1.0%	1	$0.01	0402 3 mm^2
Rpg	Vishay-Dale	CRCW0603100KFKEA Series= CRCW..e3	Res= 100.0 kOhm Power= 100.0 mW Tolerance= 1.0%	1	$0.01	0603 5 mm^2
U1	Texas Instruments	TPS628304ADRLR	Switcher	1	$0.66	DRL0008A-MFG 9 mm^2

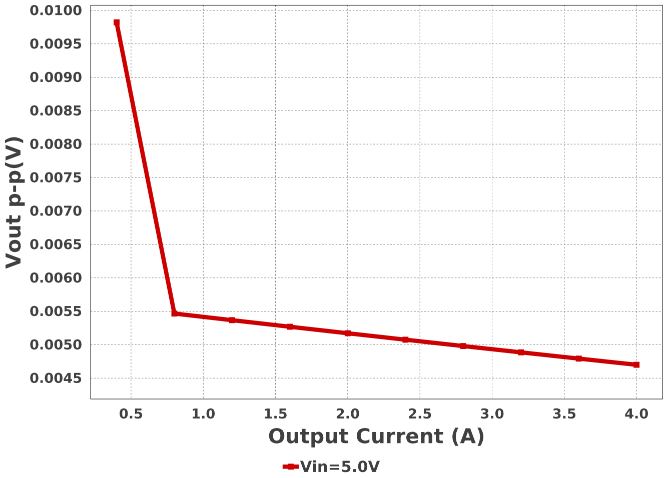
L Ipp



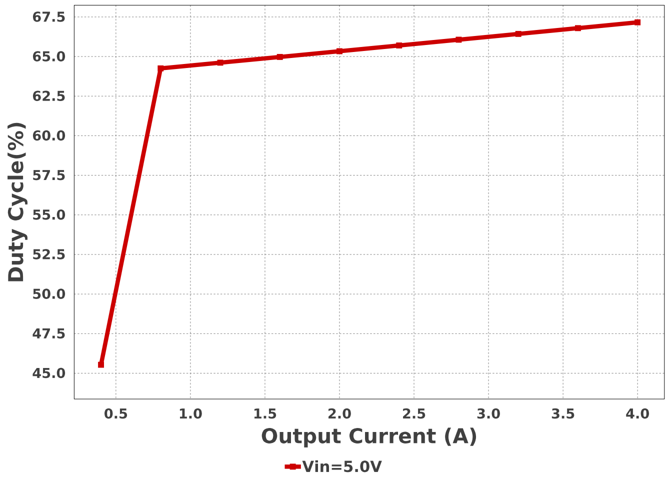
Ipp percentage



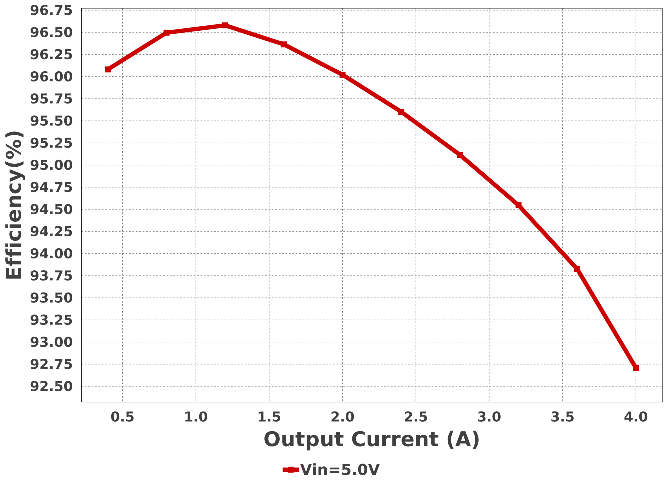
Vout p-p



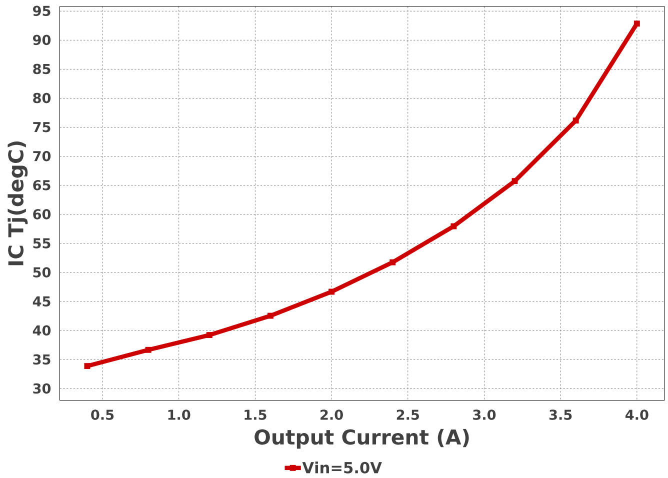
Duty Cycle

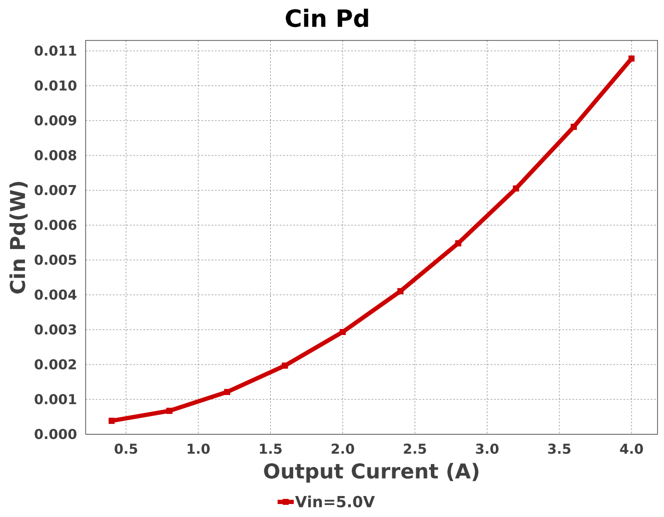


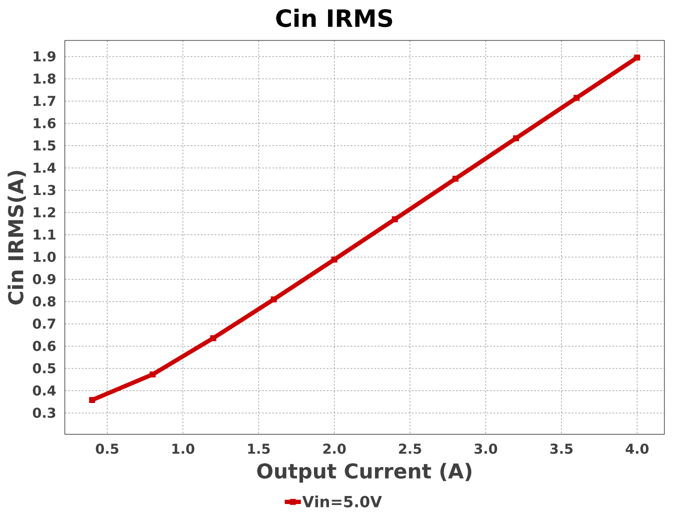
Efficiency

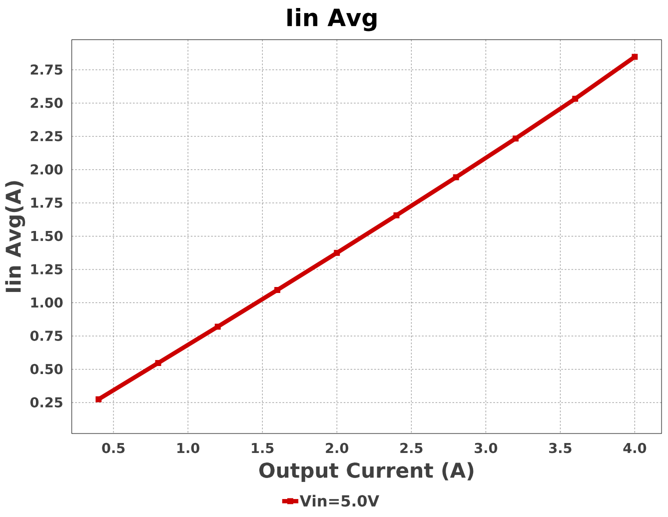


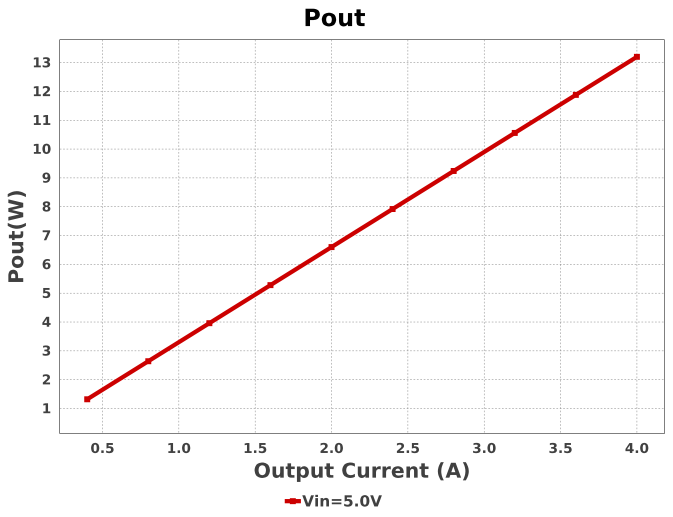
IC Tj

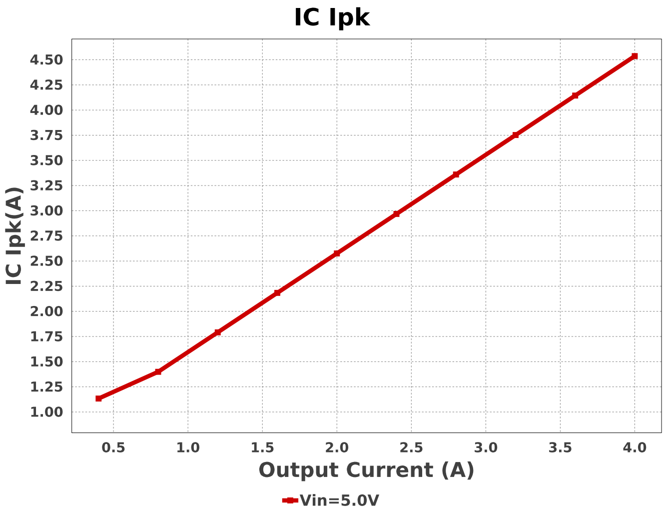


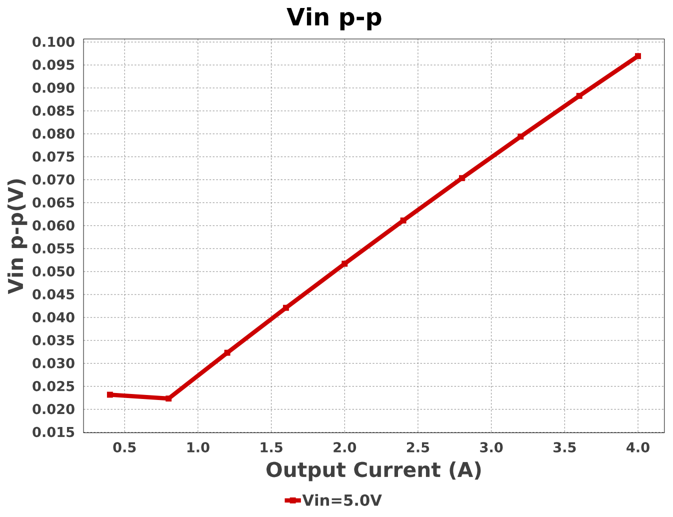

Cin Pd
Cin Pd(W)
Output Current (A)
Vin=5.0V

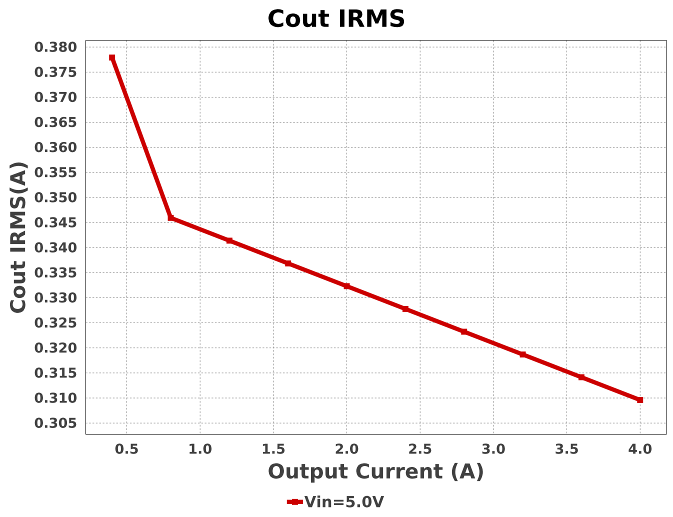

Cin IRMS
Cin IRMS(A)
Output Current (A)
Vin=5.0V

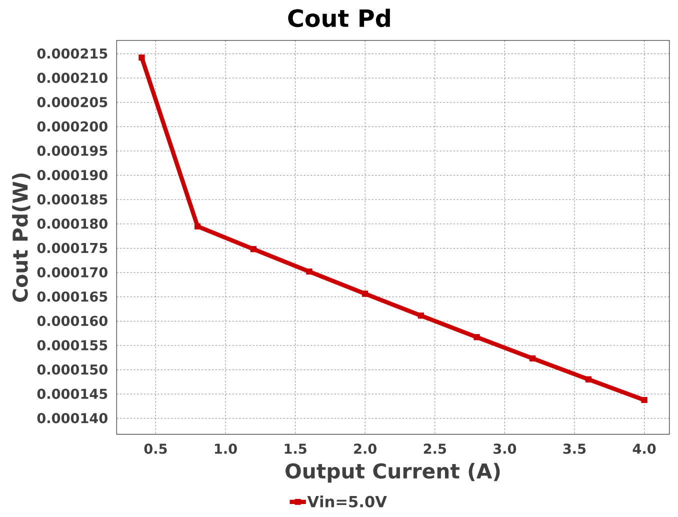

Iin Avg
Iin Avg(A)
Output Current (A)
Vin=5.0V

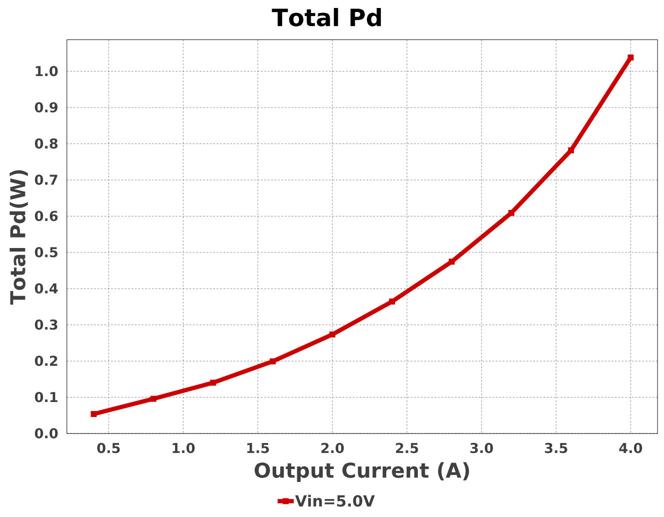

Pout
Pout(W)
Output Current (A)
Vin=5.0V

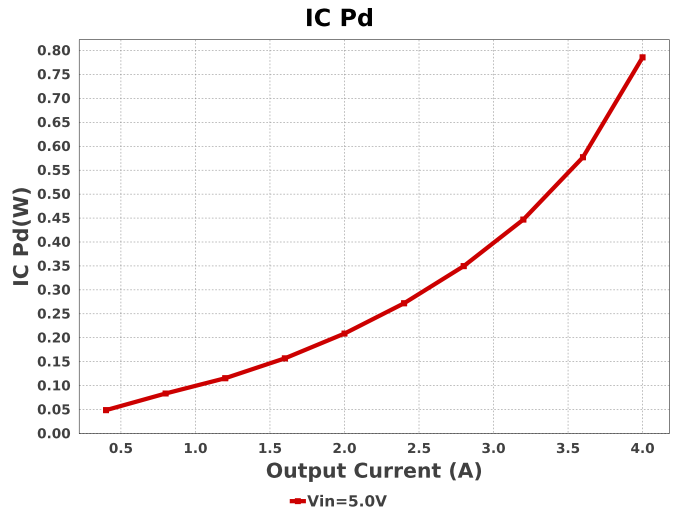

IC Ipk
IC Ipk(A)
Output Current (A)
Vin=5.0V

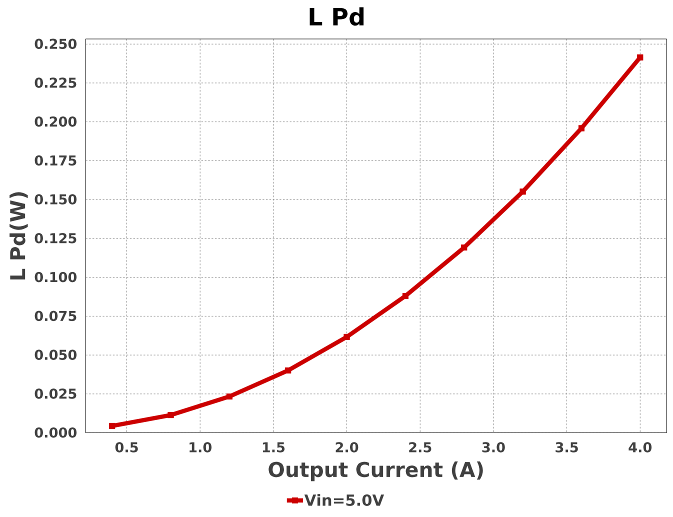

Vin p-p
Vin p-p(V)
Output Current (A)
Vin=5.0V

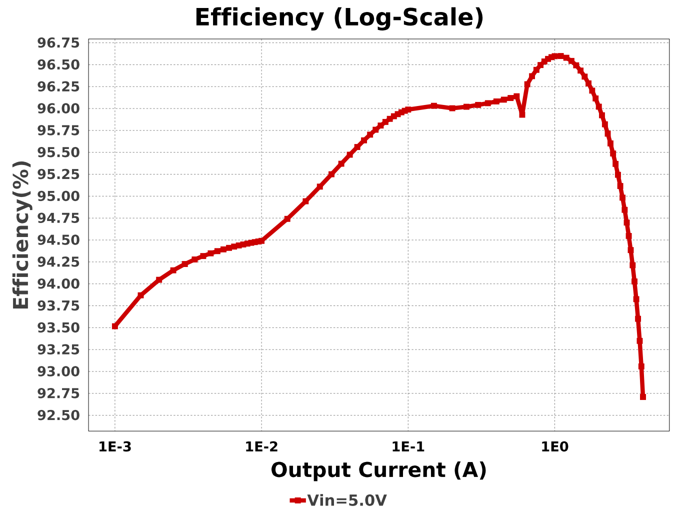












Operating Values

#	Name	Value	Category	Description
1.	Cin IRMS	1.896 A	Capacitor	Input capacitor RMS ripple current
2.	Cin Pd	10.78 mW	Capacitor	Input capacitor power dissipation
3.	Cout IRMS	309.6 mA	Capacitor	Output capacitor RMS ripple current
4.	Cout Pd	143.78 µW	Capacitor	Output capacitor power dissipation
5.	IC Ipk	4.536 A	IC	Peak switch current in IC
6.	IC Pd	785.65 mW	IC	IC power dissipation
7.	IC Tj	92.852 degC	IC	IC junction temperature
8.	ICThetaJA Effective	80.0 degC/W	IC	Effective IC Junction-to-Ambient Thermal Resistance
9.	Iin Avg	2.848 A	IC	Average input current
10.	Ipp percentage	26.812 %	Inductor	Inductor ripple current percentage (with respect to average inductor current)

#	Name	Value	Category	Description
11.	L Ipp	1.072 A	Inductor	Peak-to-peak inductor ripple current
12.	L Pd	241.44 mW	Inductor	Inductor power dissipation
13.	Cin Pd	10.78 mW	Power	Input capacitor power dissipation
14.	Cout Pd	143.78 µW	Power	Output capacitor power dissipation
15.	IC Pd	785.65 mW	Power	IC power dissipation
16.	L Pd	241.44 mW	Power	Inductor power dissipation
17.	Total Pd	1.038 W	Power	Total Power Dissipation
18.	BOM Count	8	System Information	Total Design BOM count
19.	Duty Cycle	67.16 %	System Information	Duty cycle
20.	Efficiency	92.709 %	System Information	Steady state efficiency
21.	FootPrint	59.0 mm^2	System Information	Total Foot Print Area of BOM components
22.	Frequency	1.975 MHz	System Information	Switching frequency
23.	Iout	4.0 A	System Information	Iout operating point
24.	Mode	CCM	System Information	Conduction Mode
25.	Pout	13.2 W	System Information	Total output power
26.	Total BOM	$1.41	System Information	Total BOM Cost
27.	Vin	5.0 V	System Information	Vin operating point
28.	Vin p-p	96.939 mV	System Information	Peak-to-peak input voltage
29.	Vout	3.3 V	System Information	Operational Output Voltage
30.	Vout Actual	3.325 V	System Information	Vout Actual calculated based on selected voltage divider resistors
31.	Vout Tolerance	2.734 %	System Information	Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable
32.	Vout p-p	4.7 mV	System Information	Peak-to-peak output ripple voltage

Design Inputs

Name	Value	Description
Iout	4.0	Maximum Output Current
VinMax	5.0	Maximum input voltage
VinMin	5.0	Minimum input voltage
Vout	3.3	Output Voltage
base_pn	TPS628304A-DRL	Base Product Number
source	DC	Input Source Type
Ta	30.0	Ambient temperature

WEBENCH® Assembly

Component Testing

Some published data on components in datasheets such as Capacitor ESR and Inductor DC resistance is based on conservative values that will guarantee that the components always exceed the specification. For design purposes it is usually better to work with typical values. Since this data is not always available it is a good practice to measure the Capacitance and ESR values of Cin and Cout, and the inductance and DC resistance of L1 before assembly of the board. Any large discrepancies in values should be electrically simulated in WEBENCH to check for instabilities and thermally simulated in WebTHERM to make sure critical temperatures are not exceeded.

Soldering Component to Board

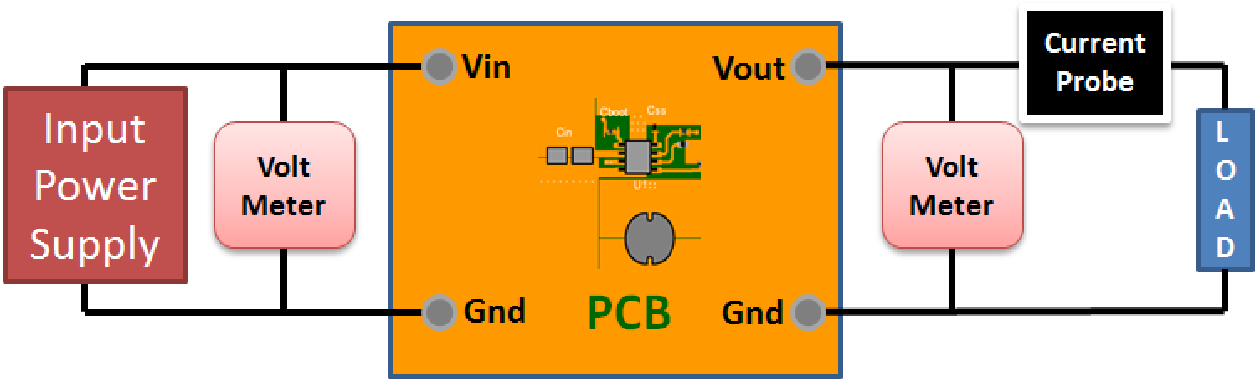
If board assembly is done in house it is best to tack down one terminal of a component on the board then solder the other terminal. For surface mount parts with large tabs, such as the DPAK, the tab on the back of the package should be pre-tinned with solder, then tacked into place by one of the pins. To solder the tab town to the board place the iron down on the board while resting against the tab, heating both surfaces simultaneously. Apply light pressure to the top of the plastic case until the solder flows around the part and the part is flush with the PCB. If the solder is not flowing around the board you may need a higher wattage iron (generally 25W to 30W is enough).

Initial Startup of Circuit

It is best to initially power up the board by setting the input supply voltage to the lowest operating input voltage 5.0V and set the input supply's current limit to zero. With the input supply off connect up the input supply to Vin and GND. Connect a digital volt meter and a load if needed to set the minimum Iout of the design from Vout and GND. Turn on the input supply and slowly turn up the current limit on the input supply. If the voltage starts to rise on the input supply continue increasing the input supply current limit while watching the output voltage. If the current increases on the input supply, but the voltage remains near zero, then there may be a short or a component misplaced on the board. Power down the board and visually inspect for solder bridges and recheck the diode and capacitor polarities. Once the power supply circuit is operational then more extensive testing may include full load testing, transient load and line tests to compare with simulation results.

Load Testing

The setup is the same as the initial startup, except that an additional digital voltmeter is connected between Vin and GND, a load is connected between Vout and GND and a current meter is connected in series between Vout and the load. The load must be able to handle at least rated output power + 50% (7.5 watts for this design). Ideally the load is supplied in the form of a variable load test unit. It can also be done in the form of suitably large power resistors. When using an oscilloscope to measure waveforms on the prototype board, the ground leads of the oscilloscope probes should be as short as possible and the area of the loop formed by the ground lead should be kept to a minimum. This will help reduce ground lead inductance and eliminate EMI noise that is not actually present in the circuit.



Design Assistance

1. Master key : B95B83C9555D0CC3[v1]

2. **TPS628304A-DRL** Product Folder : http://www.ti.com/product/TPS628304 : contains the data sheet and other resources.

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