

Case 1: Gate Driver is no problem

Condition: 50[ms/div]

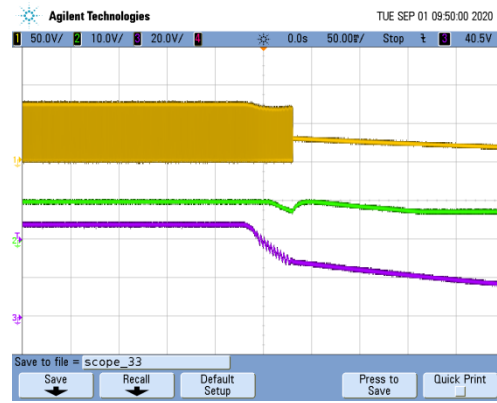
Upper Waveform: Main-Switch Q3 VDS

(UCC27200 Gate Driver #4_HS)

Middle Waveform: Gate Driver #1_VDD

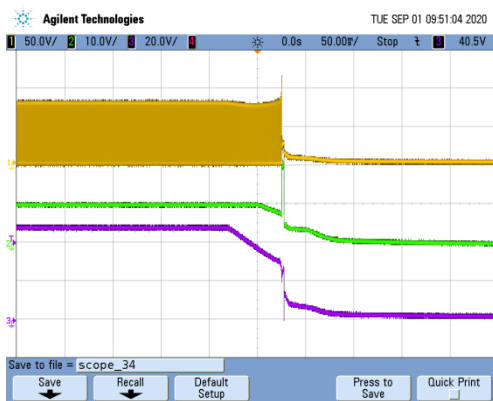
(= LM5025CMTD Controller #7_VCC)

Lower Waveform: Vin

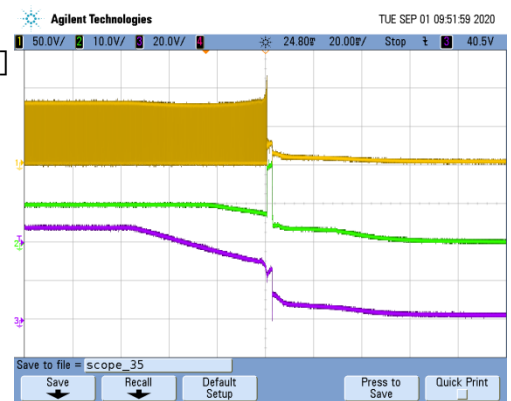


Case 2: Gate Driver has been broken

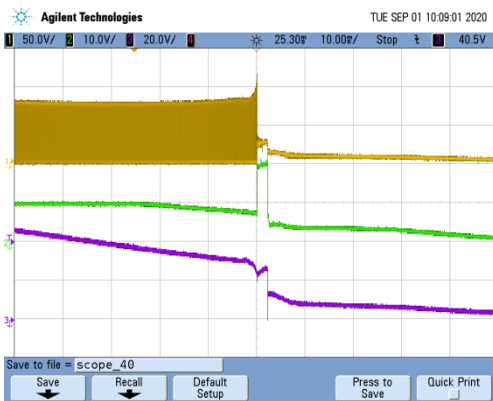
50[ms/div]



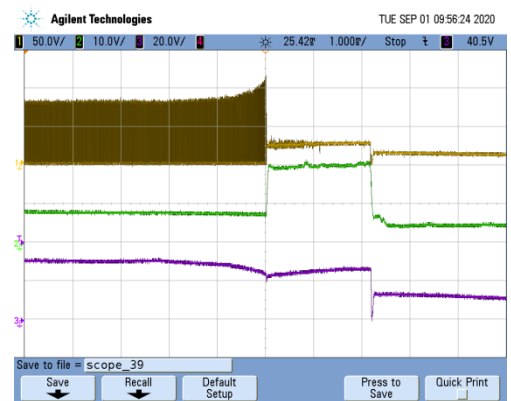
20[ms/div]



10[ms/div]



1[ms/div]



0.2[ms/div]

