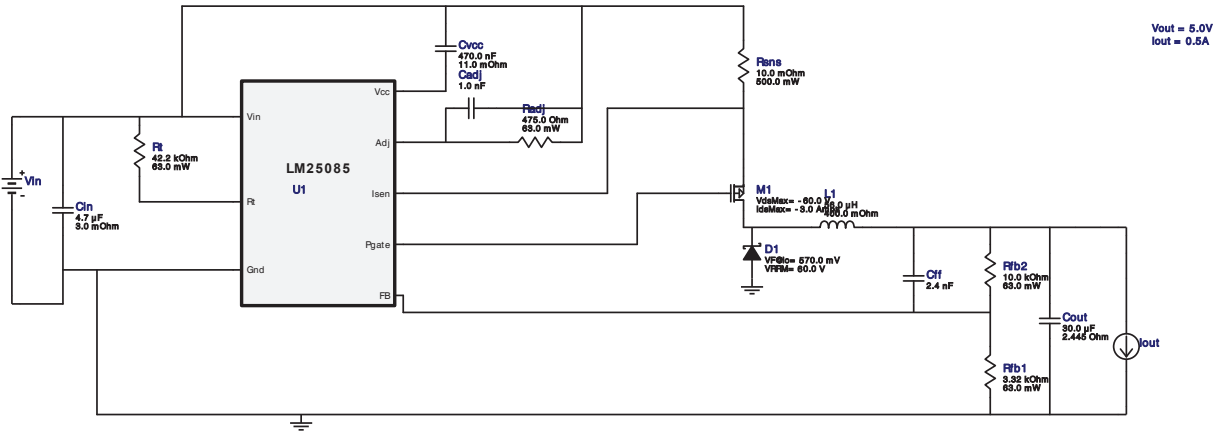






WEBENCH® Design Report

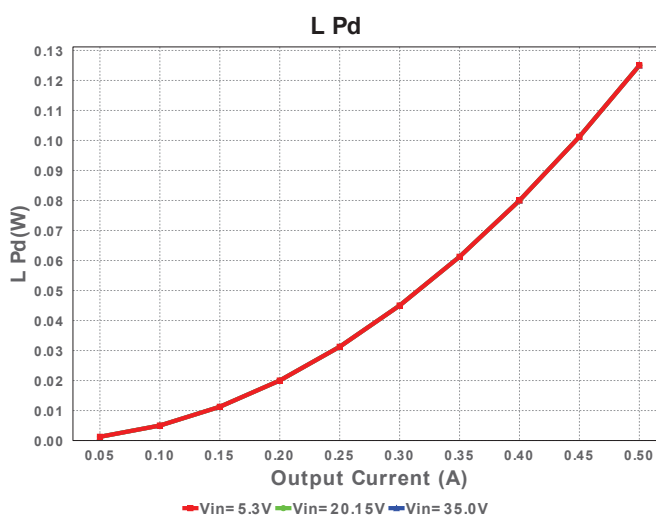
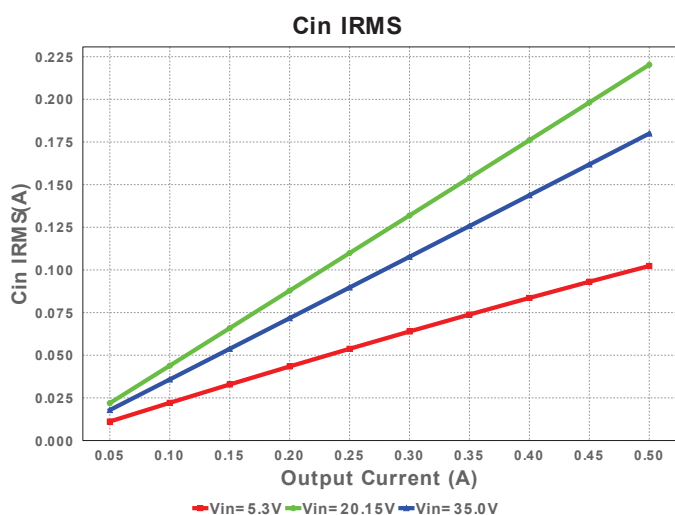
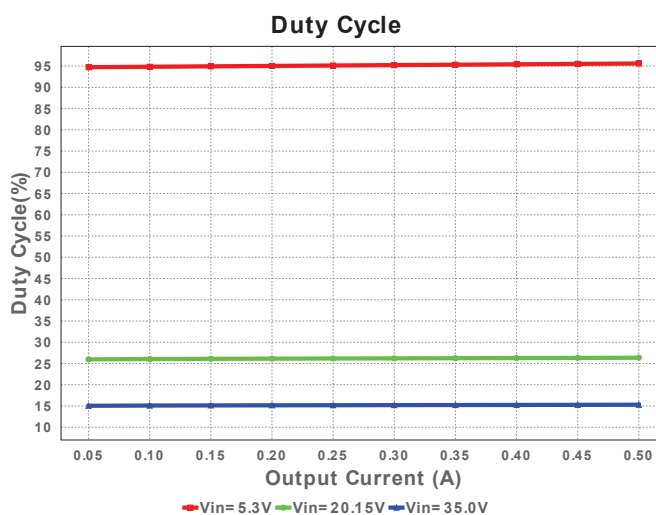
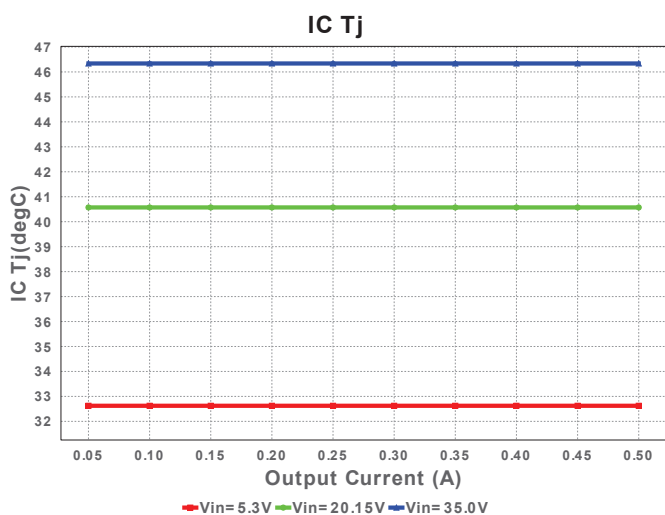
 Design : 3921961/36 LM25085MY/NOPB
 LM25085MY/NOPB 5.3V-35.0V to 5.00V @ 0.5A

My Comments

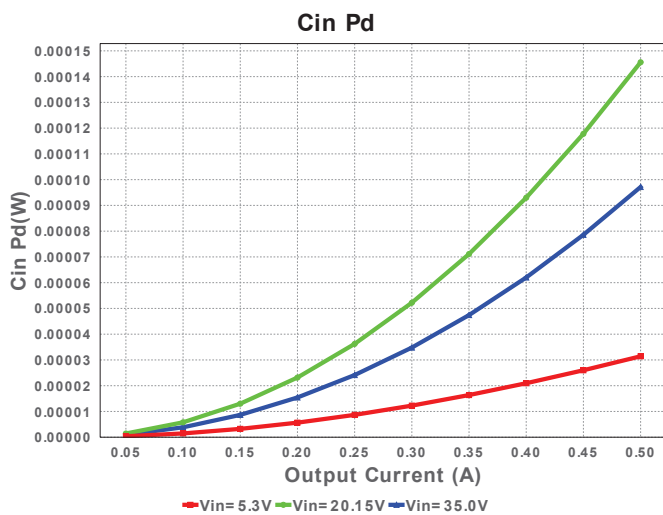
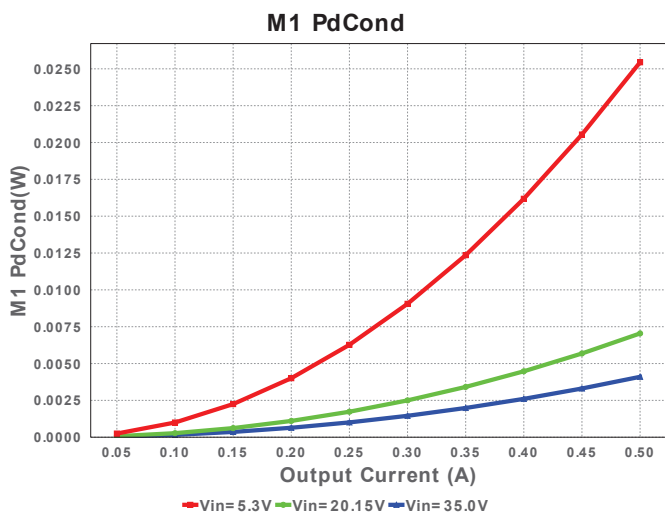
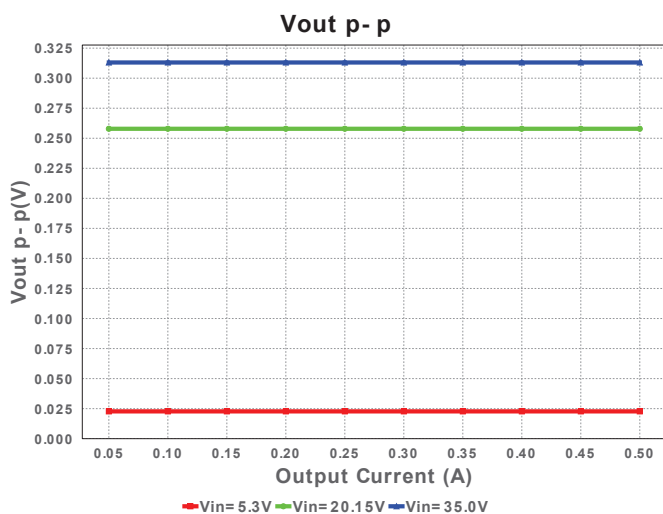
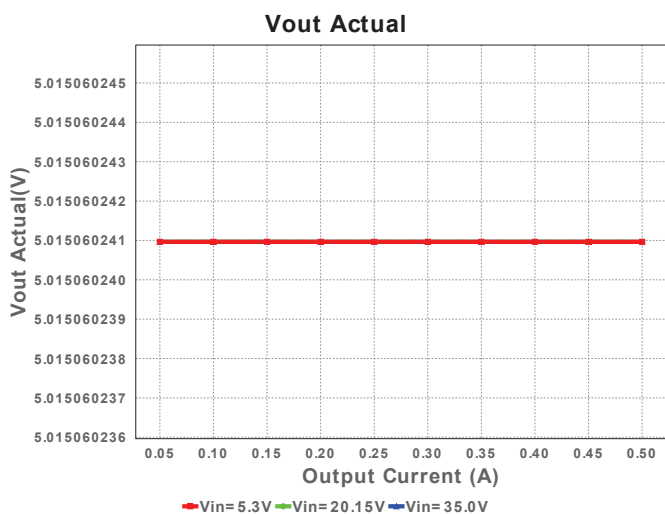
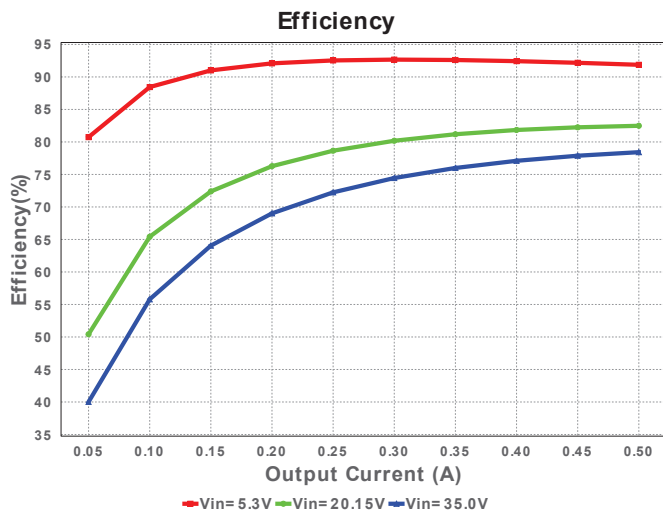
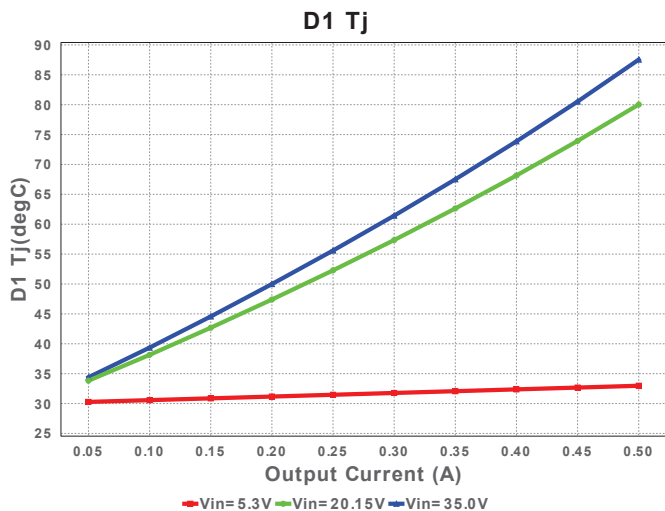
No comments

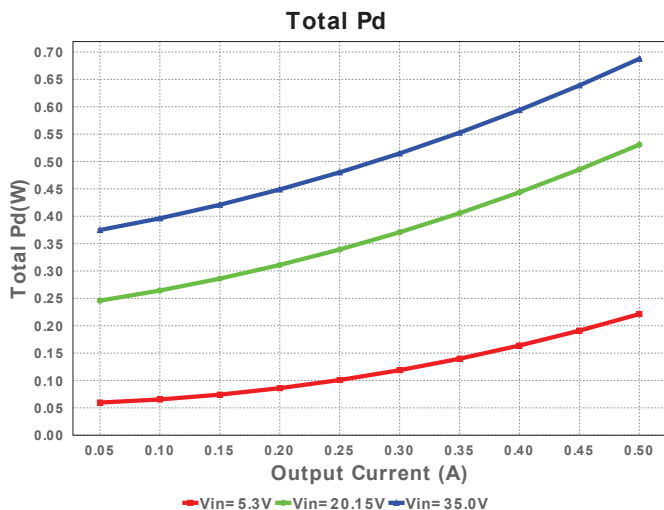
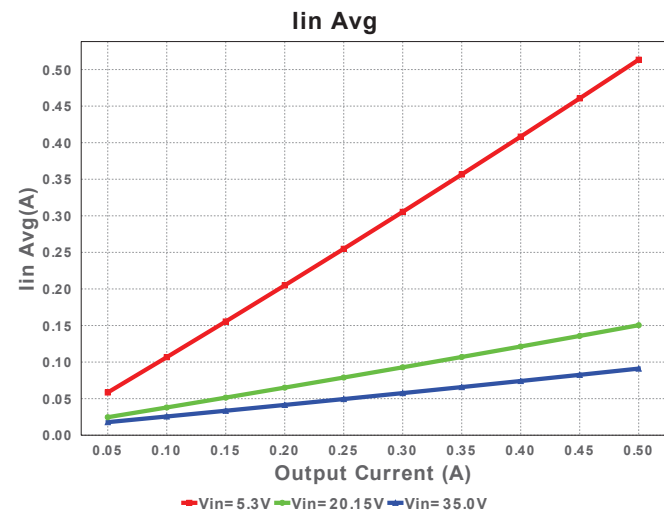
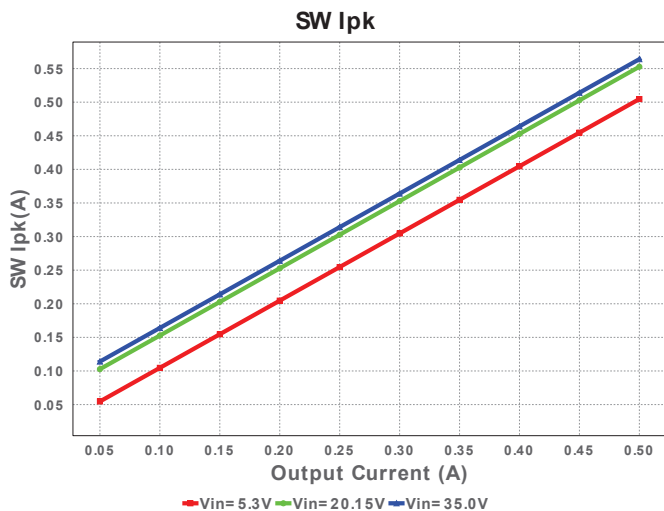
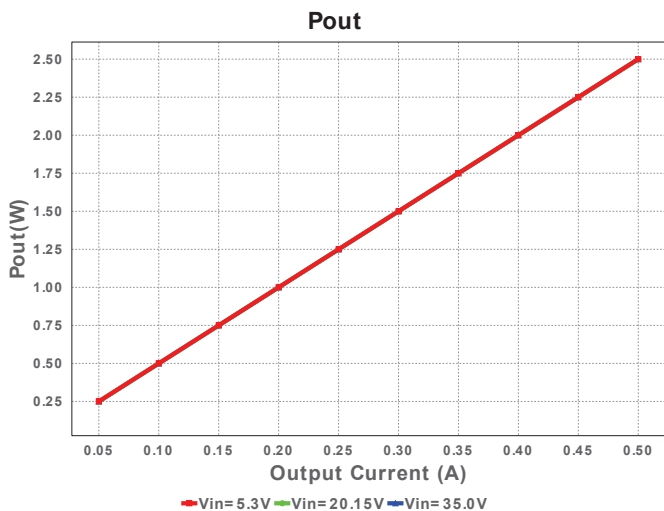
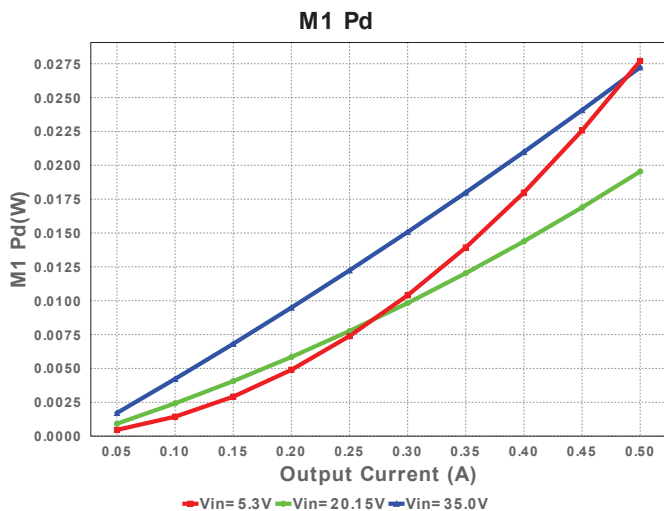
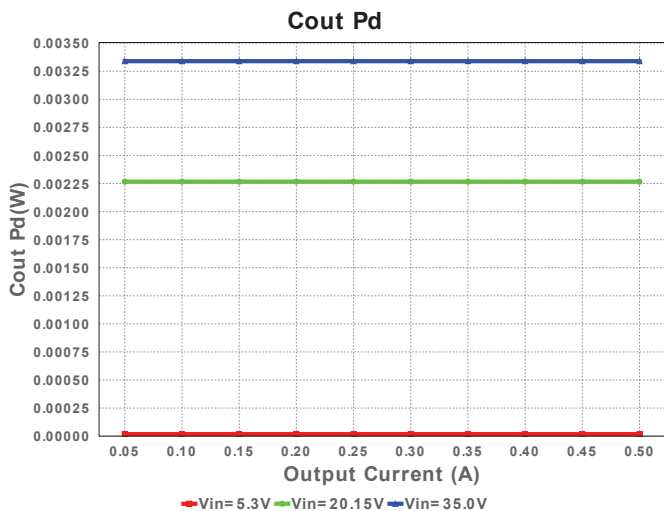
Electrical BOM

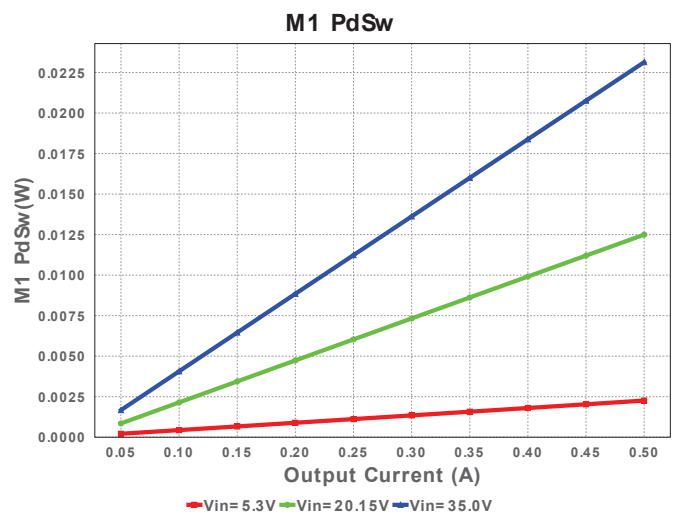
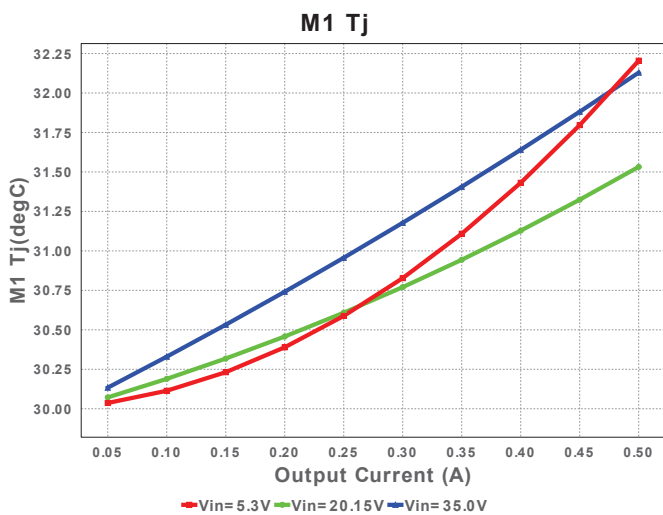
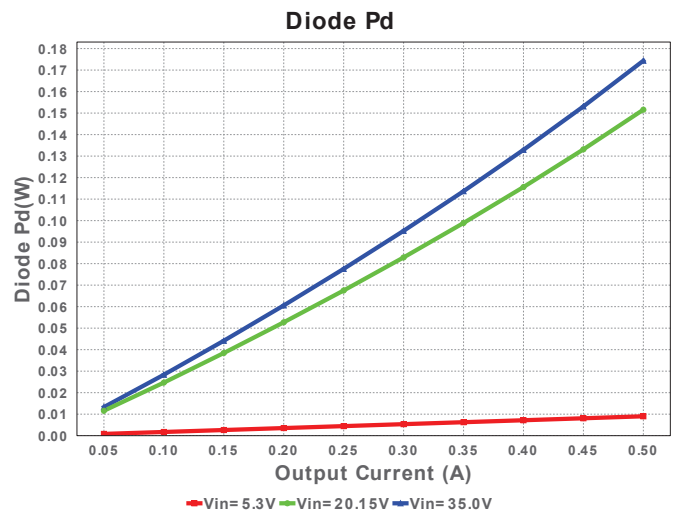
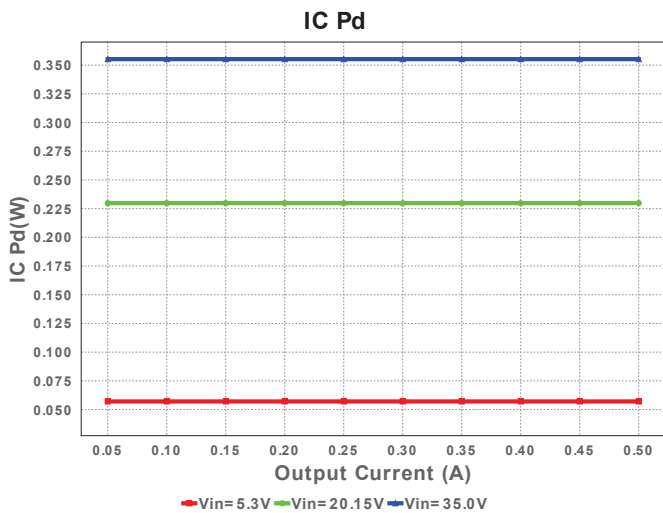
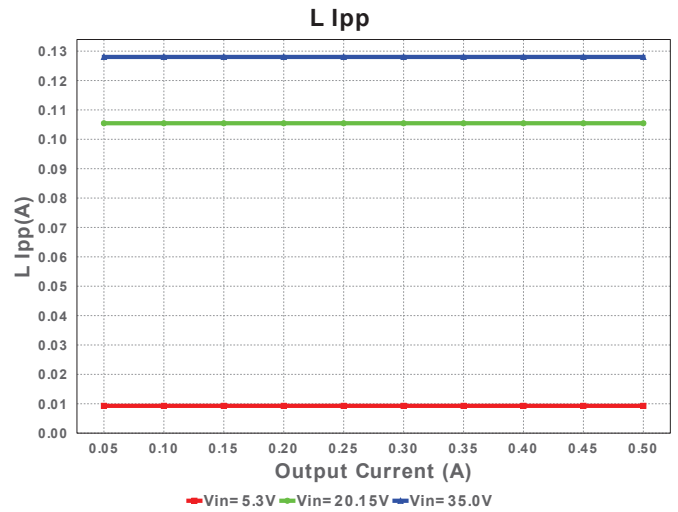
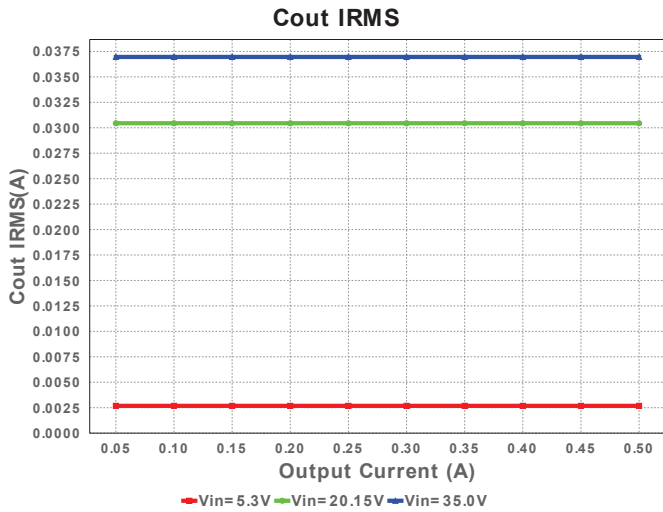
| # | Name | Manufacturer | Part Number | Properties | Qty | Price | Footprint |
|-----|------|---------------------------|---------------------------------------|--|-----|--------|-------------------------------|
| 1. | Cadj | Samsung Electro-Mechanics | CL21C102JBCNFNC Series= C0G/NP0 | Cap= 1.0 nF VDC= 50.0 V IRMS= 0.0 A | 1 | \$0.01 | 0805 7 mm ² |
| 2. | Cff | MuRata | GRM1885C1H242JA01D Series= C0G/NP0 | Cap= 2.4 nF VDC= 50.0 V IRMS= 0.0 A | 1 | \$0.01 | 0603 5 mm ² |
| 3. | Cin | MuRata | GRM31CR71H475KA12L Series= X7R | Cap= 4.7 uF ESR= 3.0 mOhm VDC= 50.0 V IRMS= 4.98 A | 1 | \$0.07 | 1206 11 mm ² |
| 4. | Cout | CUSTOM | CUSTOM Series= ? | Cap= 30.0 uF ESR= 2.44538 Ohm VDC= 6.25 V IRMS= 47.807 mA | 1 | NA | CUSTOM 0 mm ² |
| 5. | Cvcc | AVX | 0805YC474KAT2A Series= X7R | Cap= 470.0 nF ESR= 11.0 mOhm VDC= 16.0 V IRMS= 0.0 A | 1 | \$0.02 | 0805 7 mm ² |
| 6. | D1 | NXP Semiconductor | PMEG6010CEH,115 | VF@Io= 570.0 mV VRRM= 60.0 V | 1 | \$0.04 | SOD-123F 12 mm ² |
| 7. | L1 | NIC Components | NPI54C560KTRF | L= 56.0 uH DCR= 400.0 mOhm | 1 | \$0.09 | IND_NPI54C 61 mm ² |
| 8. | M1 | Fairchild Semiconductor | FDC5614P | VdsMax= -60.0 V IdsMax= -3.0 Amps | 1 | \$0.23 | SOT-23-6 15 mm ² |
| 9. | Radj | Vishay-Dale | CRCW0402475RFKED Series= CRCW..e3 | Res= 475.0 Ohm Power= 63.0 mW Tolerance= 1.0% | 1 | \$0.01 | 0402 3 mm ² |
| 10. | Rfb1 | Vishay-Dale | CRCW04023K32FKED Series= CRCW..e3 | Res= 3.32 kOhm Power= 63.0 mW Tolerance= 1.0% | 1 | \$0.01 | 0402 3 mm ² |

| # | Name | Manufacturer | Part Number | Properties | Qty | Price | Footprint |
|-----|------|---------------------------|--------------------------------------|--|-----|--------|---|
| 11. | Rfb2 | Vishay-Dale | CRCW040210K0FKED Series= CRCW..e3 | Res= 10.0 kOhm Power= 63.0 mW Tolerance= 1.0% | 1 | \$0.01 |  0402 3 mm² |
| 12. | Rsns | Stackpole Electronics Inc | CSR1206FK10L0 Series= ? | Res= 10.0 mOhm Power= 500.0 mW Tolerance= 1.0% | 1 | \$0.11 |  1206 11 mm² |
| 13. | Rt | Vishay-Dale | CRCW040242K2FKED Series= CRCW..e3 | Res= 42.2 kOhm Power= 63.0 mW Tolerance= 1.0% | 1 | \$0.01 |  0402 3 mm² |
| 14. | U1 | Texas Instruments | LM25085MY/NOPB | Switcher | 1 | \$0.70 |  MUY08A 24 mm² |









Operating Values

| # | Name | Value | Category | Description |
|-----|--------------|-----------------------|----------|---|
| 1. | Cin IRMS | 179.951 mA | Current | Input capacitor RMS ripple current |
| 2. | Cout IRMS | 36.958 mA | Current | Output capacitor RMS ripple current |
| 3. | Iin Avg | 91.078 mA | Current | Average input current |
| 4. | L Ipp | 128.02 mA | Current | Peak-to-peak inductor ripple current |
| 5. | SW Ipk | 564.012 mA | Current | Peak switch current |
| 6. | BOM Count | 14 | General | Total Design BOM count |
| 7. | FootPrint | 203.0 mm ² | General | Total Foot Print Area of BOM components |
| 8. | Frequency | 494.35 kHz | General | Switching frequency |
| 9. | IC Tolerance | 25.0 mV | General | IC Feedback Tolerance |
| 10. | Mode | CCM | General | Conduction Mode |
| 11. | Pout | 2.5 W | General | Total output power |

| # | Name | Value | Category | Description |
|-----|----------------|-------------|----------|--|
| 12. | Total BOM | \$0.0 | General | Total BOM Cost |
| 13. | D1 Tj | 87.539 degC | Op_Point | D1 junction temperature |
| 14. | Vout Actual | 5.015 V | Op_Point | Vout Actual calculated based on selected voltage divider resistors |
| 15. | Vout OP | 5.0 V | Op_Point | Operational Output Voltage |
| 16. | Duty Cycle | 15.291 % | Op_point | Duty cycle |
| 17. | Efficiency | 78.426 % | Op_point | Steady state efficiency |
| 18. | IC Tj | 46.339 degC | Op_point | IC junction temperature |
| 19. | ICThetaJA | 46.0 degC/W | Op_point | IC junction-to-ambient thermal resistance |
| 20. | IOUT_OP | 500.0 mA | Op_point | Iout operating point |
| 21. | M1 Tj | 32.129 degC | Op_point | M1 MOSFET junction temperature |
| 22. | VIN_OP | 35.0 V | Op_point | Vin operating point |
| 23. | Vout p-p | 313.072 mV | Op_point | Peak-to-peak output ripple voltage |
| 24. | Cin Pd | 97.147 μW | Power | Input capacitor power dissipation |
| 25. | Cout Pd | 3.34 mW | Power | Output capacitor power dissipation |
| 26. | Diode Pd | 174.359 mW | Power | Diode power dissipation |
| 27. | IC Pd | 355.191 mW | Power | IC power dissipation |
| 28. | L Pd | 125.0 mW | Power | Inductor power dissipation |
| 29. | M1 Pd | 27.236 mW | Power | M1 MOSFET total power dissipation |
| 30. | M1 PdCond | 4.097 mW | Power | M1 MOSFET conduction losses |
| 31. | M1 PdSw | 23.139 mW | Power | M1 MOSFET switching losses |
| 32. | Total Pd | 687.721 mW | Power | Total Power Dissipation |
| 33. | Vout Tolerance | 3.547 % | | Vout Tolerance based on IC Tolerance (no load) and voltage divider resistors if applicable |

Design Inputs

| # | Name | Value | Description |
|----|---------|---------|------------------------|
| 1. | Iout | 500.0 m | Maximum Output Current |
| 2. | VinMax | 35.0 | Maximum input voltage |
| 3. | VinMin | 5.3 | Minimum input voltage |
| 4. | Vout | 5.0 | Output Voltage |
| 5. | base_pn | LM25085 | Base Product Number |
| 6. | source | DC | Input Source Type |
| 7. | Ta | 30.0 | Ambient temperature |

Design Assistance

1. For a Constant On Time device to be stable, we need to provide a ripple at the feedback comparator. There are various methods to implement the ripple. Depending on the circuit complexity vs. the allowable ripple, we have three options to choose from. The simplest option, 'Low Complexity', would require only a high ESR cap at the output. This means that the BOM count will be small, but the output voltage ripple will be quite large. The 'optimal solution' would require a feed-forward cap in parallel with the upper feedback resistor to AC couple the ripple to the feedback node. This increases the BOM count slightly, but now we have more control over the output voltage ripple. If the output voltage requirement is very tight, then the best option is to go for the 'Low Output Ripple' solution. In this option we can go with very low ESR output caps and have very good control over the output voltage ripple

2. **LM25085** Product Folder : <http://www.ti.com/product/LM25085> : contains the data sheet and other resources.

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You should completely validate and test your design implementation to confirm the system functionality for your application prior to production.

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