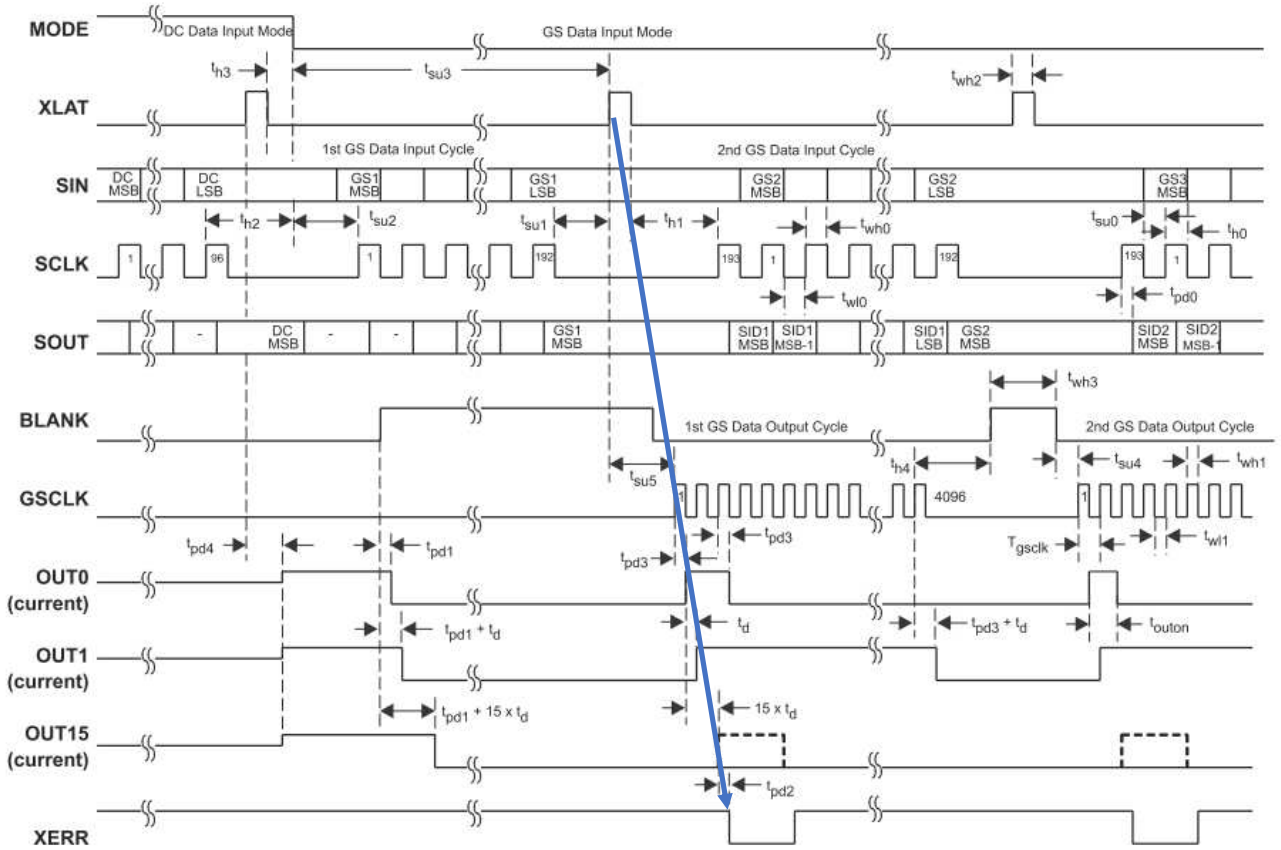

Figure 12. Serial Data Input Timing Chart