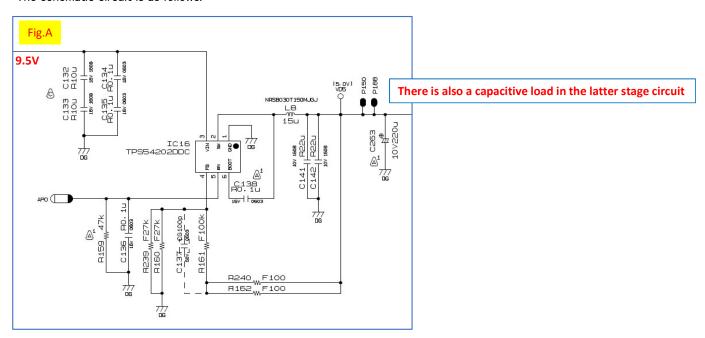
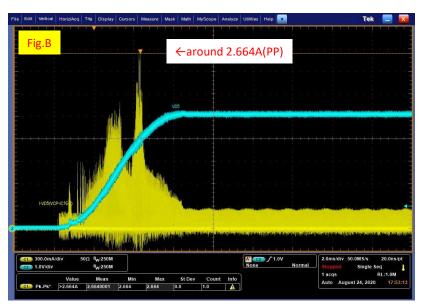
Please tell us about OCP operation of TPS54202DDCR.

I am using your power supply IC. The schematic circuit is as follows.



The waveform of the inrush current (pulse current) into the "IC-3pin" when this circuit is configured is as follows.



<Question>

What is the allowable inrush current value of this IC? Is it up to a current limit of 4A? The output VD5 is up without any problems.

Is it OK to consider that if the output voltage of 5V has risen without any problems, then there is no problem with inrush current?

CURRENT LIMIT						
(LIM_HS)	High side current limit		2.5	3.2	3.9	Α
I(LIM LS)	Low side source current limit		2	3	4	Α