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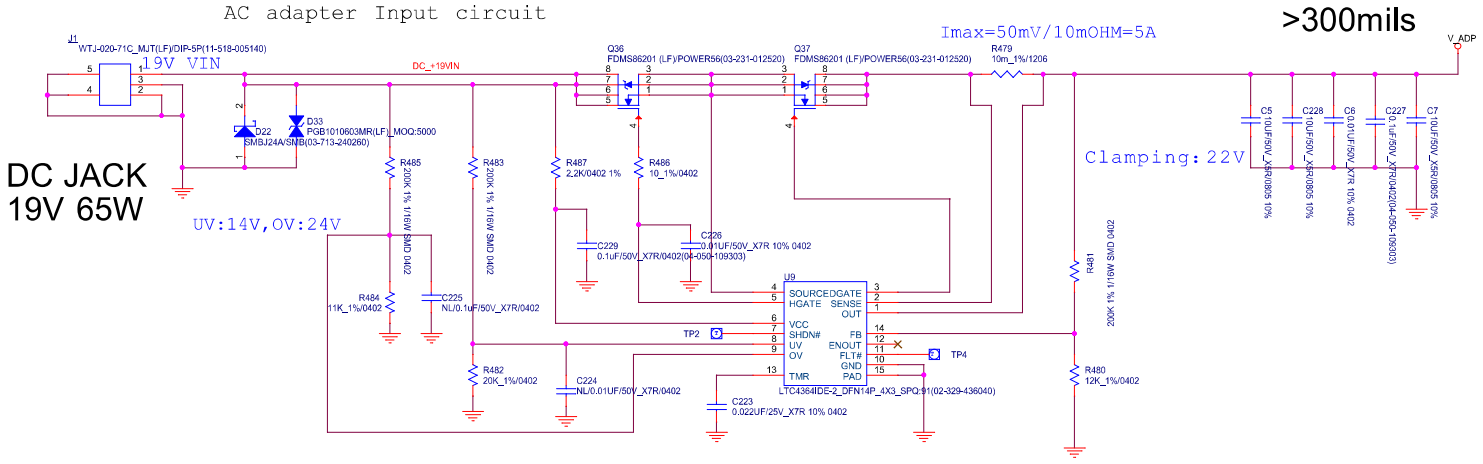
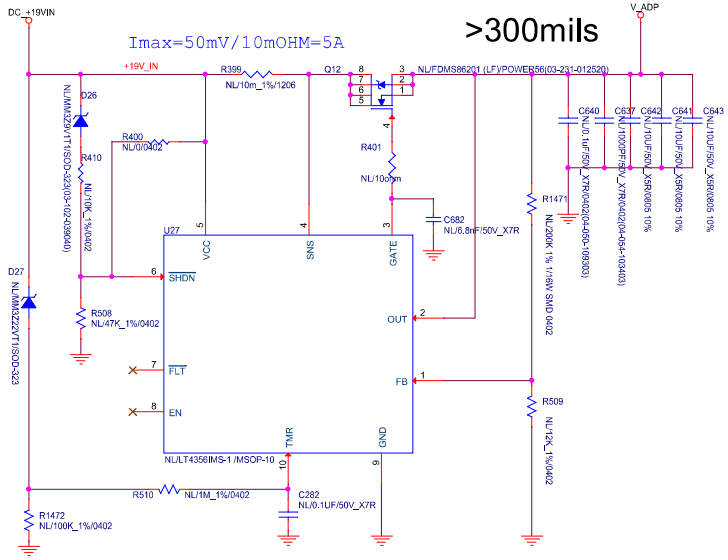
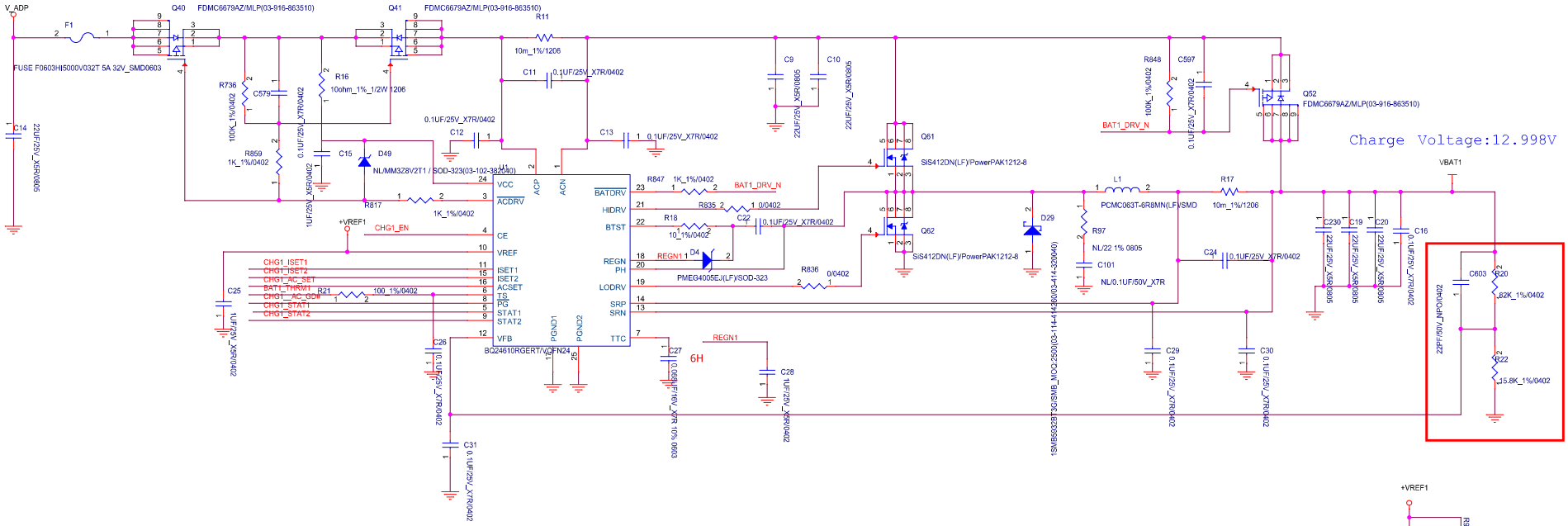
3	Charger 01
4	Hole

-040-275102) to

PMEG4005EJ(LF)/SOD-323 (03-402-005330)


AC adapter Input circuit
DC JACK
19V 65W
19V VIN
UV:14V,OV:24V
Imax=50mV/10mOHM=5A
>300mils
Clamping: 22V
DC_+19VIN
V_ADP
J1
WTJ-020-71C_MJT(LF)/DIP-5P(11-518-005140)
Q36
FDMS86201 (LF)/POWER56(03-231-012520)
Q37
FDMS86201 (LF)/POWER56(03-231-012520)
R479
10m_1%/1206
D22
SMBJ24A/SMB(03-713-240260)
D33
PGB1010603MR(LF)_MOQ:5000
R485
200K 1% 1/16W SMD 0402
R483
200K 1% 1/16W SMD 0402
R487
2.2K/0402 1%
R486
10_1%/0402
C229
0.1uF/50V_X7R/0402(04-050-109303)
C226
0.01UF/50V_X7R 10% 0402
R484
11K_1%/0402
C225
NL/0.1uF/50V_X7R/0402
R482
20K_1%/0402
C224
NL/0.01UF/50V_X7R/0402
C223
0.022UF/25V_X7R 10% 0402
U9
LTC4364IDE-2_DFN14P_4X3_SPQ:91(02-329-436040)
SOURCE
GATE
HGATE
SENSE
OUT
VCC
SHDN#
UV
OV
TMR
FB
ENOUT
FLT#
GND
PAD
TP2
TP4
R481
200K 1% 1/16W SMD 0402
R480
12K_1%/0402
C5
C228
C6
C227
C7
10UF/50V_X5R/0805 10%
0.01UF/50V_X7R 10% 0402
0.1uF/50V_X7R/0402(04-050-109303)


DC_+19VIN
Imax=50mV/10mOHM=5A
>300mils
V_ADP
+19V_IN
R399
NL/10m_1%/1206
Q12
NL/FDMS86201 (LF)/POWER56(03-231-012520)
D26
NL/MM3Z9VT1SOD-323(03-102-039040)
R400
NL/0/0402
R410
NL/10K_1%/0402
R401
NL/100m
C682
NL/6.8nF/50V_X7R
U27
VCC
SNS
GATE
SHDN
OUT
FB
FLT
EN
TMR
GND
NL/LT4356IMS-1 /MSOP-10
D27
NL/MM3Z22VT1SOD-323
R508
NL/47K_1%/0402
R510
NL/1M_1%/0402
C282
NL/0.1UF/50V_X7R
R1472
NL/100K_1%/0402
R1471
NL/200K 1% 1/16W SMD 0402
R509
NL/12K_1%/0402
C640
C637
C642
C641
C643
NL/0.1uF/50V_X7R/0402(04-050-109303)
NL/10UF/50V_X5R/0805 10%
NL/100PF/50V_X7R/0402(04-054-101303)



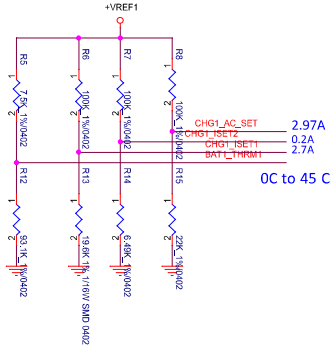
Charge Voltage:2.1*(1+82/15.8)=12.998V

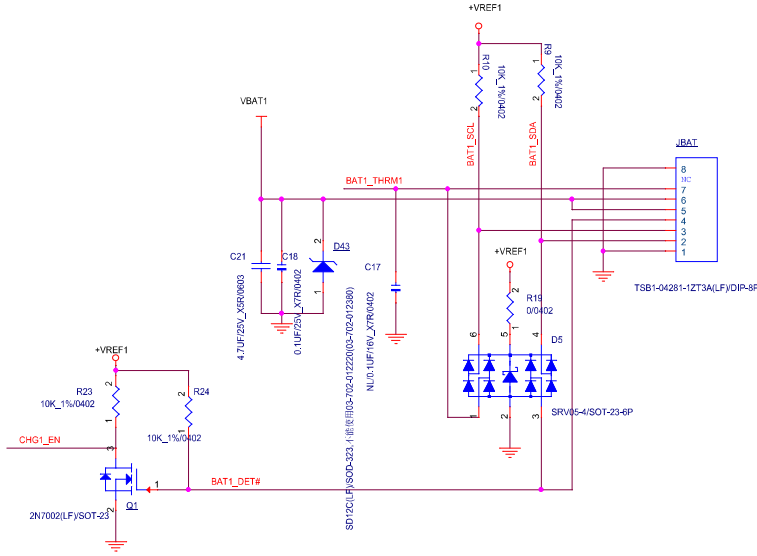
Charge Current:3.3*19.6/(100+19.6)/20/0.01=2.70A

Precharge Current:3.3*6.49/(100+6.49)/100/0.01=0.2A

AC Current Limit:3.3*22/(100+22)/20/0.01=2.97A

Charge Over Temperature Protect: 50℃
Vtco=3.3*34.4%=1.14V
1.14V=3.3*(93.1//RT)/(7.5+93.1//RT)
==>RT=4.13K ---->50℃
Thermistor 50℃ impedance(4.082K~4.239K)





9.3.22 Charge Status Outputs

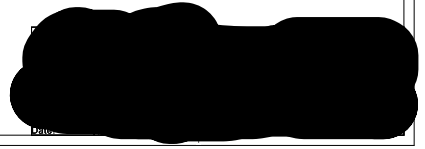
The open-drain STAT1 and STAT2 outputs indicate various charger operations as shown in the Table 2. These status pins can be used to drive LEDs or communicate with the host processor. OFF indicates that the open-drain transistor is turned off.

Table 2. STAT Pin Definition for bq2461x

CHARGE STATE	STAT1	STAT2
Charge in progress	ON	OFF
Charge complete	OFF	ON
Charge suspend, timer fault, overvoltage, sleep mode, battery absent	OFF	OFF

BATTERY CHARGE LED

Battery	LED
charge	Yellow
charge full	Green



PCB1
SC603 Single Charger_R1.2

UUID LABLE

UUID LABEL_35*5mm

PCBA LABLE

PCBA LABEL_35*5mm

SMT_NUT1
NL/NUT M2,O,D3.5,H=1.7

pad6_hole2d8

SMT_NUT2
NL/NUT M2,O,D3.5,H=1.7

pad6_hole2d8

SMT_NUT3
NL/NUT M2,O,D3.5,H=1.7

pad6_hole2d8

SMT_NUT4
NL/NUT M2,O,D3.5,H=1.7

pad6_hole2d8

SMT_NUT5
NL/NUT M2,O,D3.5,H=1.7

pad6_hole2d8

SMT_NUT6
NL/NUT M2,O,D3.5,H=1.7

pad3d2hole3d2

SMT_NUT7
NL/NUT M2,O,D3.5,H=1.7

pad3d2hole3d2

SMT_NUT8
NL/NUT M2,O,D3.5,H=1.7

pad3d2hole3d2

SMT_NUT9
NL/NUT M2,O,D3.5,H=1.7

pad3d2hole3d2